STPS5045S



Power Schottky rectifier

Datasheet - production data

Features

- Low forward voltage drop
- Very small conduction losses
- Negligible switching losses
- Extremely fast switching
- Low thermal resistance
- 200 °C maximum junction temperature
- Avalanche rated

Description

This device is a dual center tap Schottky rectifier suited for switch mode power supply and high frequency DC to DC converters.

Packaged in D²PAK, this device is especially intended for use in low voltage, high frequency inverters, freewheeling and polarity protection applications. Also ideal for PV cell-bypass diode for junction and smart junction boxes.

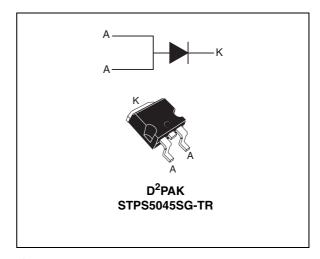


Table 1. Device summary

Symbol	Value
I _{F(AV)}	50 A
V_{RRM}	45 V
T _j (max)	200 °C
V _F (max)	0.48 V

STPS5045S **Characteristics**

Characteristics 1

Table 2. Absolute ratings (limiting values at 25 °C unless otherwise specified)

Symbol	Parameter			Value	Unit
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V_{RRM}	Repetitive peak reverse voltage			45	٧
I _{F(RMS)}	Forward rms current		90	Α	
I _{F(AV)}	Average forward current $\delta = 0.5$ $T_c = 135$ °C		50	Α	
I _{FSM}	Surge non repetitive forward current	t _p = 10 ms sinusoidal		600	Α
P_{ARM}	Repetitive peak avalanche power	t _p = 10 μs T _j = 125 °C		1200	W
T _{stg}	Storage temperature range			-65 to +175	°C
T _i ⁽¹⁾	Maximum operating junction temperature in DC forward mode ⁽²⁾			+200	°C
'j` ′	Maximum operating junction temperature			+175	°C

 $[\]frac{dPtot}{dTj} < \frac{1}{Rth(j-a)}$ condition to avoid thermal runaway for a diode on its own heatsink

Table 3. Thermal resistance

Symbol	Parameter	Value	Unit
R _{th(j-c)}	Junction to case	1.0	°C/W

Table 4. Static electrical characteristics

Symbol	Parameter	Test conditions		Min.	Тур.	Max.	Unit
I _R ⁽¹⁾	Reverse leakage current	T _j = 25 °C	$V_R = V_{RRM}$		0.090	0.36	
		T _j = 75 °C	V _R = 20 V		0.7	1.9	mA
		T _j = 125 °C	$V_R = V_{RRM}$		65	185	
V _F ⁽²⁾	Forward voltage drop	T _j = 25 °C	I 50 A		0.55	0.61	
		T _j = 125 °C	I _F = 50 A		0.48	0.56	V
		T _j = 200 °C	I _F = 10 A		0.22] '
			I _F =20 A		0.28		

^{1.} Pulse test: $t_p = 5$ ms, $\delta < 2\%$

To evaluate the conduction losses use the following equation: P = 0.38 x $I_{F(AV)}$ + 0.0036 I_{F}^{2} _(RMS)

$$P = 0.38 \times I_{F(AV)} + 0.0036 I_{F^2(RMS)}$$

^{2.} Maximum operating junction temperature only in DC forward mode

^{2.} Pulse test: $t_p = 380 \mu s$, $\delta < 2\%$

STPS5045S Characteristics

Figure 1. Average forward power dissipation Figure 2. Average forward current versus ambient temperature $(\delta=0.5)$

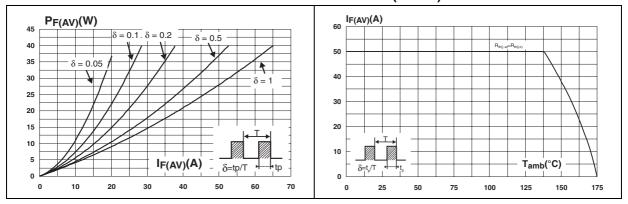
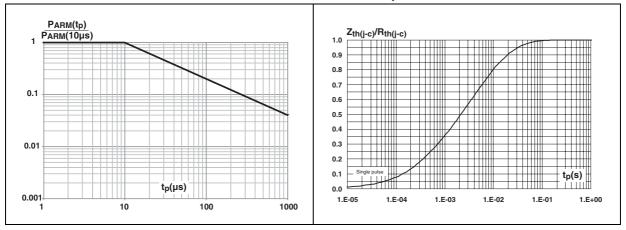


Figure 3. Normalized avalanche power derating versus pulse duration

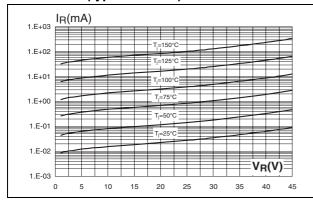
Figure 4. Relative variation of thermal impedance junction to case versus pulse duration



Characteristics STPS5045S

Figure 5. Reverse leakage current versus reverse voltage applied (typical values)

Figure 6. Junction capacitance versus reverse voltage applied (typical values)



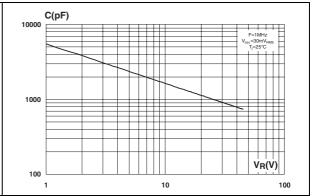
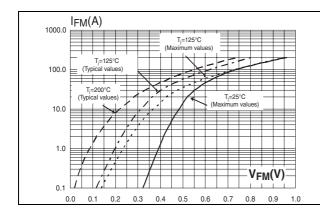
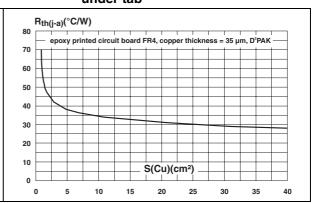


Figure 7. Forward voltage drop versus forward current

Figure 8. Thermal resistance junction to ambient versus copper surface under tab





STPS5045S Package information

2 Package information

- Epoxy meets UL94, V0
- Cooling method: by conduction (C)

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Table 5. D²PAK dimensions

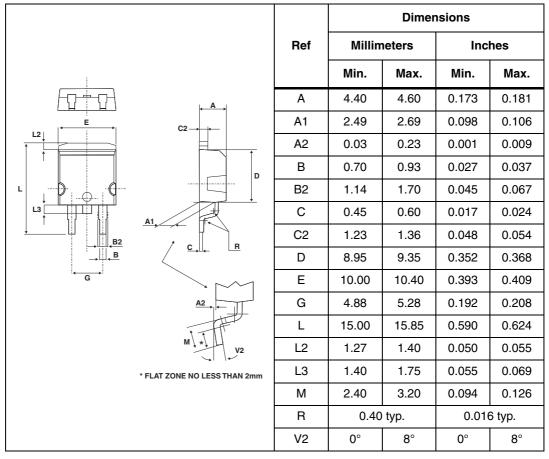
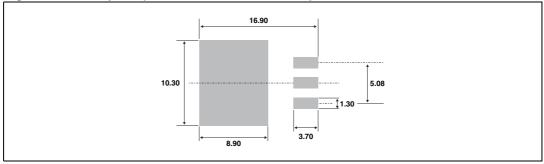


Figure 9. Footprint (dimensions in millimeters)



Ordering information STPS5045S

3 Ordering information

Table 6. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
STPS5045SG-TR	STPS5045SG	D ² PAK	1.48 g	1000	Tape and reel

4 Revision history

 Table 7.
 Revision history

Date	Revision	Changes
28-June-2012	1	First issue.

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