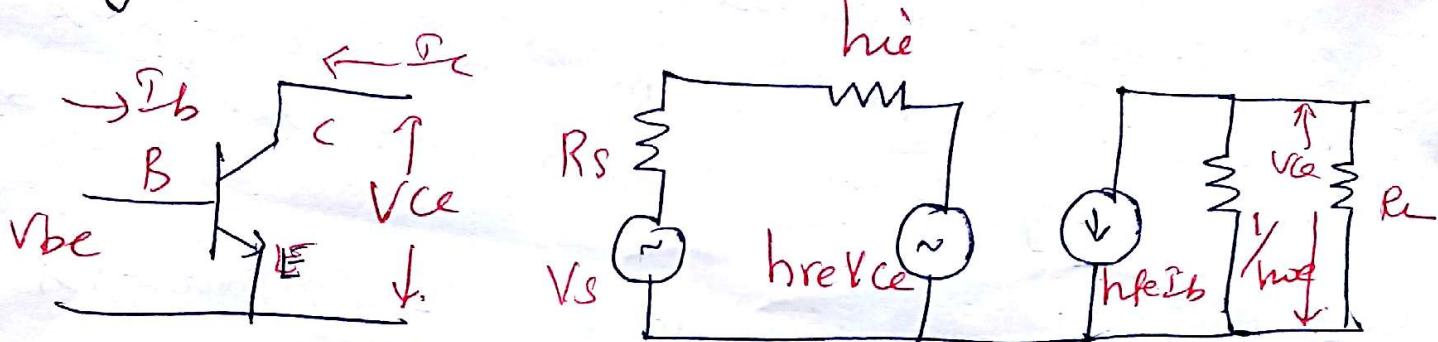


UNIT-II

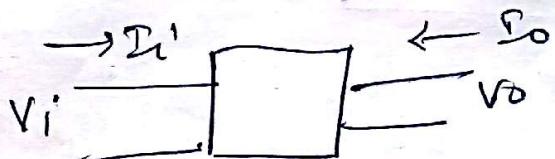
Small signal equivalent of CE amplifier
(on Hybrid model)



Hybrid parameter (or) h-parameter

- * The dimensions of the hybrid parameters are not alike, they are hybrid in nature so they are called hybrid parameters.

- * Transistor is a two port network



$$V_i = h_{11} I_i + h_{12} V_o$$

$$I_o = h_{21} I_i + h_{22} V_o$$

$$\Rightarrow h_{12} = \left[\frac{V_i}{I_i} \right]_{V_o=0} = h_{ie} \rightarrow \text{Input impedance}$$

$$h_{12} = \left[\frac{V_i}{V_o} \right]_{I_i=0} = h_{re} \rightarrow \text{Reverse voltage gain}$$

$$h_{21} = \left[\frac{I_o}{I_i} \right]_{V_o=0} = h_{fe} \rightarrow \text{Forward current gain}$$

$$h_{22} = \left[\frac{I_o}{V_o} \right]_{I_i=0} = h_{oe} \rightarrow \text{Output admittance}$$

i r
f a

Salient features of h-parameters are

1. h-parameters are real numbers
2. Easy to measure
3. Convenient to use in circuit analysis & design
4. easily convertible from Configuration to other

Small signal analysis of CE Configuration

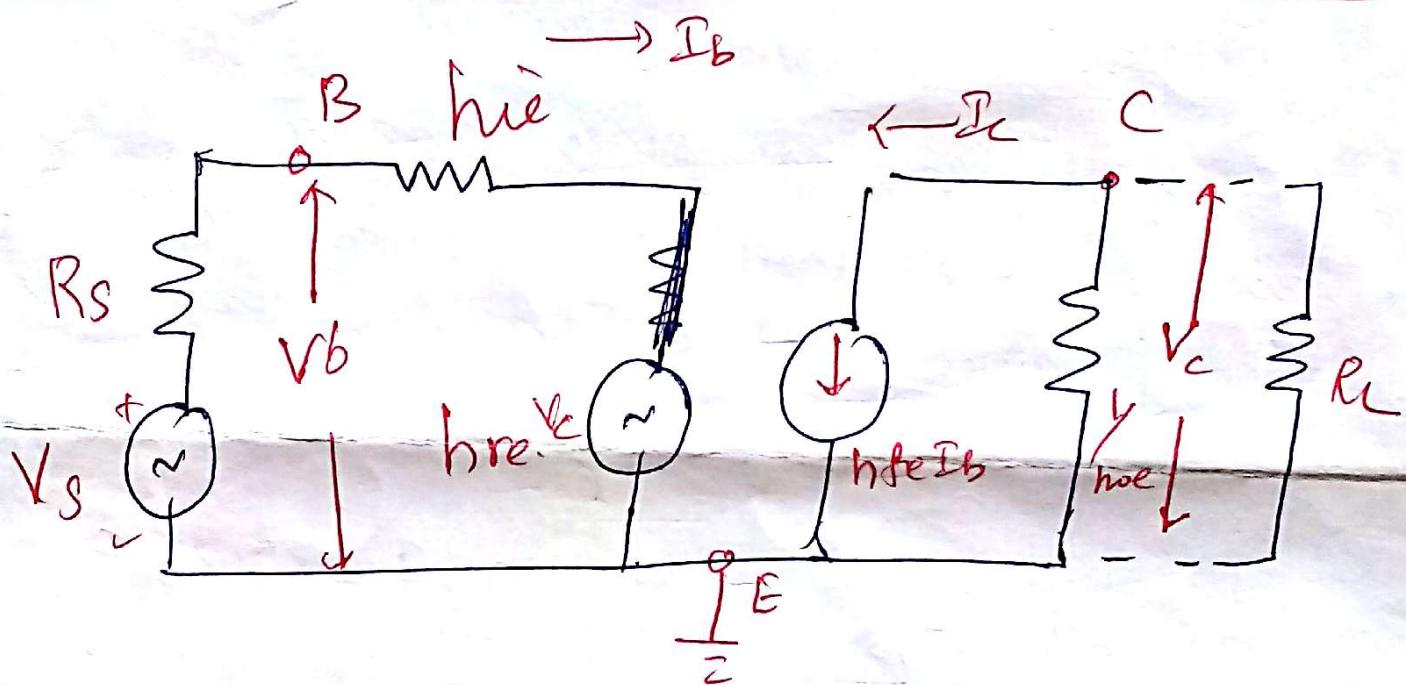
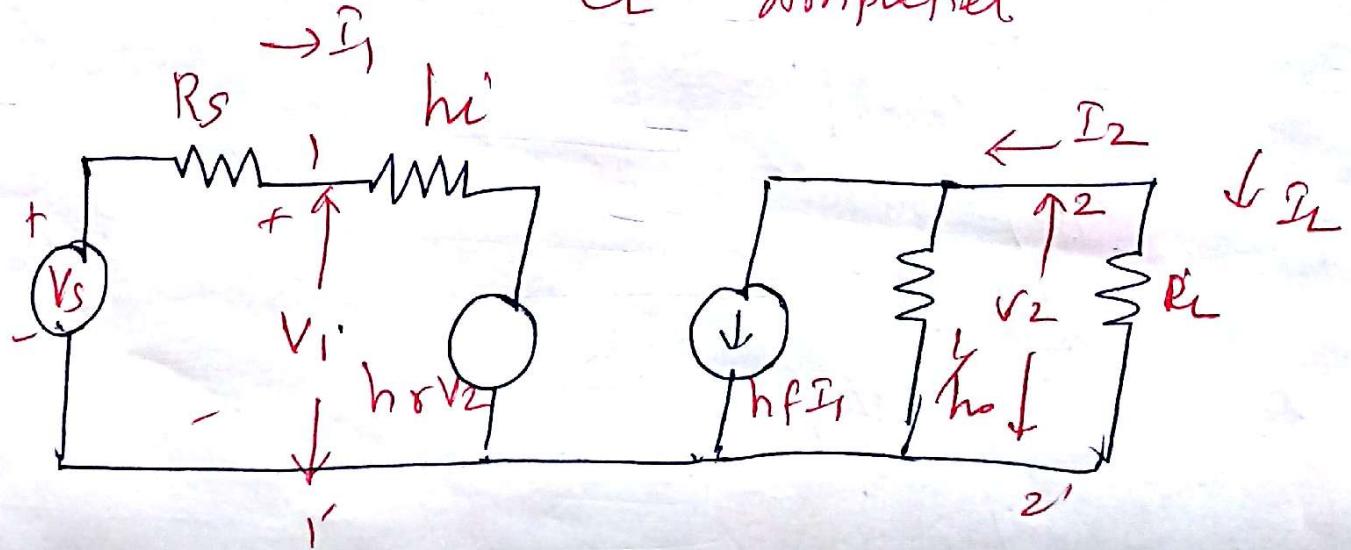


Fig (i) - h-parameter equivalent circuit for
CE Amplifier



Current Gain (A_i)

$$A_i = \frac{I_L}{I_1} = -\frac{I_2}{I_1} \quad [\because I_L = -I_2]$$

$$I_2 = h_f I_1 + h_o V_2$$

$$\text{Sub } V_2 = -I_2 R_L$$

$$I_2 = h_f I_1 + h_o I_2 R_L$$

$$I_2 + h_o I_2 R_L = h_f I_1$$

$$I_2 (1 + h_o R_L) = h_f I_1$$

$$\frac{I_2}{I_1} = \frac{h_f}{1 + h_o R_L}$$

$$A_i^2 = \frac{-I_2}{I_1} = \frac{-h_f}{1 + h_o R_L}$$

$$A_i = \frac{-h_f}{1 + h_o R_L}$$

For CE Amp

$$A_i = \frac{-h_{fe}}{1 + h_{oe} R_L}$$

Input Impedance (Z_i)

$$V_I = h_{11} I_1 + h_{12} V_2$$

$$V_I = h_i I_1 + h_{rL} V_2$$

$$Z_i = \frac{V_I}{I_1} = h_i + \frac{h_{rL} V_2}{I_1}$$

$$\text{Sub } V_2 = -\frac{R_L}{A_i} I_1 R_L = A_i I_1 R_L$$

$$Z_i = h_i + \frac{h_{rL} A_i I_1 R_L}{I_1}$$

$$Z_i = h_i + h_{rL} A_i R_L$$

$$\text{Sub } A_i = \frac{-h_f}{1 + h_o R_L}$$

$$Z_i = h_i - \frac{h_{rL} \cdot h_f \cdot R_L}{1 + h_o R_L}$$

\therefore Nr \propto Dr by R_L

$$Z_i = h_i - \frac{h_{rL} h_f}{h_o + \frac{1}{R_L}}$$

$$Z_i = h_i - \frac{h_{rL} h_f}{h_o + Y_L} \quad \text{where } Y_L = \frac{1}{R_L}$$

Voltmeter
✓

Voltage Gain (Av)

$$A_{v e} = \frac{V_2}{V_1}$$

$$\text{Using } V_2 = -I_2 R_L \approx A_i I_1 R_L$$

$$A_{v e} = \frac{A_i \cancel{\frac{I_2 R_L}{V_1}}}{\cancel{V_1}} = \frac{A_i R_L}{Z_i}$$

Voltage Gain with Source

$$A_{v s} = \frac{V_2}{V_s} = \frac{V_2}{V_1} \cdot \frac{V_1}{V_s}$$

$$\therefore A_{v s} = A_{v e} \cdot \frac{V_1}{V_s}$$

Power Gain (Ap)

$$A_p = P_2 / P_1 = \frac{V_2 I_2}{V_1 I_1} = A_{v e}, A_i = A_i \frac{R_L}{Z_i}$$

$$A_p = \frac{A_i^2 R_L}{Z_i}$$

$$\left[\because A_e = A_i \frac{R_L}{Z_i} \right]$$

Output Admittance (y_o).

$$y_o = \frac{1}{h_o} = \frac{I_2}{V_2} \text{ with } V_S = 0$$

$$I_2 = h_f I_1 + h_o V_2.$$

\therefore above eqn by V_2 .

$$\frac{I_2}{V_2} = h_f \frac{I_1}{V_2} + h_o$$

$V_S = 0$, we can write

$$R_S I_1 + h_i I_1 + h_r V_2 = 0.$$

$$(R_S + h_i) I_1 = -h_r V_2$$

$$\frac{I_1}{V_2} = \frac{-h_r}{R_S + h_i}$$

$$y_o = \frac{-h_f h_r}{R_S + h_i} + h_o$$

$$y_o = h_o - \frac{h_f h_r}{R_S + h_i}$$

Power Gain (A_p)

$$A_p = \frac{P_2}{P_1} = \frac{V_2 I_2}{V_1 I_1} = A_{le} \cdot A_i$$

$$[P_2 = V_2 I_L = -V_2 I_2]$$

$$A_p = A_i^2 \frac{R_L}{Z_i} \quad [\therefore A_{le} = \frac{A_i R_L}{Z_i}]$$

Summary :-

$$A_i = \frac{-h_f}{1 + h_{fe}}$$

$$A_{is} = \frac{A_f R_s}{Z_i + R_s}$$

$$Z_i = h_{ie} + h_{fr} A_i R_L = h_{ie} - \frac{h_f h_r}{h_{ot} + Y_L}$$

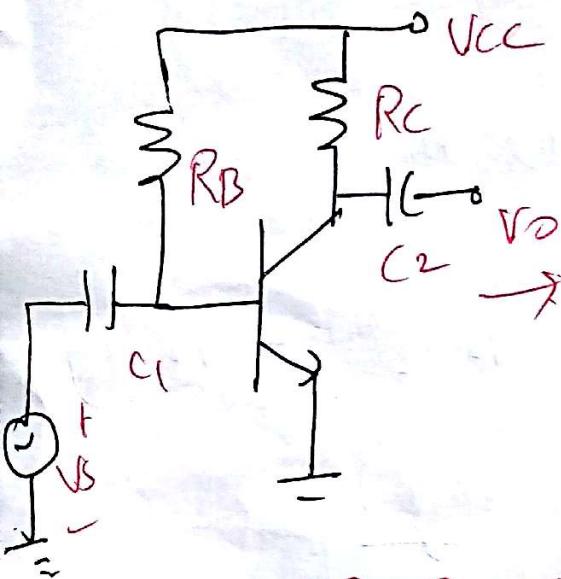
$$A_{le} = \frac{A_i R_L}{R Z_i}$$

$$A_{les} = \frac{A_{is} R_L}{R_s}$$

$$Y_o = h_o - \frac{h_f h_r}{h_{ie} + R_s}$$

$$A_p = A_i^2 \cdot \frac{R_L}{Z_i}$$

Transistor Amplifiers



re-model is also sensitive to the dc operating point of the amplifiers circuit. re model is used for low frequency analysis

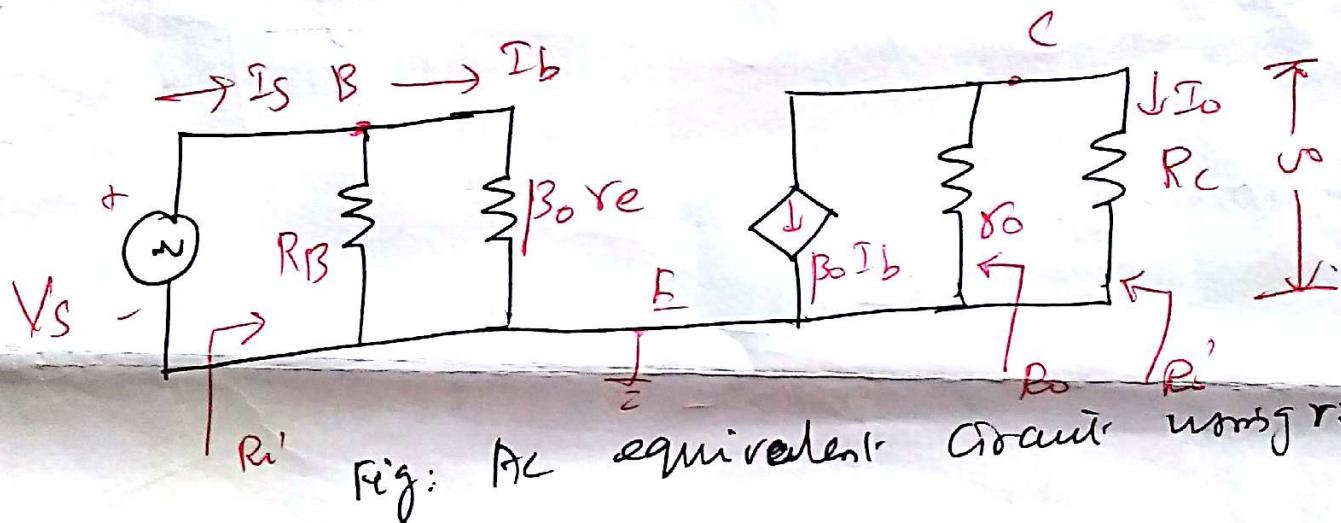


Fig: AC equivalent circuit using re-model

DC Analysis

$$I_B = \frac{V_{CC} - V_{BE}}{R_B}$$

$$I_E = (1 + \beta) I_B$$

$$r_e = \frac{26 \text{ mV}}{I_E}$$

BIP resistance

$$R_i = \beta r_e$$

$$R_i' = R_i \parallel R_B$$

o/p resistance.

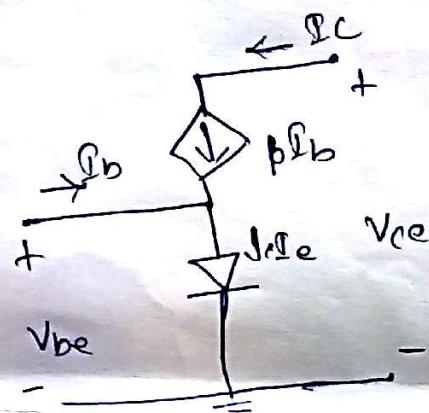
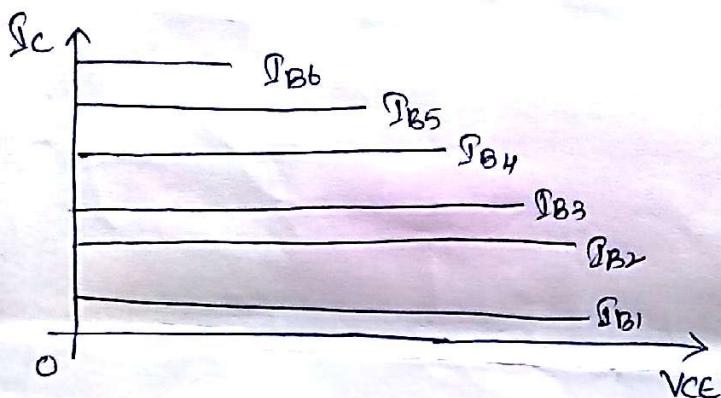
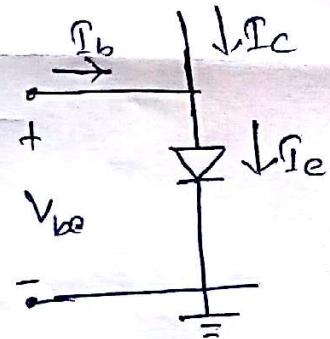
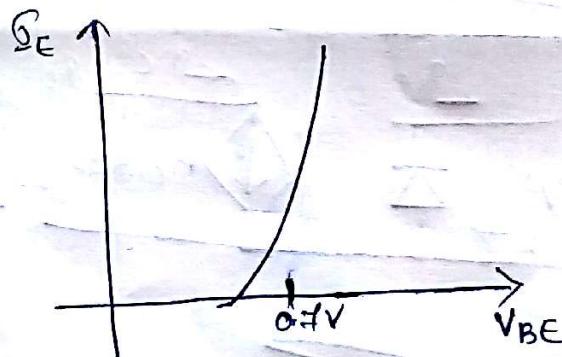
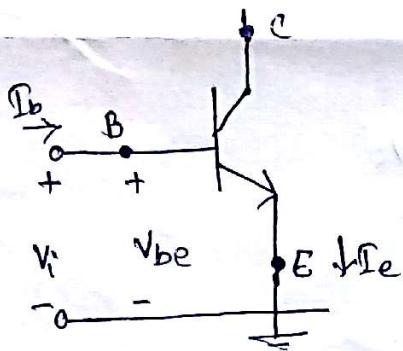
$$R_o = r_o$$

$$R_o' = R_C \parallel r_o$$

(5)

The V_e Transistor Model:

C.G Configuration

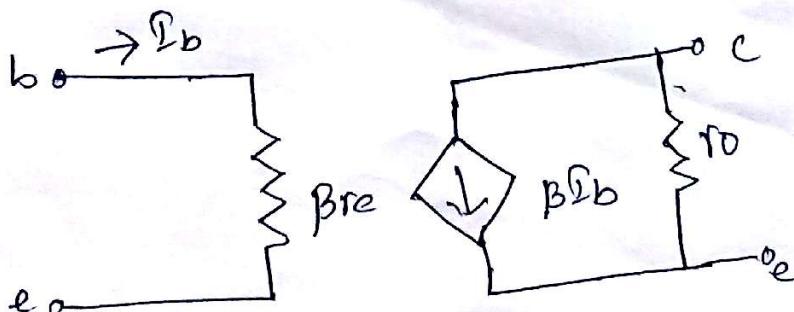


$$Z_i = \frac{V_i}{I_b} = \frac{V_{be}}{I_b}$$

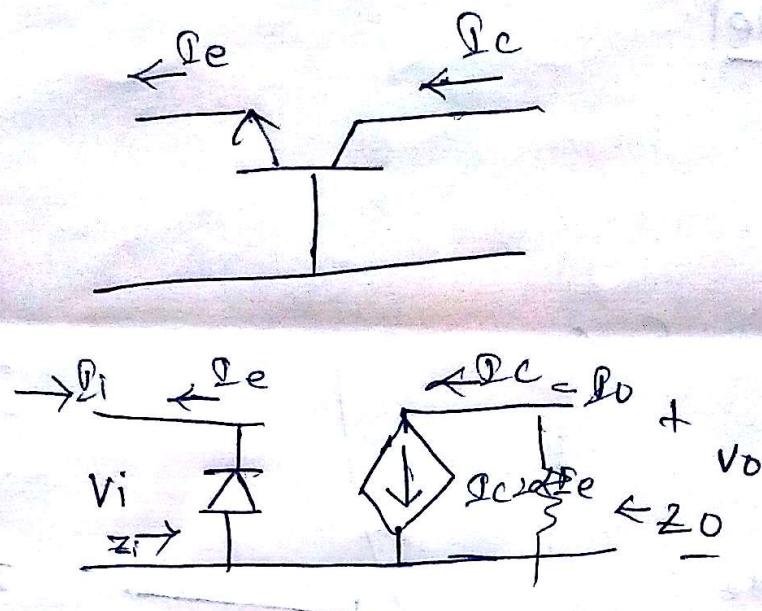
$$\begin{aligned} V_{be} &= I_{ere} = (R_c + R_b) r_e = (\beta R_b + R_b) r_e \\ &= (\beta + 1) R_b r_e \end{aligned}$$

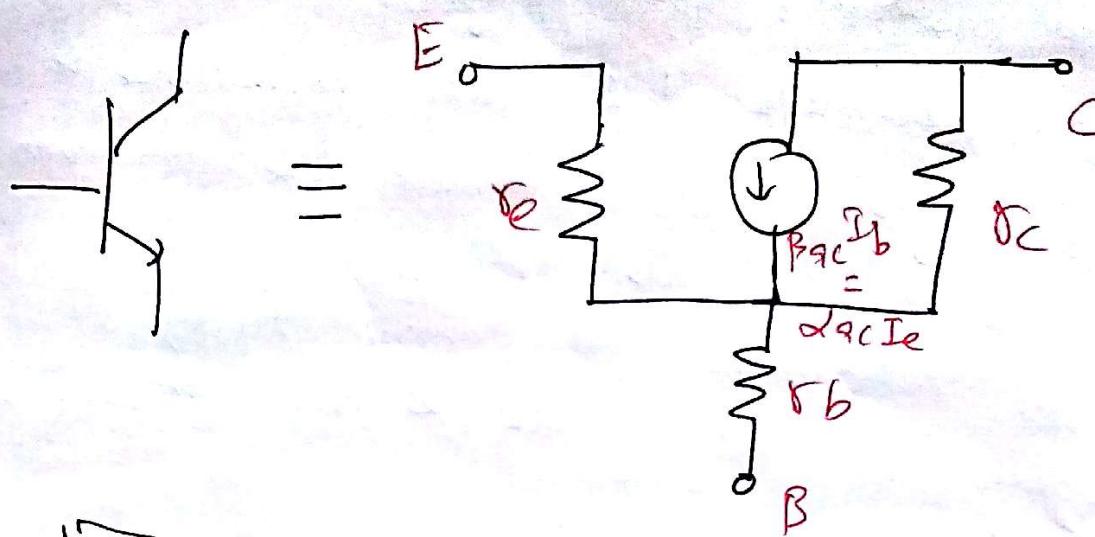
$$Z_i = \frac{V_{be}}{I_b} = \frac{(\beta + 1) R_b r_e}{I_b} = (\beta + 1) r_e \approx \beta r_e$$

$$r_e = \frac{26 \text{ mV}}{\beta}$$



Common Base Configuration:





r-parameter	Description
α_{ac}	ac alpha (I_c/I_e)
β_{ac}	ac beta (I_c/E_b)
r_e	ac emittes resistance.
r_c	ac collector "
r_b	ac base "

Relationships of r-parameter & h-parameters

$$\alpha_{ac} = h_{fb}$$

$$\beta_{ac} = h_{fe}$$

$$r_e = \frac{h_{re}}{h_{oe}}$$

$$r_c = \frac{h_{ce} + 1}{h_{oe}}$$

$$r_b = h_{ie} - \frac{h_{re}(1+h_{fe})}{h_{oe}}$$

$$r_e = \frac{KT}{qIE}$$

$$K = 1.38 \times 10^{-23} \text{ J/K}$$

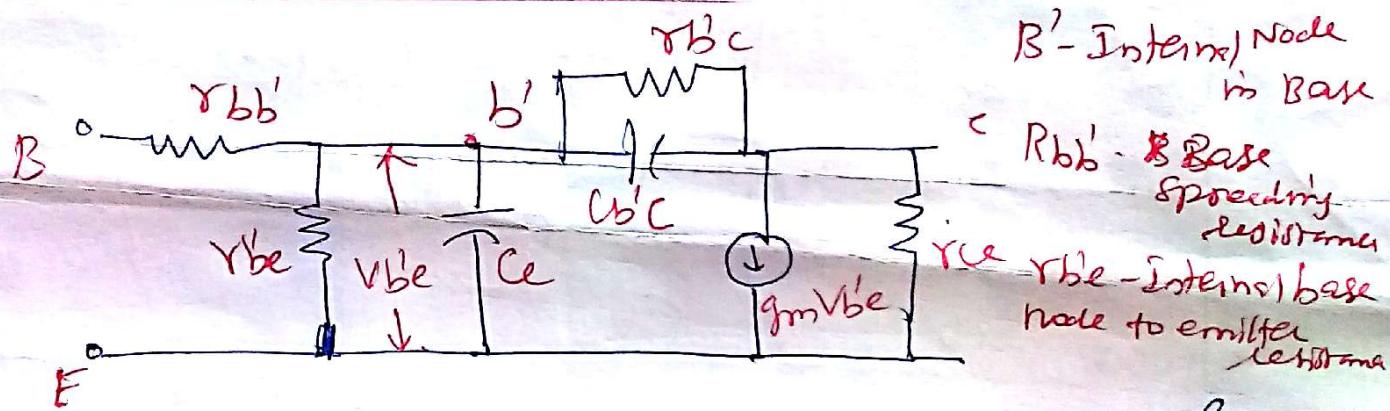
$$T = 273^\circ\text{C}$$

$$q = 1.602 \times 10^{-19}$$

$$r_e = 26 \text{ mV} \cdot \frac{1}{25^\circ\text{C}}$$

The re model is sufficiently accurate and requires one parameter h_{fe} . The input impedance is derived from just one parameter h_{fe} . However the de model does not have parameters for output admittance g_{o} reverse voltage ratio. It is only suitable at dc & mid frequencies.

Common Emitter Short Circuit Current Gain of Transistor at high frequency using hybrid-II model



The hybrid-II model with resistive load
Hybrid-II model is used to analyse the BJT
in high frequency range.

r_a = Collector to emitter resistance

C_b = diffusion capacitance of emitter base J_{BS}

$r_{be'}$ = Feedback resistance from internal
base node to collector node

g_m = transconductance

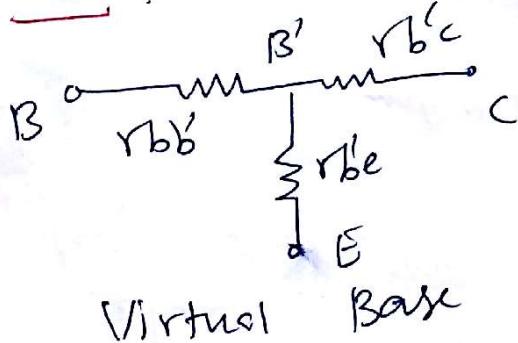
(7)

The main advantage of hybrid-T model is that it can be simplified to obtain low frequency model of BJT. This is done by eliminating capacitance. So that BJT responds without any significant delay to the input signal.

Elements of hybrid-T model.

C_{be} & C_{bc} : forward biased pN Junction exhibits a capacitive effect called the diffusion capacitance. This is represented as $C_{be} \approx C_e$. By the reverse bias pN junction exhibits capacitive effect called the transition capacitance. It is represented by $C_{bc} \approx C_c$.

$r_{bb'}$:



The internal node b' is physically not accessible; bulk node b represents external base terminal. base spreading resistance

$r_{bb'}$ is called as

r_{bc} : Due to early effect, the voltage across the collector to emitter junction results in base-width modulation. A change in the effective base width causes the emitter current to change. This feedback effect

between o/p to I_{dp} is represented by $r_{b'}$

$r_{b'}$: This resistance is in series with the collector junction.

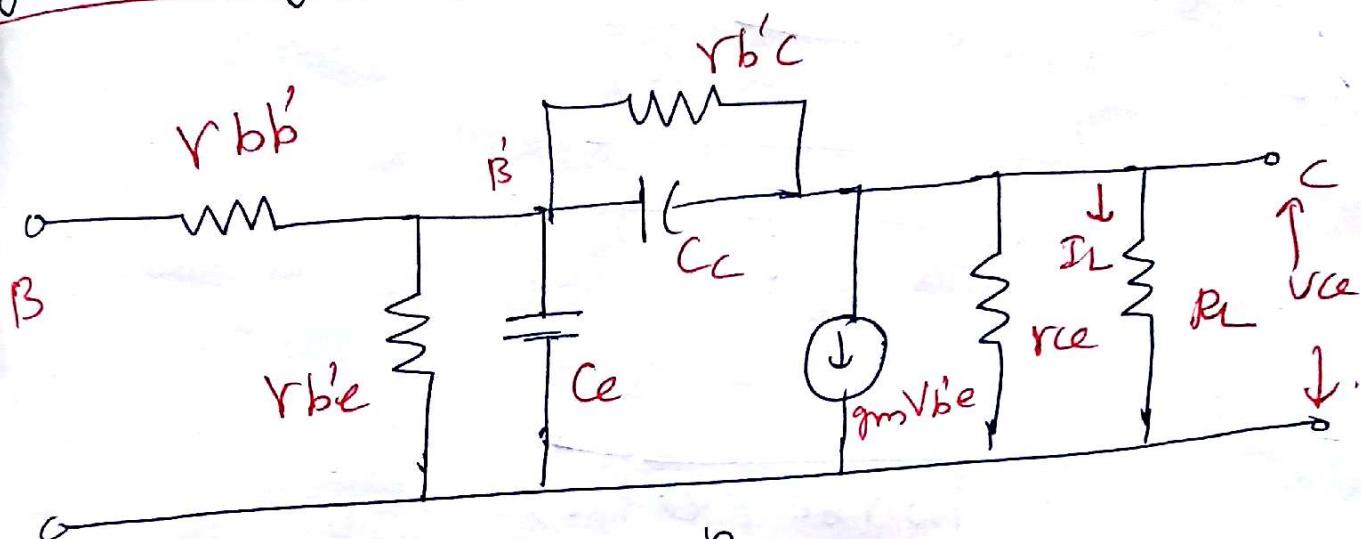
g_m : Due to small changes in voltage $V_{b'e}$ across the emitter junction, there is excess minority carrier concentration injected into the base which is proportional to $V_{b'e}$.

Hybrid π Parameters value.

Parameter	meaning	Value.
g_m	Mutual conductance	50 mA/V
r_{bb}	Base spreading resistance	100 Ω
r_{ce}	O/p resistance	80 k Ω
$r_{b'e}$	Resistance between b' & e	1 k Ω
$r_{b'c}$	Reverse biased PN junction between base & collector	4 M Ω
C_e	junction capacitance	100 pF
C_c	junction capacitance	3 pF

Common Emitter Short circuit Current
gain using hybrid- Π -model,

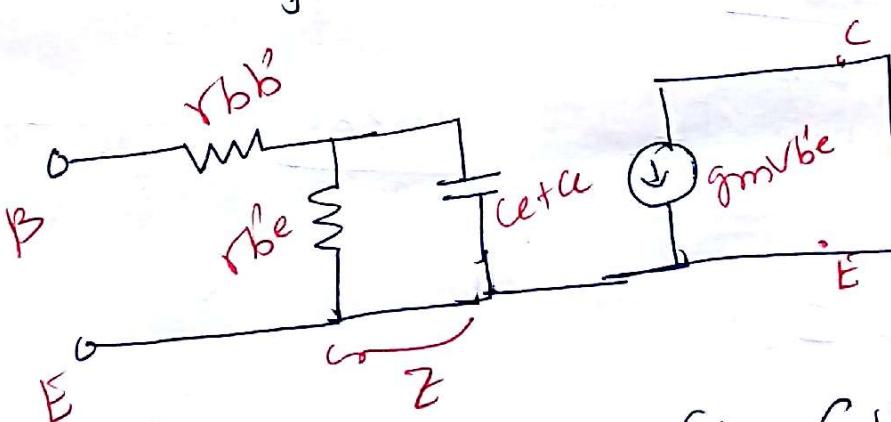
(8)



E Let us assume $R_L = 0$

If $R_L = 0$, OIP short circuited V_{ce} becomes zero, $C_e \gg C_{bc'}$ becomes parallel. $C_{bc'}$ appears between base & emitters, it is known as Miller capacitance. (C_M)

$$j_{ph} C_M = C_{bc'} (1 + g_m R_L)$$



Fig(2)
Simplified hybrid- Π model for short circuit

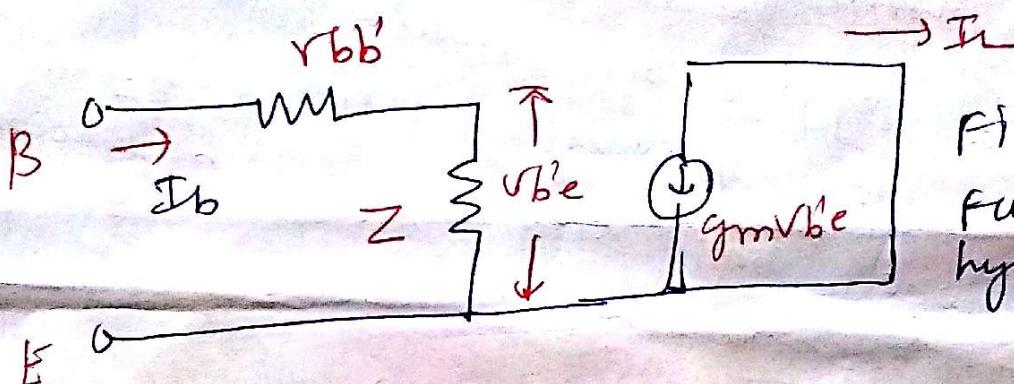
Here $R_L = 0$; $C_M = C_{bc'} \cdot \infty$.

As $r_{bc'} \gg r_{be}$, r_{be} is neglected.
 With these approximation, we get simplified hybrid- Π -model for short circuit CB transistor as shown in Fig.

Net combination of $r_{b'e} \times (C_{et} + C_c)$ is given

$$Z = \frac{r_{b'e} \times \frac{1}{j\omega(C_{et} + C_c)}}{r_{b'e} + \frac{1}{j\omega(C_{et} + C_c)}}$$

$$Z = \frac{r_{b'e}}{1 + j\omega r_{b'e} (C_{et} + C_c)}$$



Fig(3)

further simplifying
hybrid-pi model

$$V_{b'e} = I_b \cdot Z$$

$$\Rightarrow Z = \frac{V_{b'e}}{I_b}$$

Current gain $A_i = \frac{I_c}{I_b} = -\frac{g_m V_{b'e}}{I_b}$ [since $I_c = -g_m V_{b'e}$]

$$A_i = -g_m \cdot Z$$

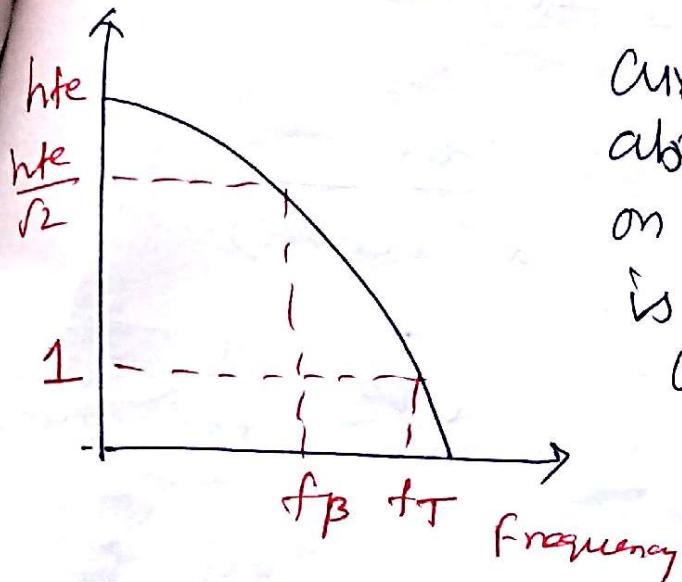
$$A_i = -g_m \cdot \frac{r_{b'e}}{1 + j\omega r_{b'e} (C_{et} + C_c)}$$

Already we know $g_m V_{b'e} = V_{be} h_{fe}$

(9)

$$A_i = \frac{-h_{fe}}{1 + j\omega r_b'e(C_e + C_c)}$$

Current Gain



Current is not constant in the above equation. It depends on frequency. When frequency is small, the term containing f is very small compared to 1 so hence at low frequency $A_i = -h_{fe}$. But as frequency increases A_i reduces as shown in fig.

Let us take

$$f_p = \frac{1}{2\pi r_b'e(C_e + C_c)}$$

$$A_i = \frac{-h_{fe}}{1 + j\frac{f}{f_p}}$$

$$|A_i| = \frac{h_{fe}}{\sqrt{1 + \left(\frac{f}{f_p}\right)^2}}$$

As $f = f_p$

$$A_i = \frac{h_{fe}}{\sqrt{2}}$$

f_B (cut-off frequency)

It is the frequency at which the transistor's short circuit CE current gain drops by 3dB or $\frac{1}{\sqrt{2}}$ times from its value at low frequency.

$$f_B = \frac{1}{2\pi r_{be}^{\prime}e (c_{et} + c_e)} = \frac{g_{be}^{\prime}e}{2\pi (c_{et} + c_e)}$$

$$\boxed{f_B = \frac{1}{h_{fe}} \cdot \frac{g_m}{2\pi (c_e + c_{et})}}$$

$$\left[g_{be}^{\prime}e = \frac{1}{r_{be}^{\prime}e} = \frac{g_m}{h_{fe}} \right]$$

f_α (Cut-off frequency)

It is the frequency at which the transistor's short circuit CB current gain drops by 3dB or $\frac{1}{\sqrt{2}}$ times from its value at low frequency.

$$A_i = \frac{-h_{fb}}{1 + j(\frac{f}{f_\alpha})}$$

$$\text{where } f_\alpha = \frac{1}{2\pi r_{be}^{\prime}e (1 + h_{fe}) \cdot c_e} = \frac{1 + h_{fe}}{2\pi r_{be}^{\prime}e c_e}$$

$$\approx \frac{h_{fe}}{2\pi r_{be}^{\prime}e c_e}$$

$$|A_i| = \frac{h_{fb}}{\sqrt{1 + \left(\frac{f}{f_\alpha}\right)^2}} \quad \text{when } f = f_\alpha$$

$$A_i = \frac{h_{fb}}{\sqrt{2}}$$

(10)

The parameter f_T :

It is the frequency at which short circuit CE current gain becomes unity

at $f = f_T$

$$1 \neq \frac{h_{fe}}{f_T / f}$$

$$1 = \frac{h_{fe}}{\sqrt{1 + \left(\frac{f}{f_T / f_B}\right)^2}}$$

$f_T / f_B \gg 1$, then

$$1 = \frac{h_{fe}}{f_T / f_B}$$

$$\Rightarrow f_T = f_B \cdot h_{fe}$$

$$f_T = \frac{h_{fe} \cdot g_m}{h_{fe} \cdot 2\pi (C_L + C_C)}$$

$$f_T = \frac{g_m}{2\pi (C_L + C_C)}$$

$$\therefore f_B = \frac{1}{h_{fe}} \cdot \frac{g_m}{2\pi (C_L + C_C)}$$

Since $C_L \gg C_C$

$$f_T = \frac{g_m}{2\pi C_L}$$