

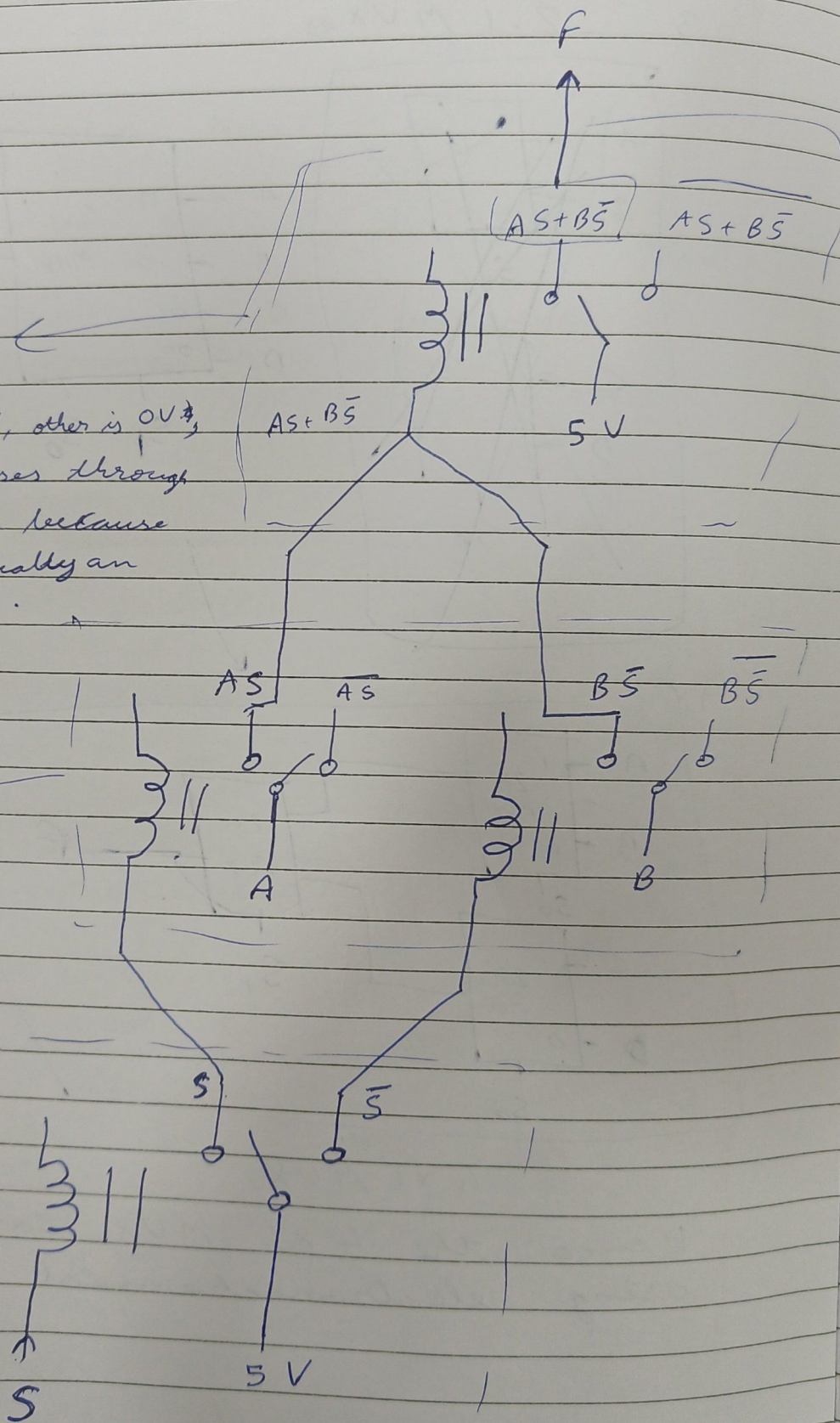
2:1 MUX

Wired OR
Gate

(If 1 wire is 5V, other is 0V, still current passes through electromagnet, because 0V wire is actually an open circuit).

AND
Gates

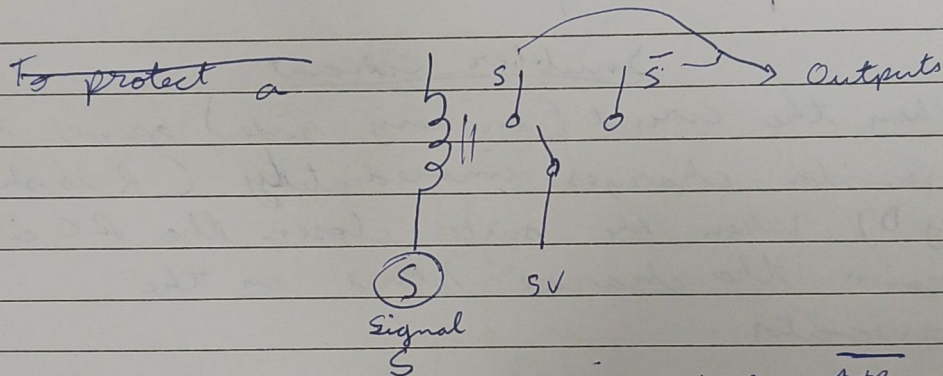
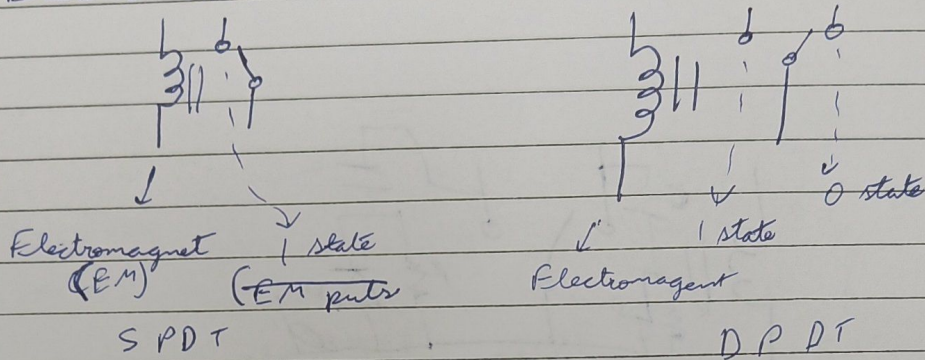
Selector



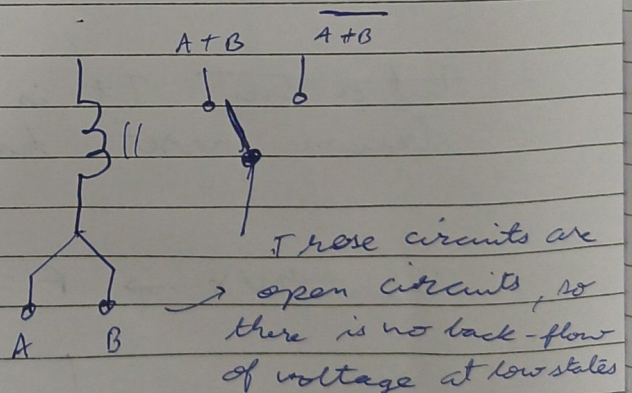
Q2)

PART - A

Electromechanical Switch's schematic diagram



Wired OR Gate :-



Electromagnet pulls the armature towards the pin in the left when it is ON.
 ∴ If signal S is passed through EM, the left pin gives S as output and right pin gives \bar{S} as output.

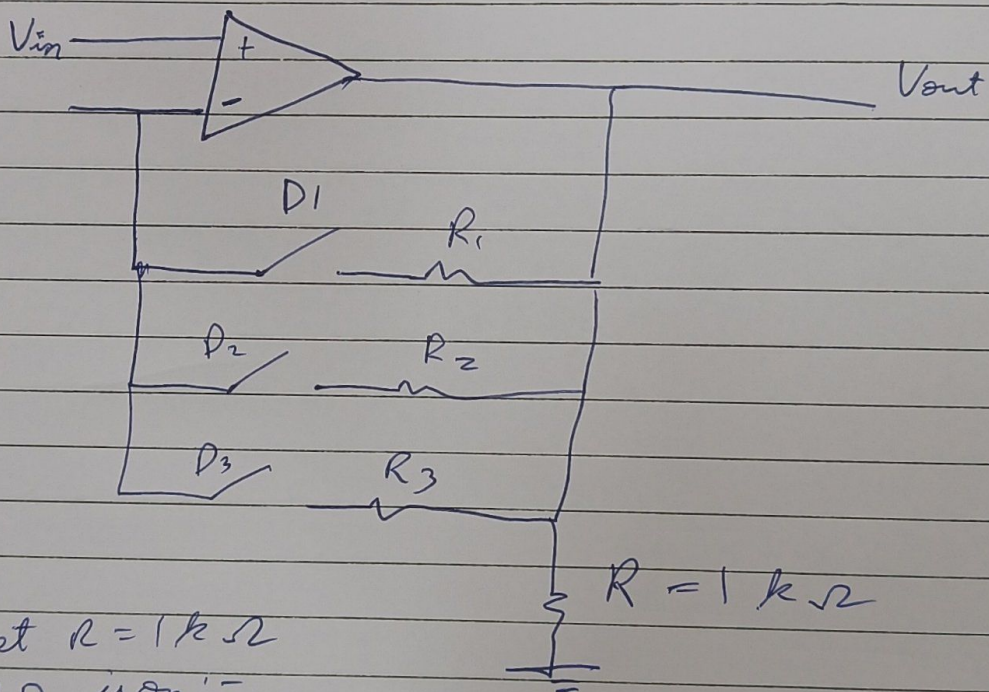
$$D_1 = \bar{A} \quad (1 \text{ if } V_{in} < 100)$$

$$D_2 = A \bar{B} \quad (1 \text{ if } V_{in} > 100 \text{ and } V_{in} < 200)$$

$$D_3 = B \quad (1 \text{ if } V_{in} > 200)$$

Part B

Designing the programmable gain amplifier



Let $R = 1 \text{ k}\Omega$

If D_1 is on :-

$$V_{out} = \left(\frac{V_{in}}{R_1} + \frac{0}{R_a} \right) \left(\frac{R_1 R_a}{R_1 + R_a} \right)$$

$$= V_{in} \times \frac{1}{R_1 + 1}$$

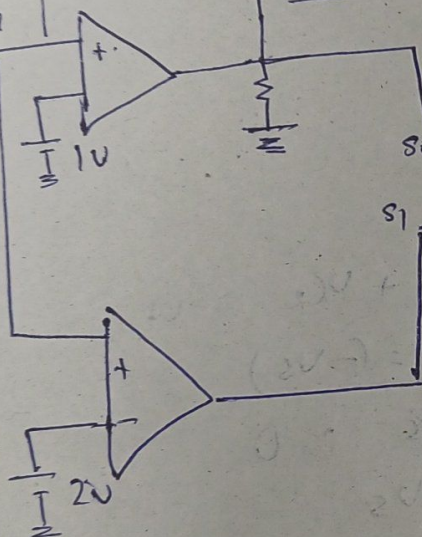
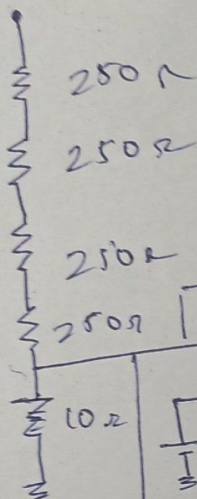
~~The~~ V_{out} must be 3.3 V

$$\therefore R_1 = \frac{V_{in}}{3.3} - 1$$

$$= \frac{100}{3.3} - 1 = \underline{\underline{29.303 \text{ k}\Omega}}$$

D_1 is on if $V_{in} < 100 \therefore V_{out} = 3.3 \text{ V}$ if $V_{in} = 100$

$V_{out} \approx 300V$

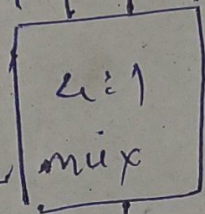


$R_F 33k\Omega$
200-200V

$R_F 110k\Omega$
1-100V

$R_F 55k\Omega$
100-200V

$R_F 110k\Omega$
Default



of-amp gain

4bit ADC

2.4

00	0000
01	0010
10	0100
11	1000