

AMLOGIC S905x- XIAOMI MI TV BOX S ANDROID TVBOX

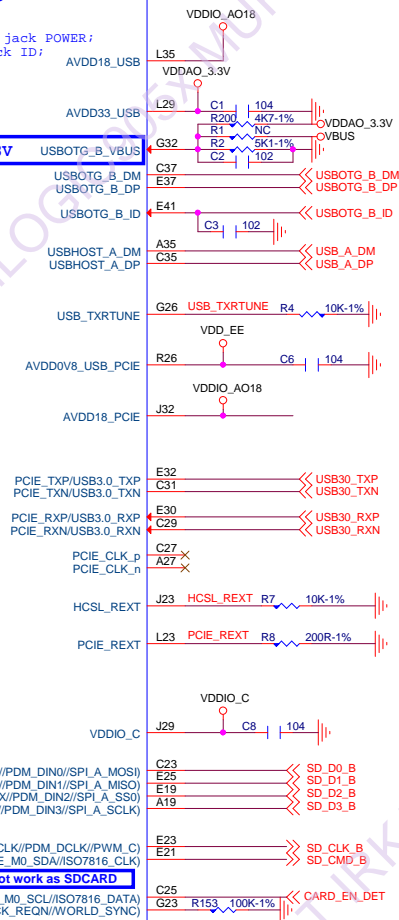
02 CPU01

U1E

LIB VER: 2018-01-05

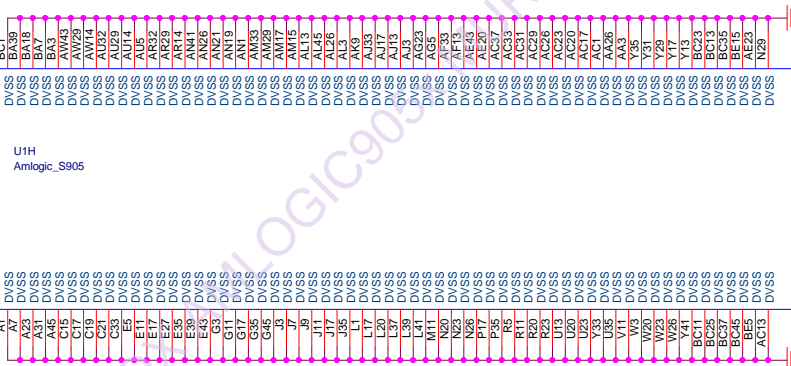
For USB OTG application note:
VBUS:1.8V tolerate
USB_VBUS series a 10K 1% to usb jack POWER;
USB_ID series a 200ohm to usb jack ID;

Tolerance 1.8V



Amlogic_S905

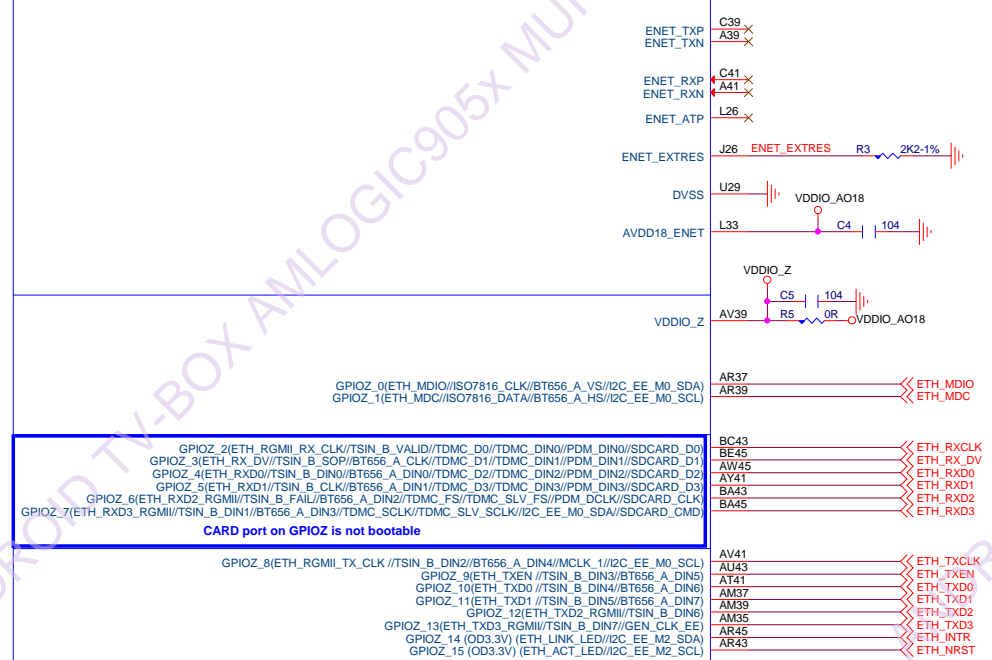
LIB VER: 2018-01-05



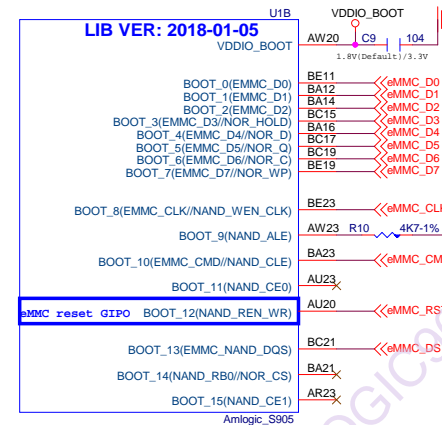
U1H
Amlogic_S905

U1G

LIB VER: 2018-01-05

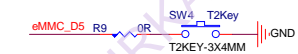


Amlogic_S905



Amlogic_S905

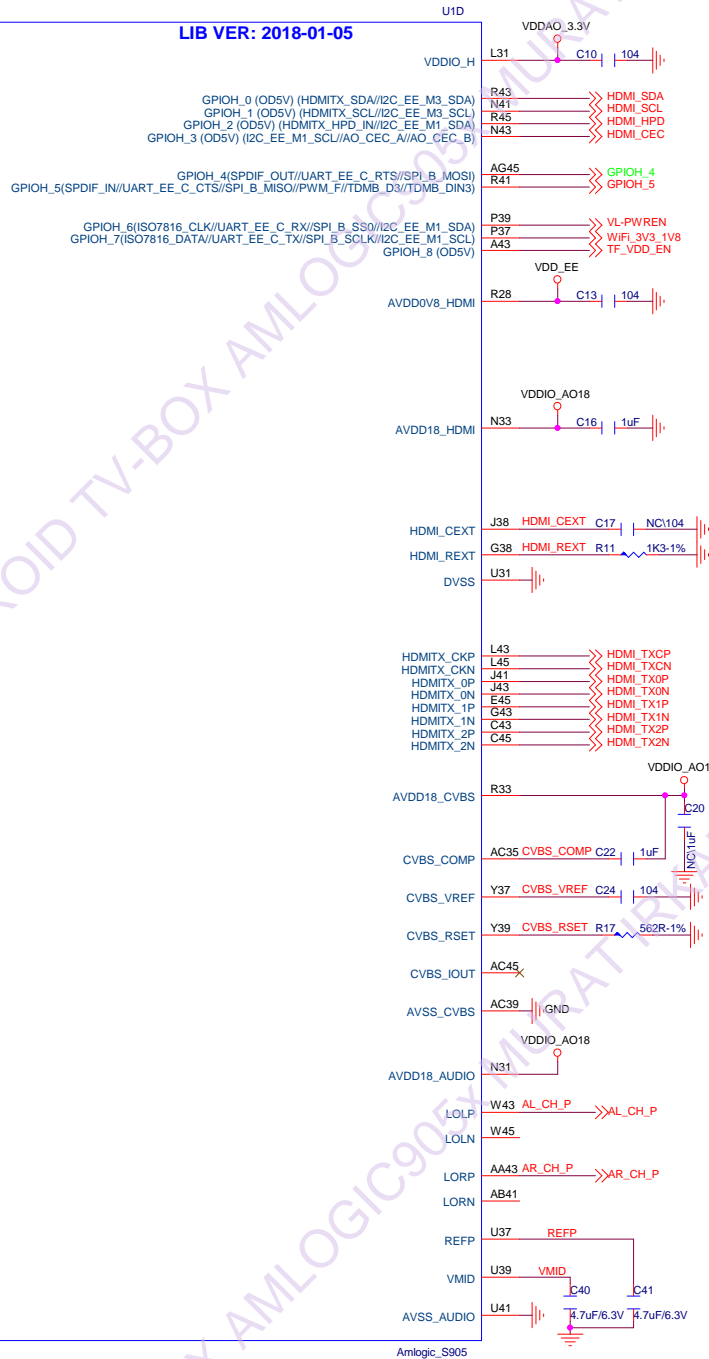
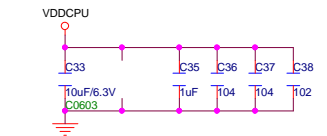
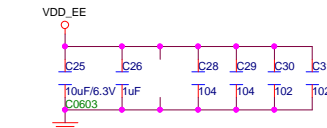
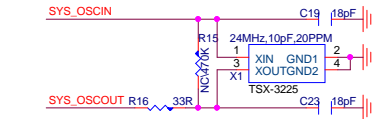
Power ON Config



BOOT_5[NAND_D5]: 0= USB Boot First

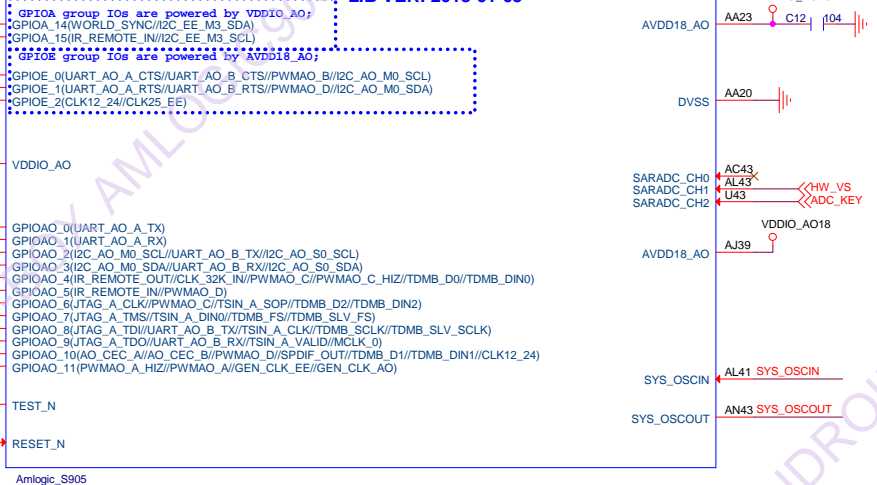
03 CPU02

LIB VER: 2018-01-05

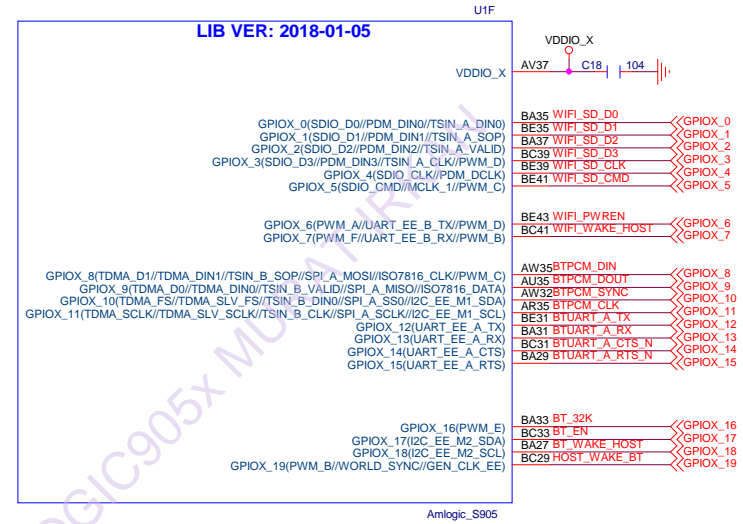
GPIOA_14
GPIOA_15VDD_EE_PWM
VDDCPU_PWM
TF_IO_PWR_ENLINUX_TX
LINUX_RX
GPIOA_2
GPIOA_3
GPIOA_4
IR_IN
TF_3V3N_1V8_EN
GPIOA_7
GPIOA_8
GPIOA_9
GPIOA_10
SYS_LEDVDDCPU_EN
VDDAO_3.3V
CPU_RST

U1A

LIB VER: 2018-01-05

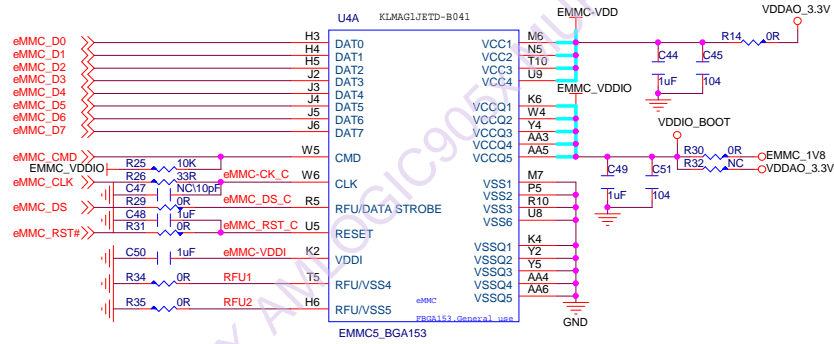


LIB VER: 2018-01-05

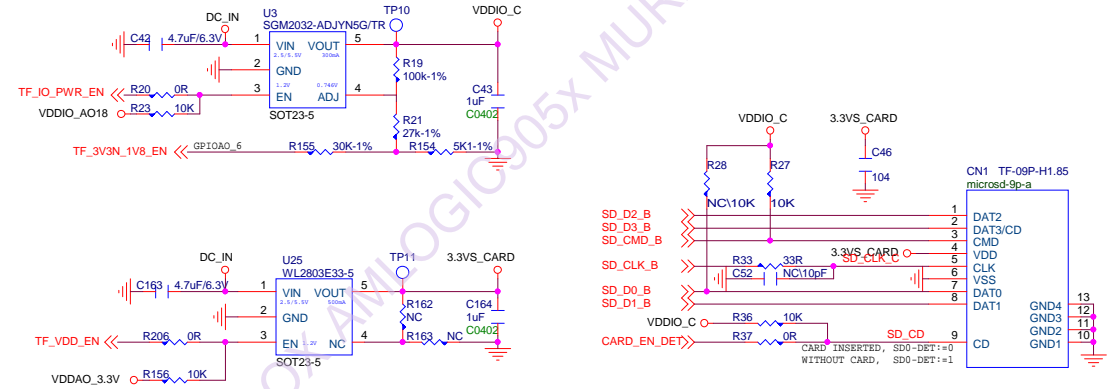


04 MSDC

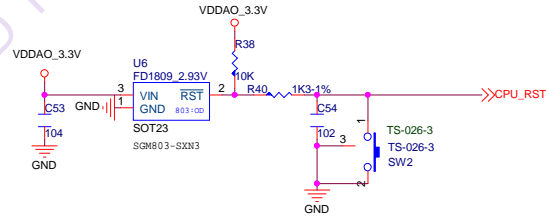
eMMC



MicroSD

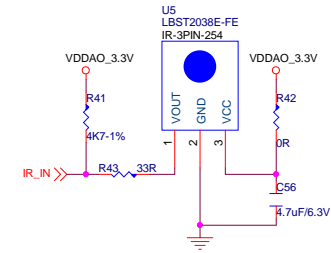


CPU reset



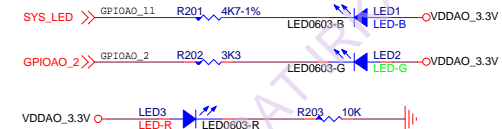
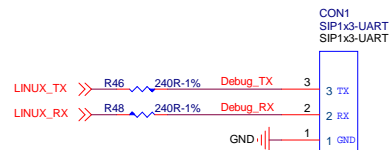
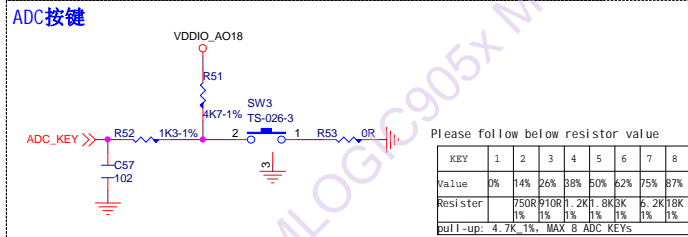
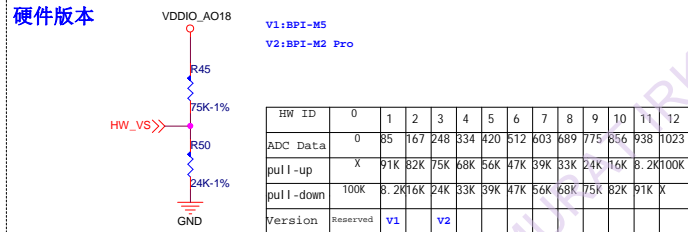
Update button

IR-IN

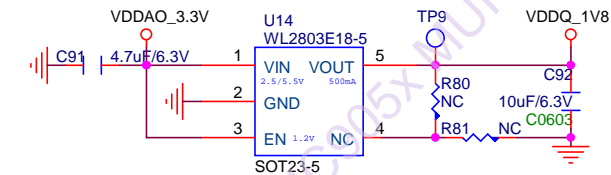
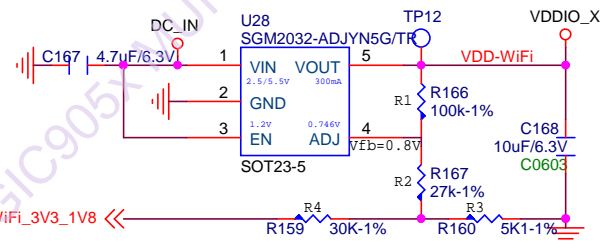
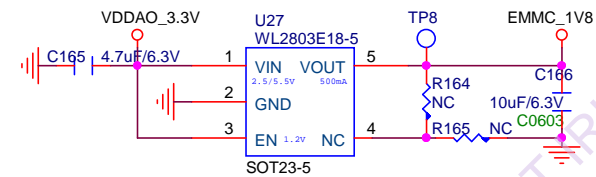
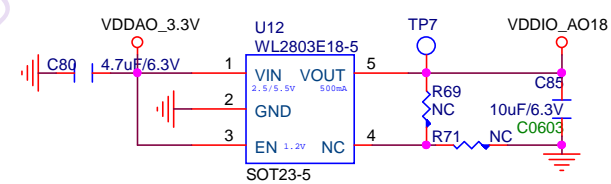
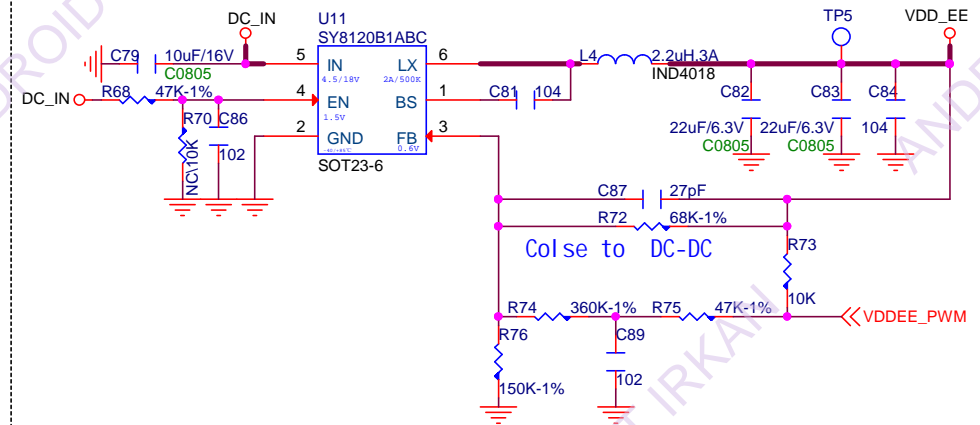
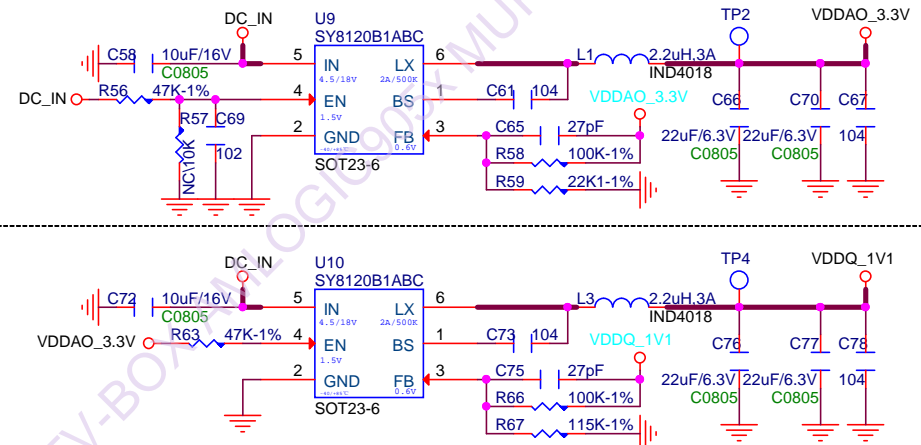
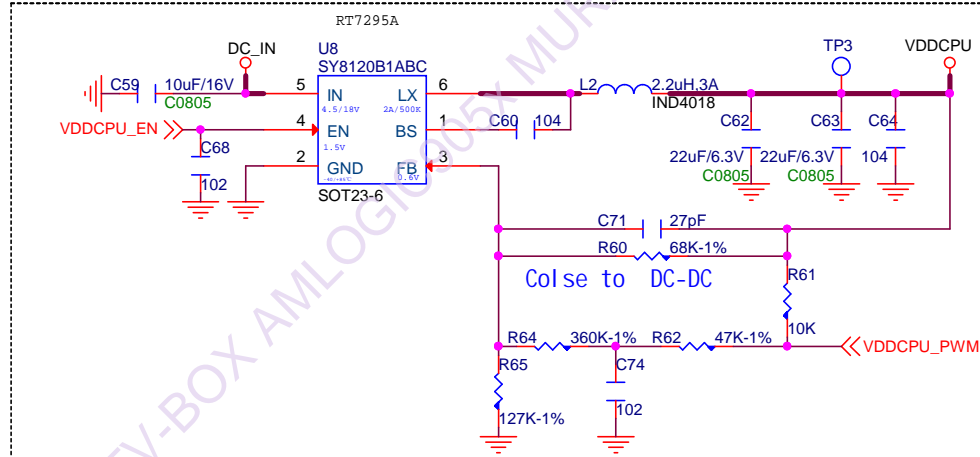


linux debug

System LED



05 Power



06-Ethernet

The diagram illustrates the Ethernet interface circuit for the RTL8211F(D)(I) chip. The chip is shown with its pinout and the components connected to it. The components are organized into several groups, each with a label indicating the pin it is connected to.

RTL8211F(D)(I) Pinout:

- 1: MDI[0]+
- 2: MDI[0]-
- 3: AVDD10 3
- 4: MDI[1]+
- 5: MDI[1]-
- 6: MDI[2]+
- 7: MDI[2]-
- 8: AVDD10 8
- 9: MDI[3]+
- 10: MDI[3]-
- 11: AVDD33 11
- 12: PHYRSTB
- 13: MDC
- 14: TXD3
- 15: TXD2
- 16: TXD1
- 17: TXD0
- 18: TXD0
- 19: TXD0
- 20: TXD0
- 21: TXD0
- 22: TXD0
- 23: TXD0
- 24: TXD0
- 25: TXD0
- 26: TXD0
- 27: TXD0
- 28: TXD0
- 29: TXD0
- 30: TXD0
- 31: TXD0
- 32: TXD0
- 33: TXD0
- 34: TXD0
- 35: TXD0
- 36: TXD0
- 37: TXD0
- 38: TXD0
- 39: TXD0
- 40: TXD0
- 41: TXD0

Components to be placed near specific pins:

- close to pin 21:** CG5, 104, GND
- close to pin 30:** LG1 WIP252012P-2R2ML, CG6 4.7uF/6.3V, CG7 104, GND
- close to pin 3 & 8 & 38:** CG8 104, CG9 104, CG10 104, GND
- close to pin 29:** CG11 104, CG12 4.7uF/6.3V, GND
- close to pin 11 & 40:** CG13 104, CG15 104, GND
- close to pin 39:** RSET0 RG42 2K49-1%, GND
- close to pin 28:** DVDDRG0 CG14 104, CG16 4.7uF/6.3V, GND

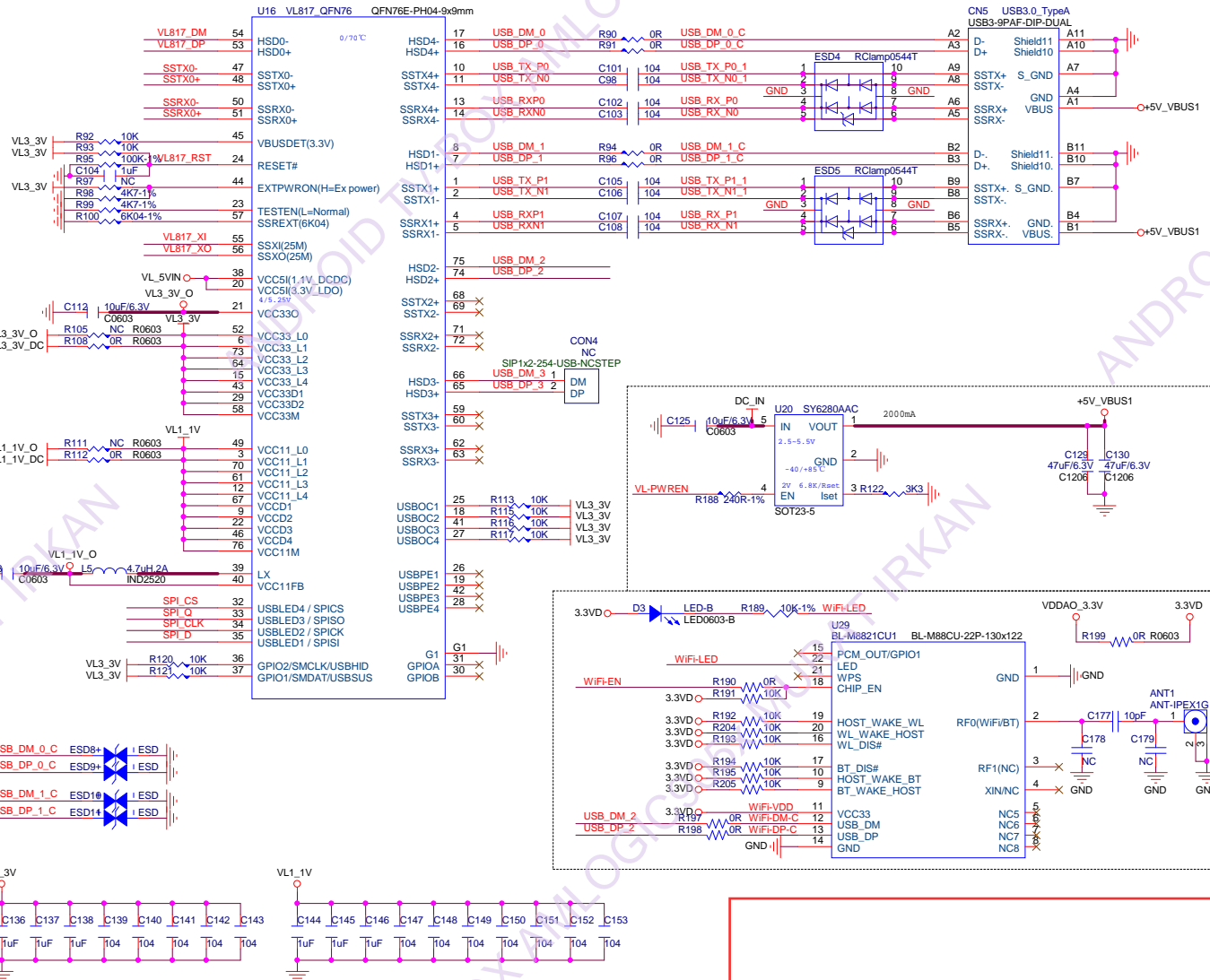
Other components and connections:

- ETH_MDIO: RGMII MDIO RG2 2K2-1%, VDDIO_Z
- ETH_RXCLK: CG2 104, NC10pF, GND
- ETH_RX_DV: RG3 33R, 0_RXC, RG6 33R, 0_RXCTL
- ETH_RXD0: 0_RXD0
- ETH_RXD1: 0_RXD1
- ETH_RXD2: 0_RXD2
- ETH_RXD3: 0_RXD3
- ETH_TXCLK: CG1 104, NC10pF, GND
- ETH_TXEN: RG11 33R, 0_TXC, RG16 33R, 0_TXCTL
- ETH_TXD0: 0_TXD0
- ETH_TXD1: 0_TXD1
- ETH_TXD2: 0_TXD2
- ETH_TXD3: 0_TXD3
- ETH_INTR: 0_INT
- ETH_Nrst: PHYRSTB0
- PHY addr = 1
- PHY2: 0_RXCTL, PHYAD2 RG5 4K7-1%
- PHY1: 0_RXC, PHYAD1 RG7 4K7-1%
- PHY0: 0_RXD3, PHYAD0
- PHY2: 0_RXD2, PLLOFF RG9 4K7-1%
- PHY1: 0_RXD1, TXDLY RG15 4K7-1%
- PHY0: 0_RXD0, RXDLY
- XTAL_OUT0: R178 33R, CG3 27pF, GND
- XTAL_IN0: CG4 27pF, GND
- TSX-3225
- CG1 104, NC10pF, GND
- CG2 104, NC10pF, GND
- CG3 27pF, GND
- CG4 27pF, GND
- CG5 104, GND
- CG6 4.7uF/6.3V, GND
- CG7 104, GND
- CG8 104, GND
- CG9 104, GND
- CG10 104, GND
- CG11 104, GND
- CG12 4.7uF/6.3V, GND
- CG13 104, GND
- CG15 104, GND
- CG14 104, GND
- CG16 4.7uF/6.3V, GND
- RG1 4K7-1%, DVDD33
- RG2 2K2-1%, VDDIO_Z
- RG3 33R, 0_RXC
- RG4 2K49-1%, GND
- RG5 4K7-1%
- RG6 33R, 0_RXCTL
- RG7 4K7-1%
- RG8 4K7-1%
- RG9 4K7-1%
- RG10 4K7-1%
- RG11 33R, 0_TXC
- RG12 4K7-1%
- RG13 4K7-1%
- RG14 4K7-1%
- RG15 4K7-1%
- RG16 33R, 0_TXCTL
- RG17 4K7-1%
- RG18 33R
- RG19 4K7-1%
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- RG164 4K7-1

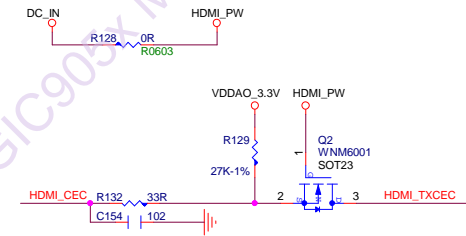
TypeC



USB_A_DM R88 0R VL817_DM
 USB_A_DP R89 0R VL817_DP
 USB30_TXN C96 104 SSRX0-
 USB30_TXP C99 104 SSRX0+
 USB30_RXN C100 104 SSTX0-
 USB30_RXP C97 104 SSTX0+
 VL-PWREN
 GPIOH_4 VL-RST_N R169 0R VL817_RST
 GPIOAO_3 WiFi-EN



HDMI OUT



Header-SIP1x3-254-MALE-NCSTEP

CON3
NC

AR_CH_P >> 3 3 TX

AL_CH_P >> 2 2 RX

1 1 GND

