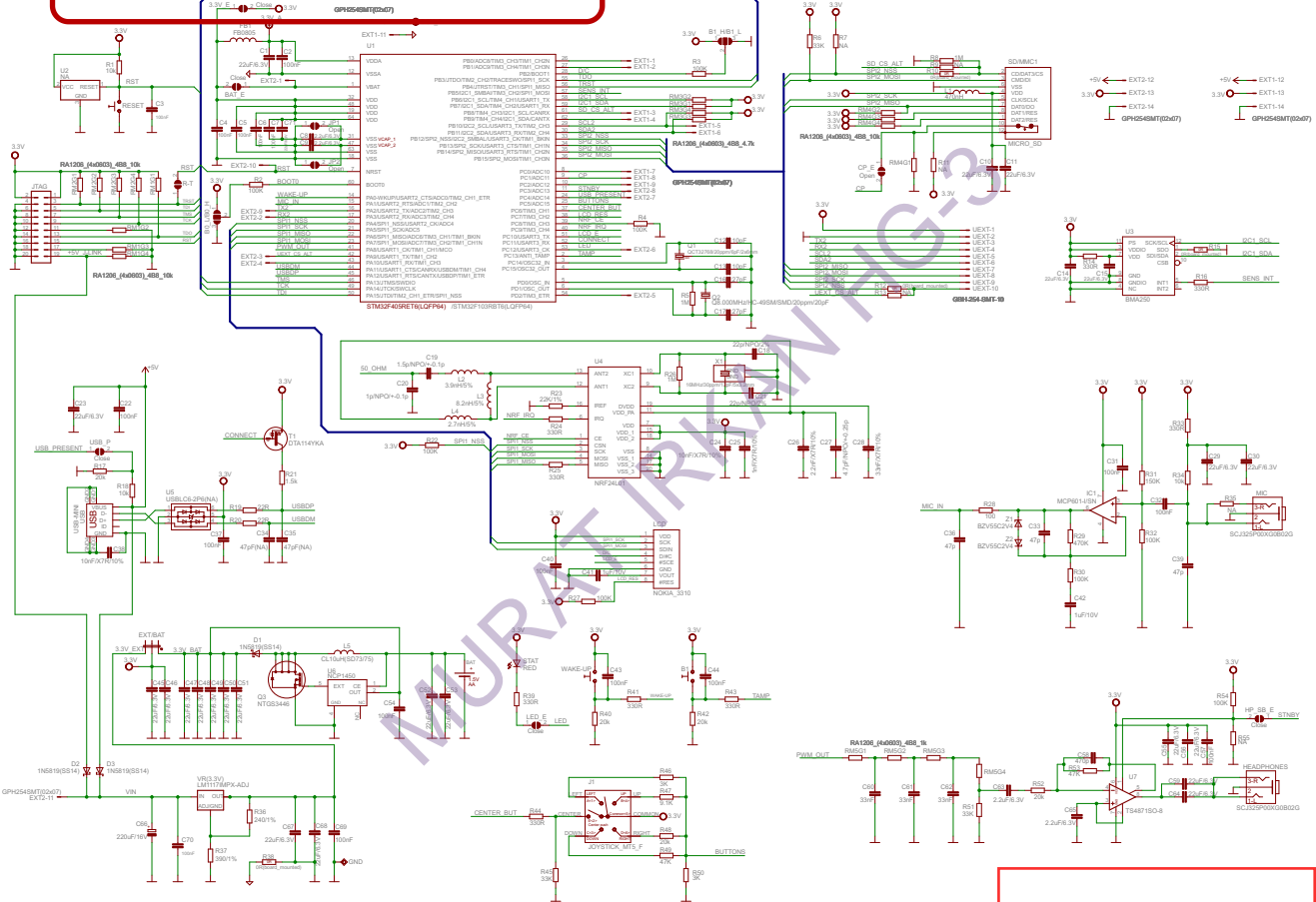


STM32F405RET6 ARM-CORTEX M4 HG-3



Notes:

1. When STM32F405RET6LQFP94 is mounted, J1 and J2 must be opened! C46 and C47 must be populated!
2. When STM32F405RET6LQFP94 is mounted, J1 and J2 must be closed! C46 and C47 should be not populated!
3. U1 is optional when is STM32F405RET6LQFP94.

For the STM32F405RET6LQFP94, please see the corresponding datasheet