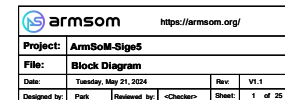
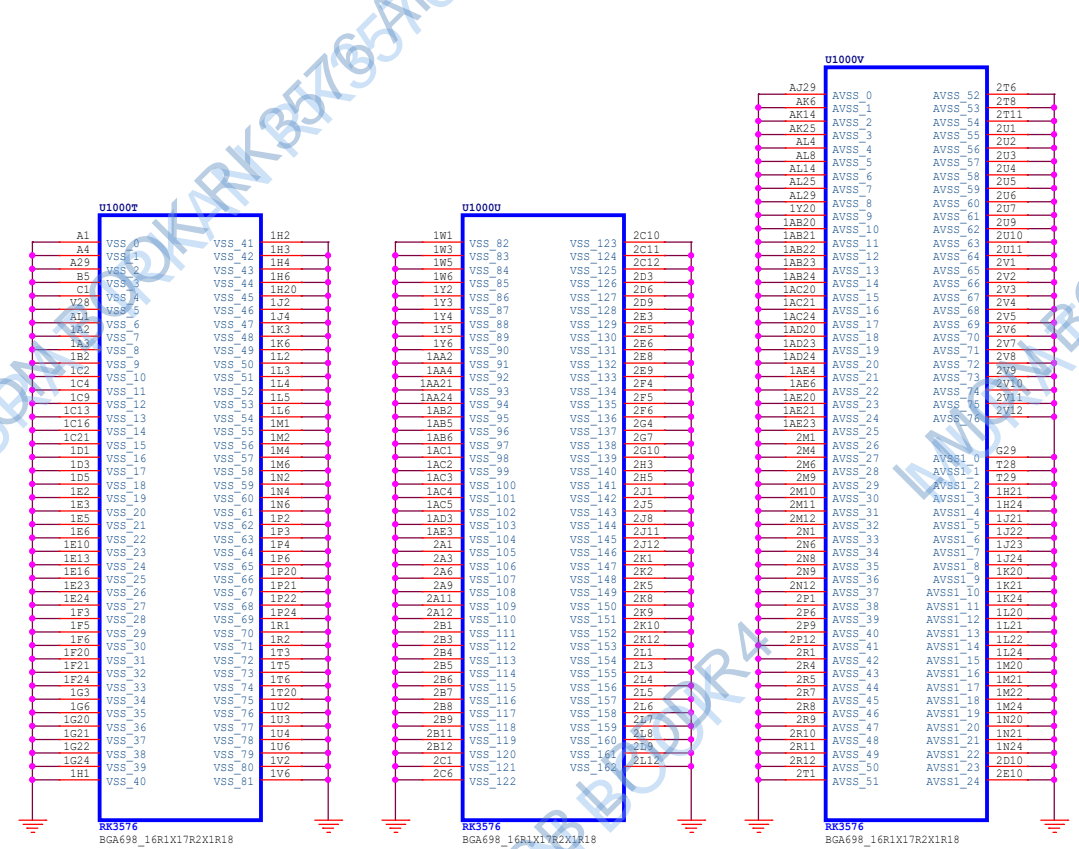
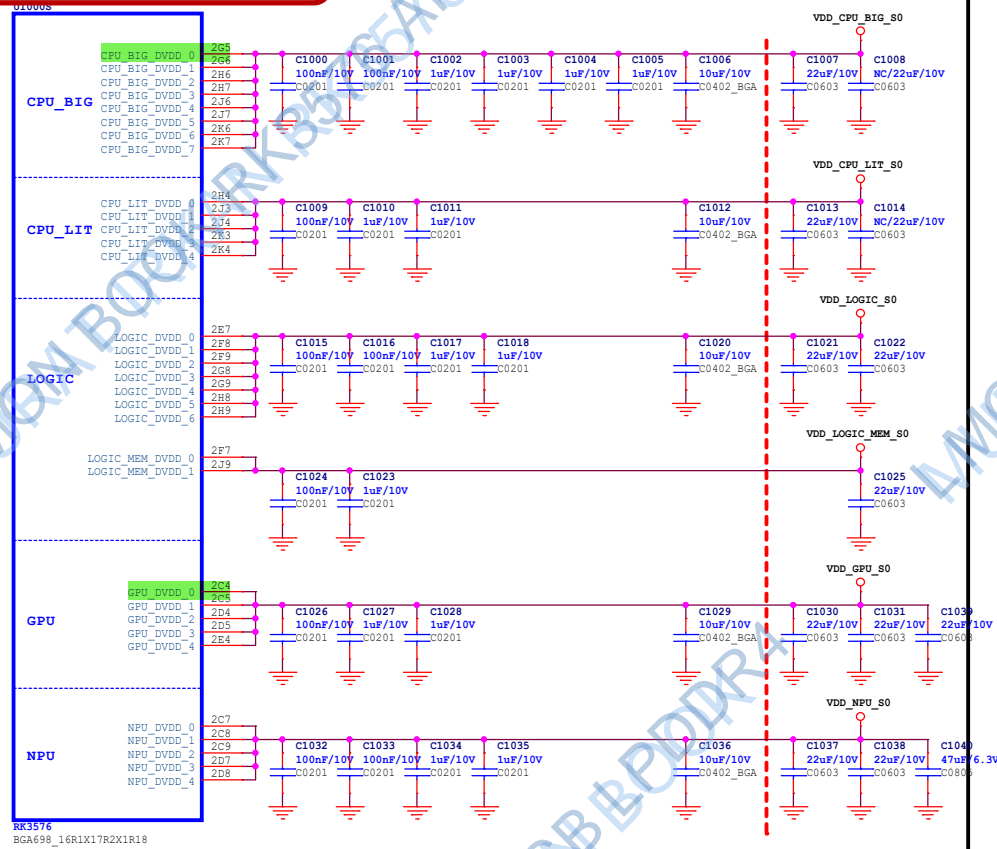


RK3576

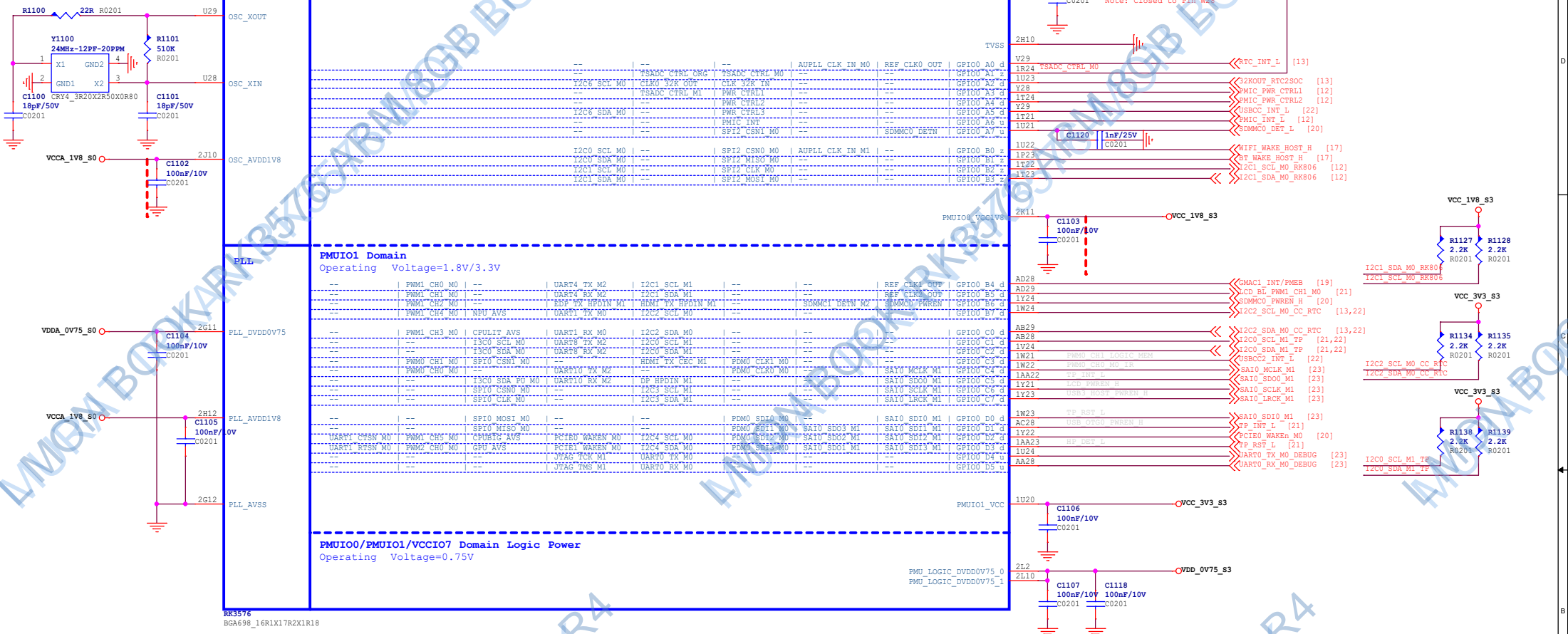


LOKUM-RK3576 ARM LINUX NOTEBOOK

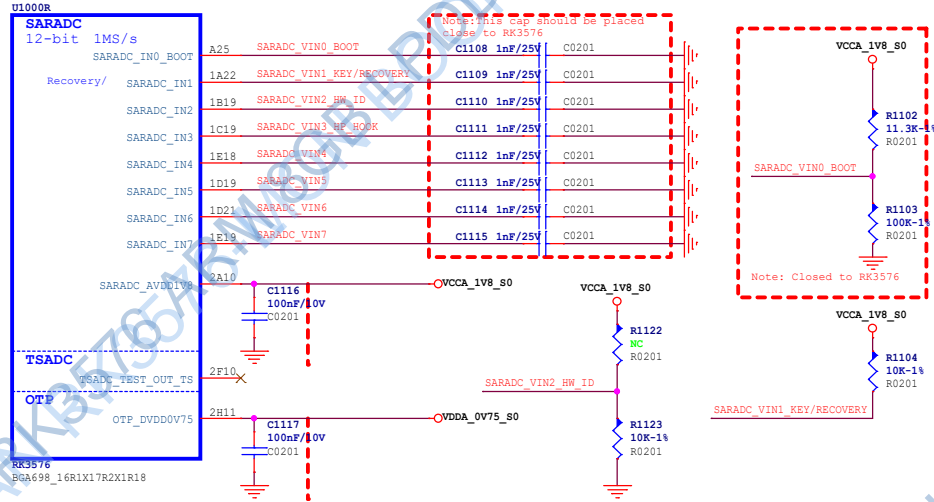
RK3576 ARM CPU



RK3576 E
(PMUIO0/1)




RK3576 R
(SARADC)



BOOT MODE CONFIG

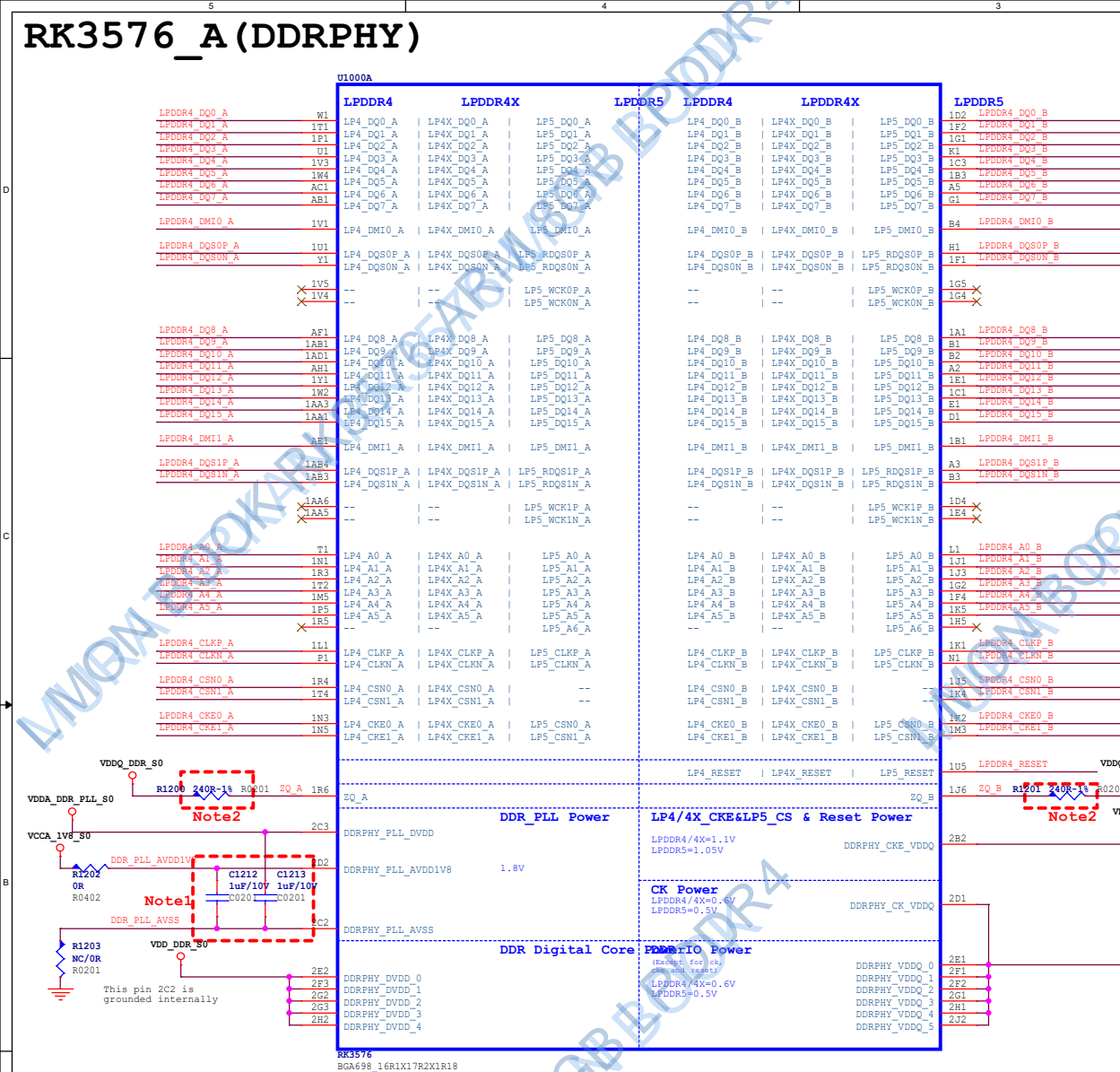
Config Table for SARADC_VINO_BOOT				
Item	Rup	Rdown	ADC Value	Boot Mode
Config1	NC	10K	0	USB (Maskrom mode)
Config2	100K	11.3K	416	FSPI0->USB
Config3	100K	24.9K	816	FSPI1_M0->EMMC->USB
Config4	100K	43K	1231	FSPI1_M1->EMMC->USB
Config5	100K	68K	1658	FSPI0->UFS->USB
Config6	100K	100K	2048	FSPI1_M0->UFS->USB
Config7	68K	100K	2438	UFS->USB
Config8	43K	100K	2864	UFS->SDMMC0->USB
Config9	24.9K	100K	3279	RFU
Config10	11.3K	100K	3679	EMMC->SDMMC0->USB
Config11	10K	NC	4095	EMMC->USB

<https://armsom.org/>

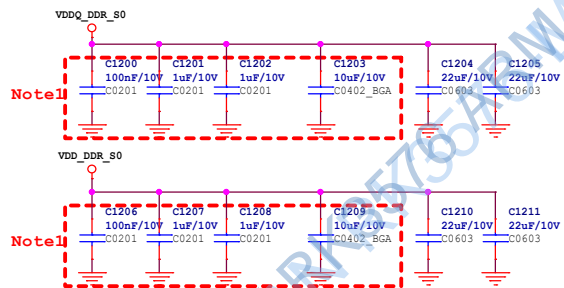
Project:	ArmSoM-Sigs5
File:	RK3576-OSC/PLL/PMUIO/SARADC
Date:	Tuesday, May 21, 2024
Designed by:	Park
Reviewed by:	<Checker>
Rev:	V1.1
Sheet:	3 of 25

Note:
Caps of between dashed red lines and U1000 should be placed under the U1000 package.
Other caps should be placed close to the U1000 package.

5
RK3576 A (DDRPHY)



DDR FILTER



Note:

- (1) Power Sequence: VDD-VDDQ_CKE-VDDQ
(2) Hold power of DDRPHY_CKE_VDDQ during retention times.

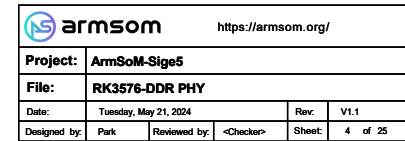
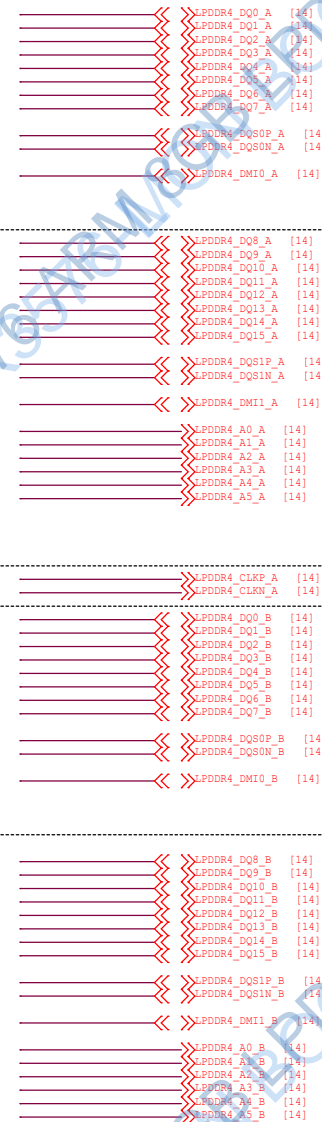
Note1:

■ Caps in the red line dotted box
■ should be placed under the U1000 package

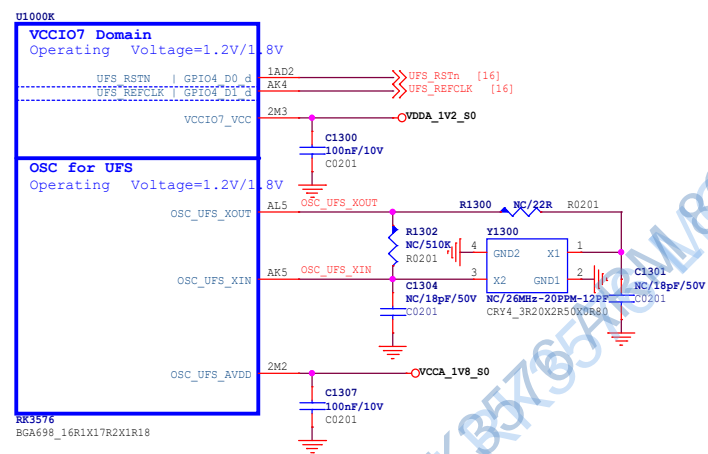
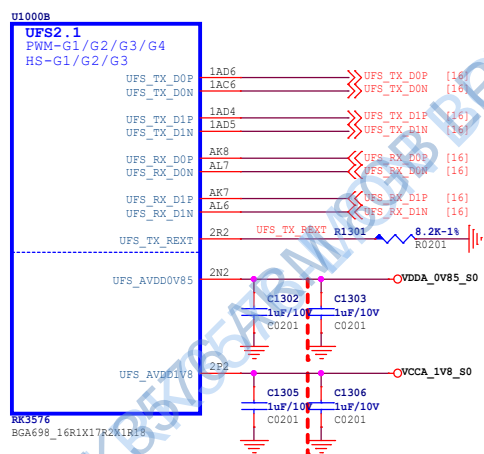
Note2:

Resistors in the red line dotted box should be placed under the U1000 package

LPDDR4 Signal



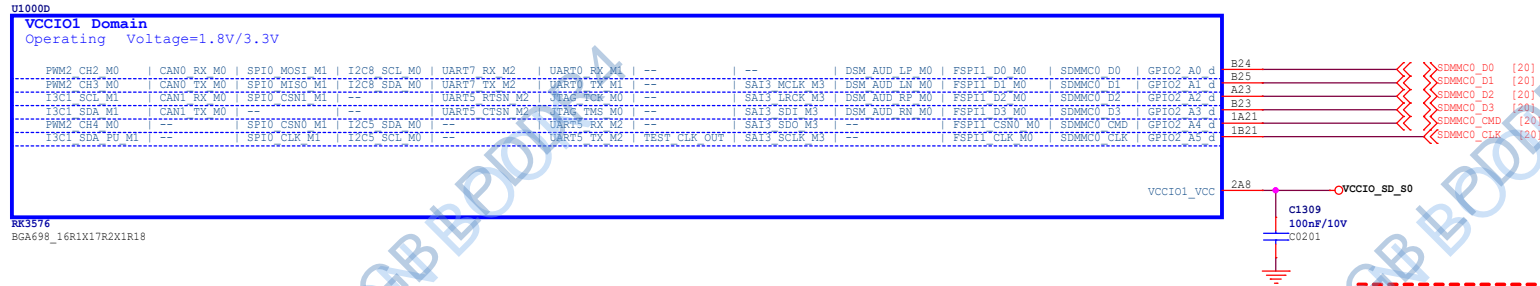
RK3576 B (UFS2.1)



RK3576 C (VCCIO0)



RK3576 D (VCCIO1)



Note:
Caps of between dashed red lines and U1000 should be placed under the U1000 package.
Other caps should be placed close to the U1000 package

RK3576 L (USB3/DP)

U1000L

USB3 OTG0/DP1.4 Alt
USB:USB3.2 Gen1x1 OTG0
DP :RBR/HBR/HBR2/HBR3

-- | DP_TX_AUXP
-- | DP_TX_AUXN
USB3_OTG0_SSRX1P | DP_TX_D0P
USB3_OTG0_SSRX1N | DP_TX_D0N
USB3_OTG0_SSTX1P | DP_TX_D1P
USB3_OTG0_SSTX1N | DP_TX_D1N
USB3_OTG0_SSRX2P | DP_TX_D2P
USB3_OTG0_SSRX2N | DP_TX_D2N
USB3_OTG0_SSTX2P | DP_TX_D3P
USB3_OTG0_SSTX2N | DP_TX_D3N

USB3_OTG0_REXT | DP_TX_REXT

USB3_OTG0_DP_TX_AVDD0V85
USB3_OTG0_DP_TX_DVDD0V85

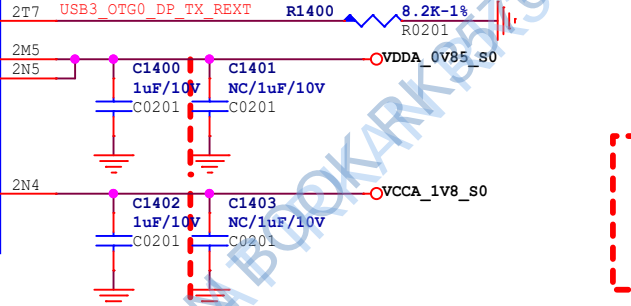
USB3_OTG0_DP_TX_AVDD1V8

RK3576

BGA698_16R1X17R2X1R18

Support:
Type-C With Displayport Alternate Mode

2T2 >>> DP_TX_AUXP [22]
2T3 >>> DP_TX_AUXN [22]
AK10 >>> USB3_OTG0_SSRX1P/DP_TX_D0P [22]
AL10 >>> USB3_OTG0_SSRX1N/DP_TX_D0N [22]
AK11 >>> USB3_OTG0_SSTX1P/DP_TX_D1P [22]
AL11 >>> USB3_OTG0_SSTX1N/DP_TX_D1N [22]
AK12 >>> USB3_OTG0_SSRX2P/DP_TX_D2P [22]
AL12 >>> USB3_OTG0_SSRX2N/DP_TX_D2N [22]
AK13 >>> USB3_OTG0_SSTX2P/DP_TX_D3P [22]
AL13 >>> USB3_OTG0_SSTX2N/DP_TX_D3N [22]



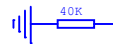
Note:

Caps of between dashed red lines and U1000 should be placed under the U1000 package. Other caps should be placed close to the U1000 package.

RK3576 M (USB2)

U1000M

USB2 OTG0
OTG/HOST/DEVICE
HS/FS/LS Download Port



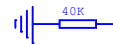
USB2_OTG0_DP
USB2_OTG0_DM

USB2_OTG0_ID

USB2_OTG0_VBUSDET

USB2_OTG0_REXT

USB2 OTG1
OTG/HOST/DEVICE
HS/FS/LS



USB2_OTG1_DP
USB2_OTG1_DM

USB2_OTG1_ID

USB2_OTG1_VBUSDET

USB2_OTG1_REXT

USB2_OTG_DVDD0V75

USB2_OTG_AVDD1V8

USB2_OTG_AVDD3V3

RK3576

BGA698_16R1X17R2X1R18

AK9 >>> USB2_OTG0_DP [22]
AL9 >>> USB2_OTG0_DM [22]

2R6 USB2_OTG0_IDNote: There is an internal pull-up resistor connected to 1.8V

2P3 >>> USB2_OTG0_VBUSDET [22]

2T4 >>> USB2_HOST1_DP [22]
2T5 >>> USB2_HOST1_DM [22]

2T9 USB2_OTG1_ID

2T10 USB2_OTG1_VBUSDET

2U8 USB2_OTG1_REXT

2P5 >>> VDDA_0V75_S0

2P4 >>> VCCA_1V8_S0

2P7 >>> VCC_3V3_S0

2P4 >>> VCCA_1V8_S0

2P7 >>> VCC_3V3_S0

2P7 >>> VCC_3V3_S0


2P7 >>> VCC_3V3_S0

2P7 >>> VCC_3V3_S0

2P7 >>> VCC_3V3_S0

2P7 >>> VCC_3V3_S0

The USB2 OTG1 function cannot be used, if the PCIe1 or SATA1 function of Combo PHY1 is selected

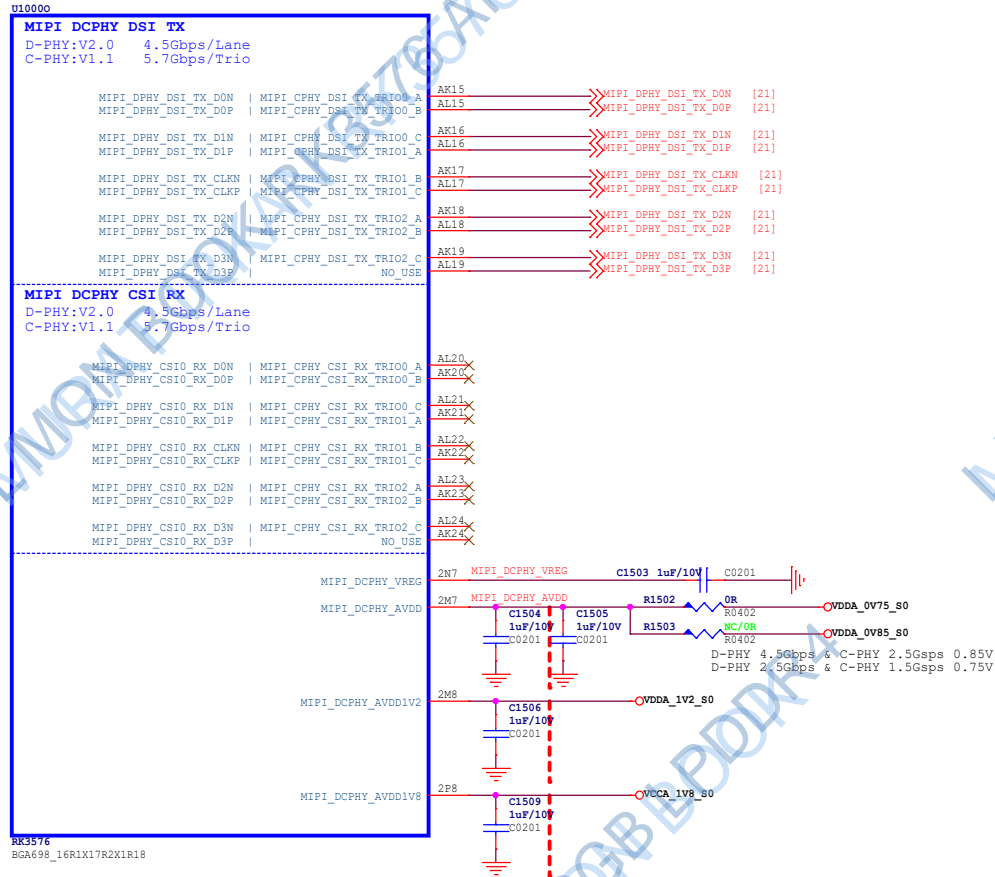


armsom

<https://armsom.org/>

Project:	ArmSoM-Sig5		
File:	RK3576-TypeC/USB		
Date:	Wednesday, May 22, 2024		Rev: V1.1
Designed by:	Park	Reviewed by: <Checker>	Sheet: 6 of 25

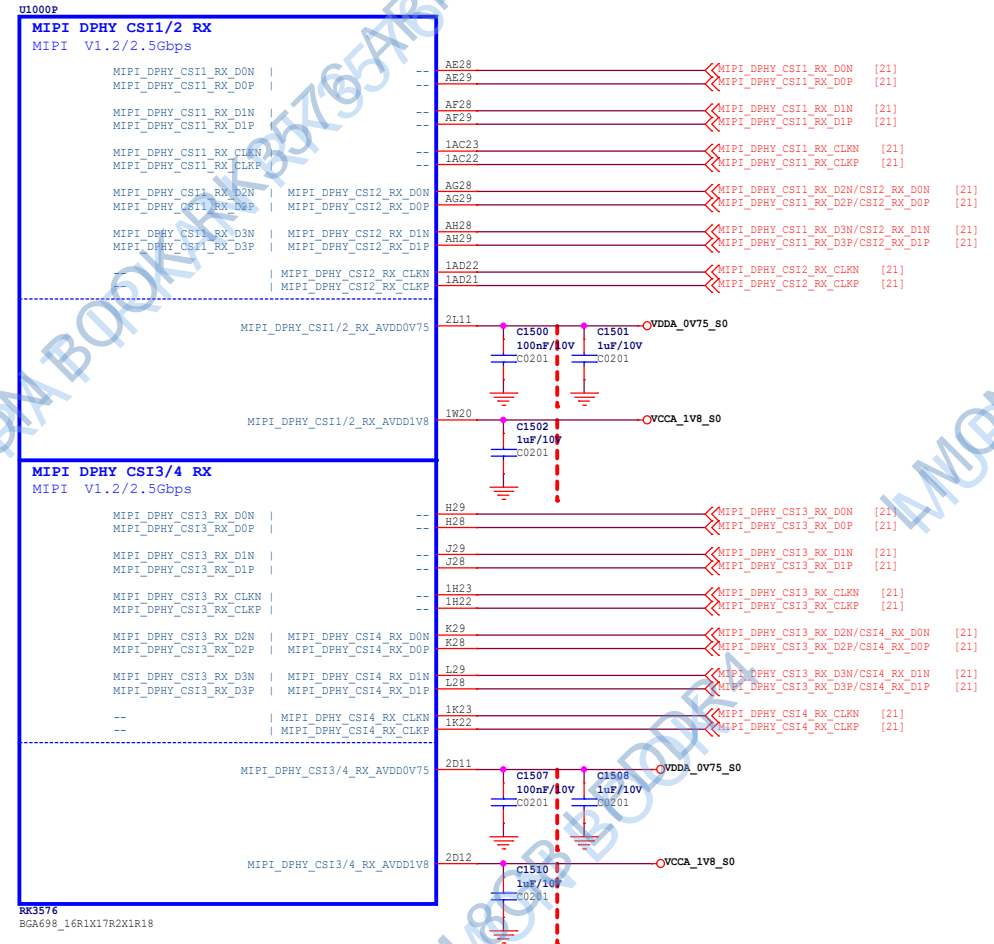
RK3576_O (MIPI DCPHY)



Note:


Caps of between dashed red lines and U1000 should be placed under the U1000 package.
Other caps should be placed close to the U1000 package

RK3576_P (MIPI DPHY CSI RX)



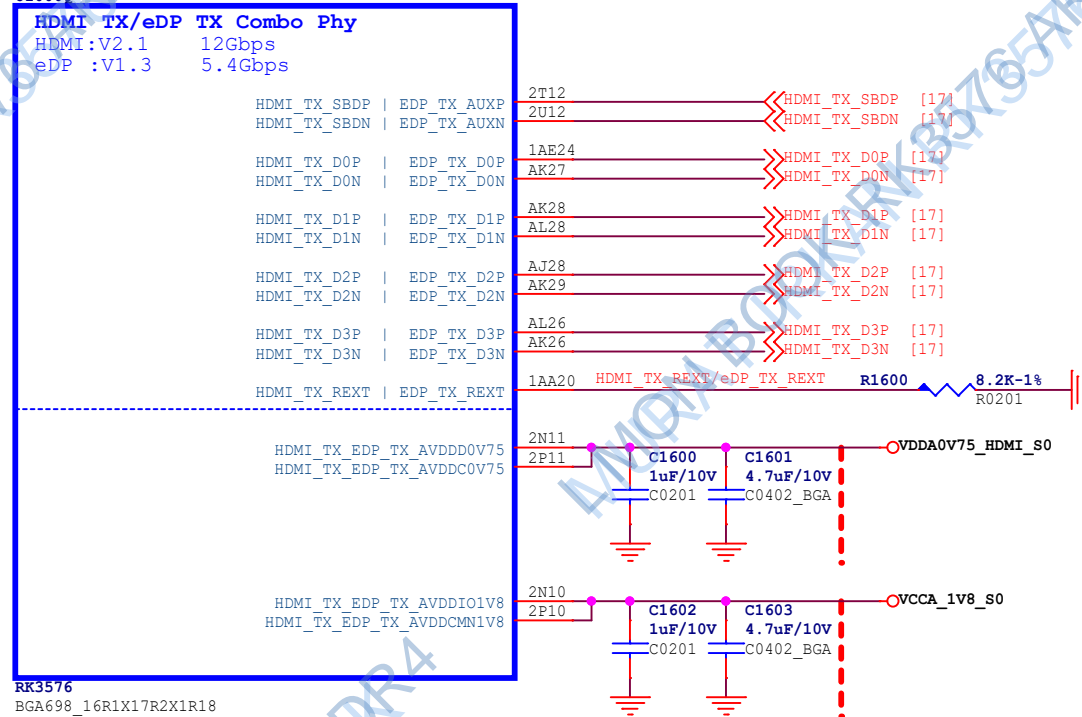
Note:

Caps of between dashed red lines and U1000 should be placed under the U1000 package.
Other caps should be placed close to the U1000 package

		https://armsom.org/	
Project: ArmSoM-Sigs5			
File: RK3576-MIPI DSI/CSI			
Date: Tuesday, May 21, 2024		Rev: V1.1	
Designed by: Park	Reviewed by: <Checker>	Sheet: 7	of 25

RK3576_Q (HDMI/eDP)

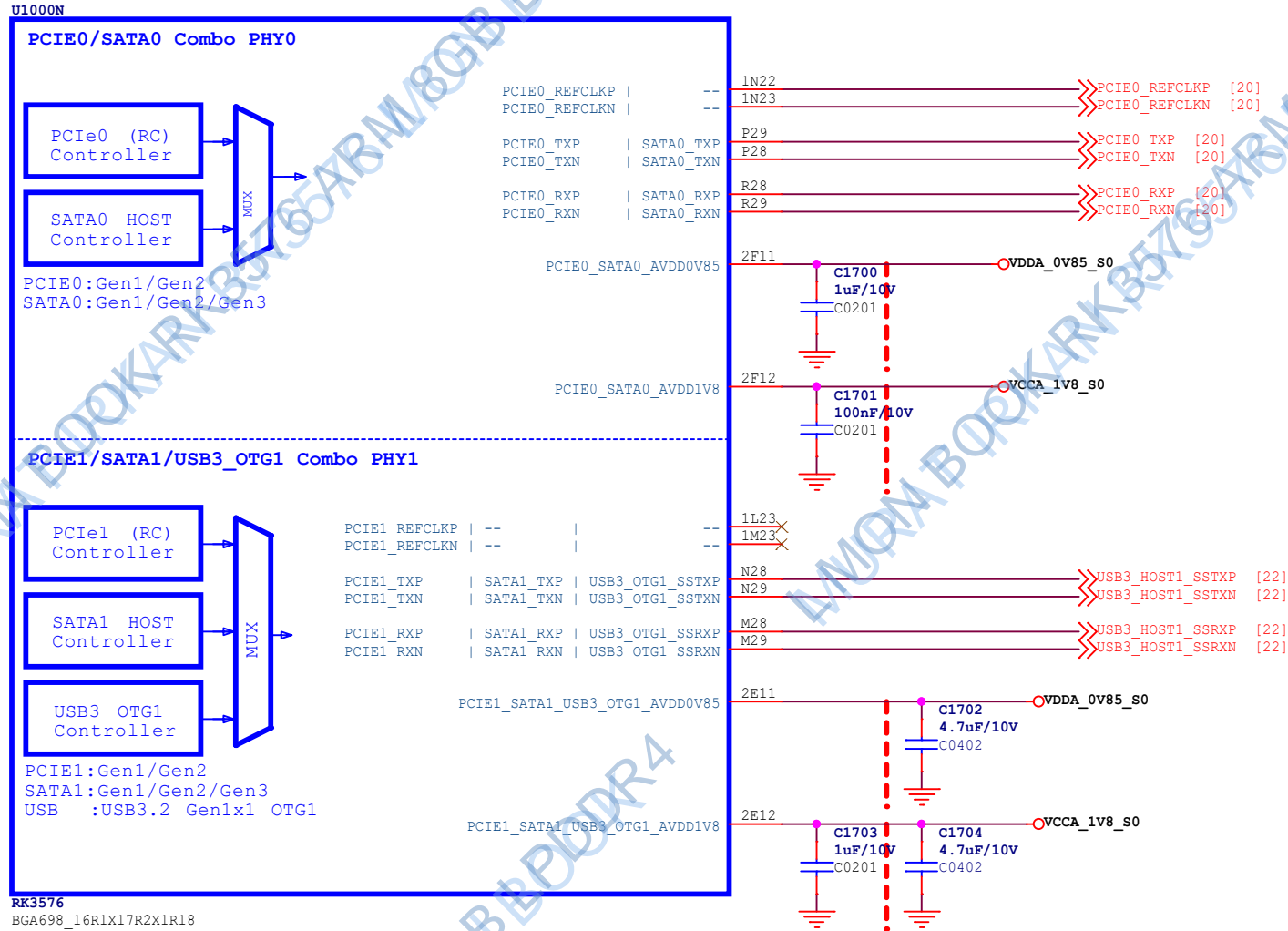
Note:
HDMI 2.1 supports up to 4Kx2K@120Hz
U10000



Note:


Caps of between dashed red lines and U1000 should be placed under the U1000 package. Other caps should be placed close to the U1000 package

RK3576_N (PCIe/SATA/USB3)

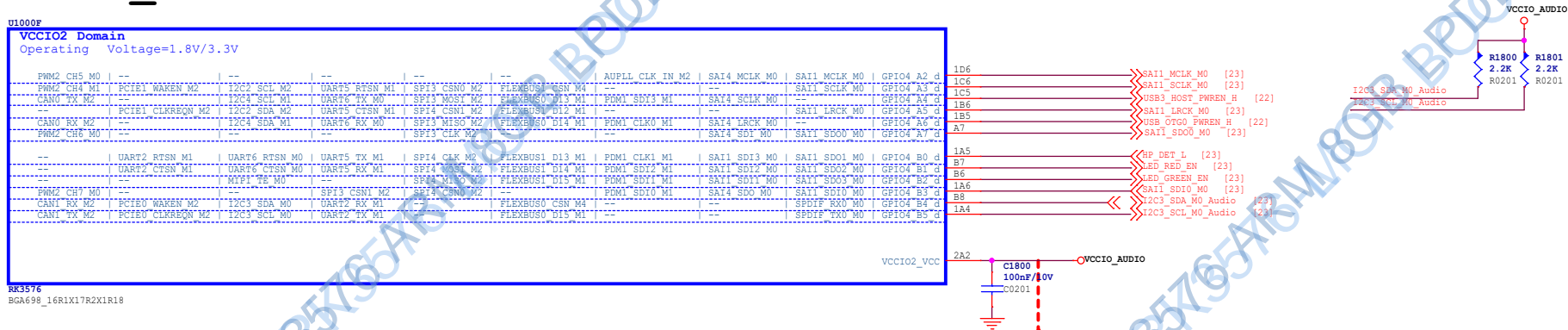


Note:

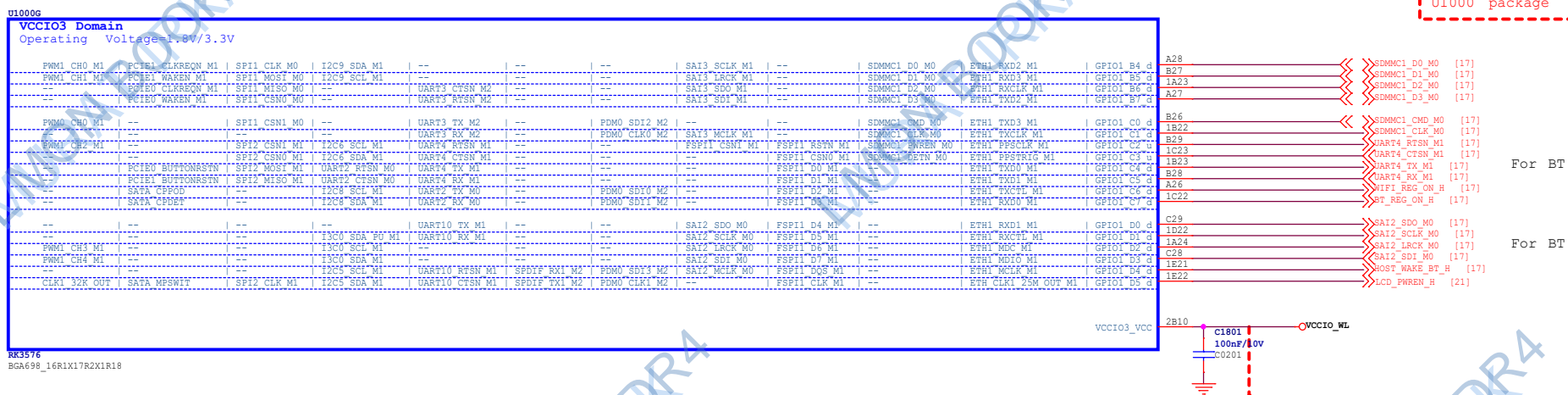
Caps of between dashed red lines and U1000 should be placed under the U1000 package. Other caps should be placed close to the U1000 package

 armsom		https://armsom.org/	
Project:	ArmSoM-Sige5		
File:	RK3576-PCIe/SATA/USB3		
Date:	Tuesday, May 21, 2024		Rev: V1.1
Designed by:	Park	Reviewed by: <Checker>	Sheet: 9 of 25

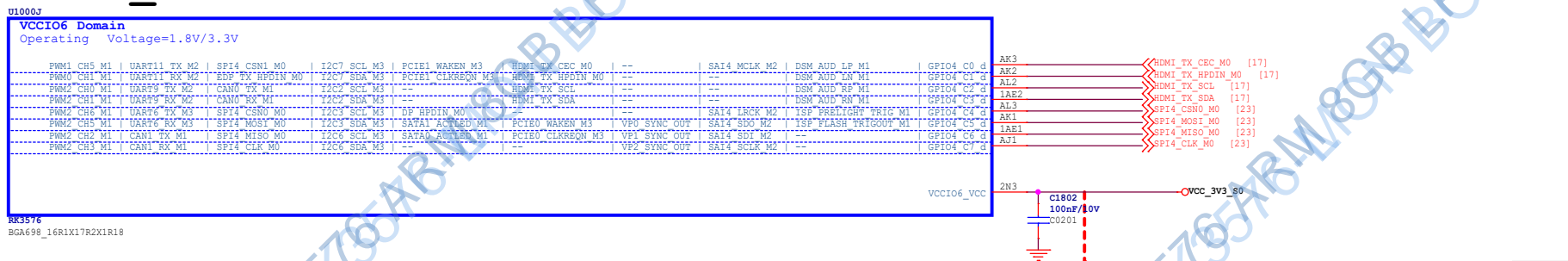
RK3576_F (VCCIO2)



RK3576 G (VCCIO3)



RK3576 J (VCCIO6)



RK3576_H (VCCIO4)

U1000H

VCCIO4 Domain
Operating Voltage=1.8V/3.3V

[illegible]

RK3576 I (VCCIO5)

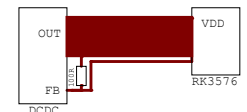
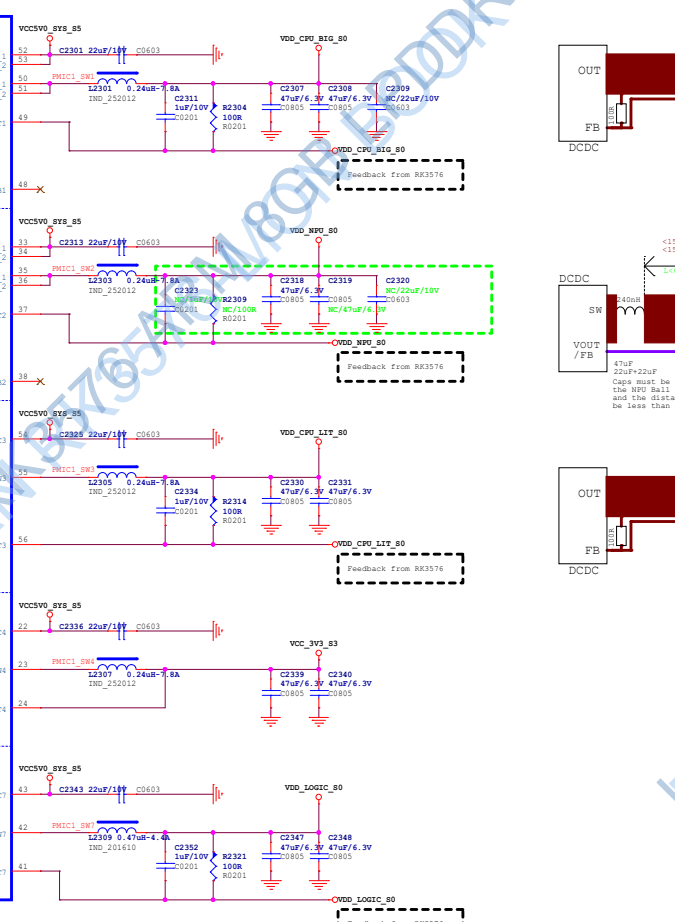
VCCI05 Domain
Operating Voltage=1.8V/3.3V

[illegible]

```

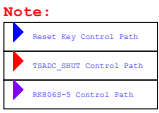
[3] 12C1_SDA_M0_RK804
[3] 12C1_SCL_M0_RK804
[3] PMIC_FWR_CTL0
[3] PMIC_FWR_CTL0
[3] PMIC_INT_0
[3,23] RESET_0
[13] PMIC_EXT_EN_OUT
[23] PWRON_0

```



```
Note:
I2C Mode:CS(pin18) connected to VCCA(pin21);
SPI Mode(Def):CS(pin18) floating or connected to GND
```

```
Note:
I2C Mode:CS(pin18) connected to VCCA(pin21);
SPI Mode(Def):CS(pin18) floating or connected to GND
```

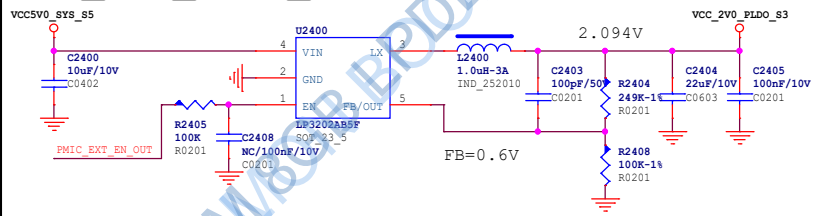


PMIC RK806S-5 LDO

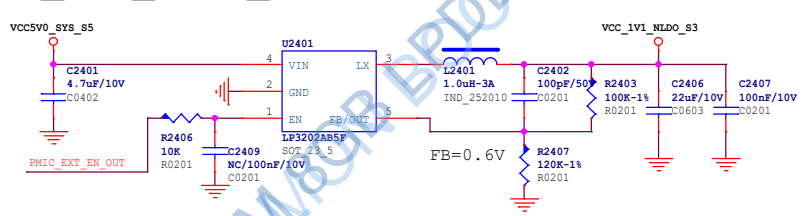


Note:
The RK806 LDO power distribution of the reference schematics is only suitable for the interface used in the reference schematics.
If other interface functions are to be added to the reference schematics, the RK806 LDO distribution needs to be re-evaluated, otherwise the added

VCC_2V0_PLDO_S3

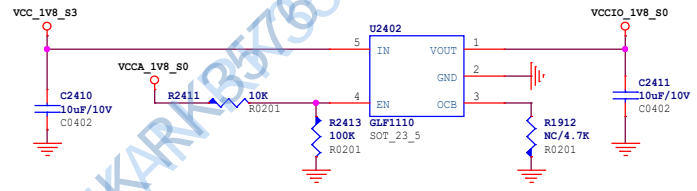


VCC_1V1_NLDO_S3



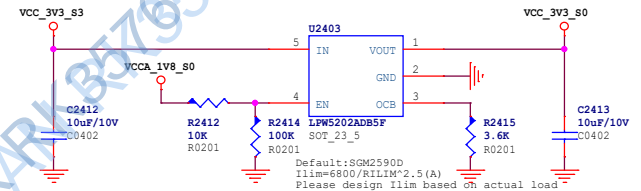
VCCIO_1V8_S0

Note: Need quick output discharge

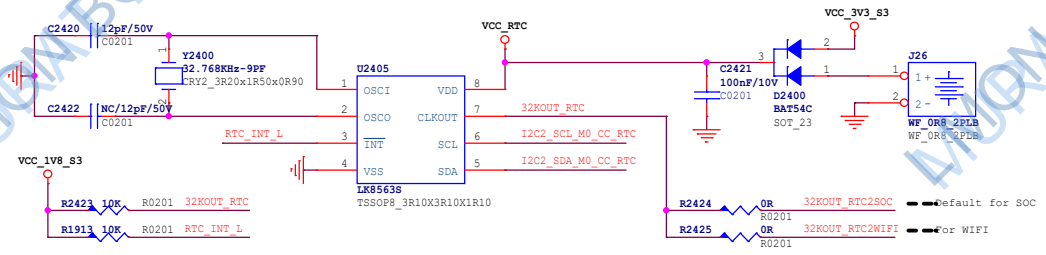


VCC_3V3_S0

Note: Need quick output discharge



RTC

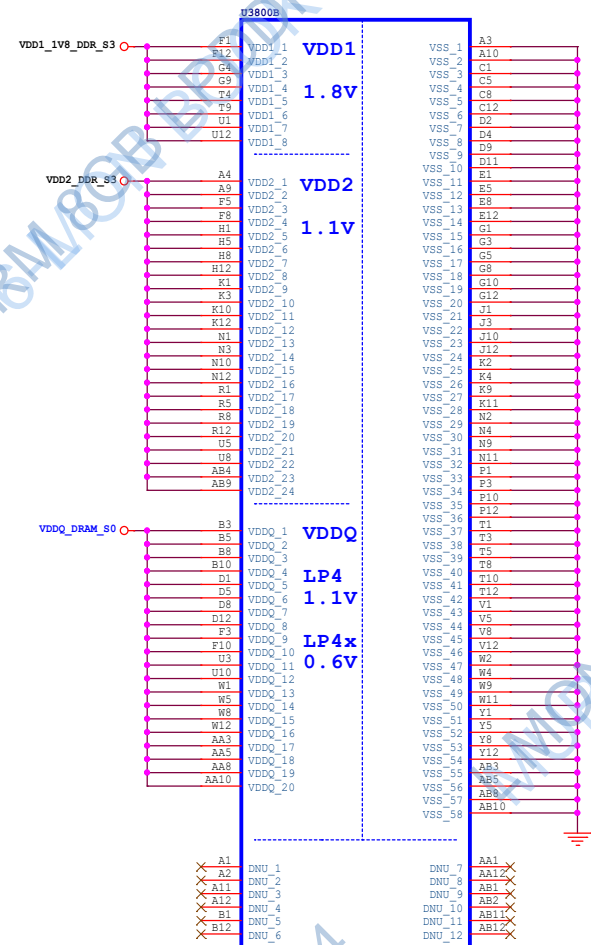
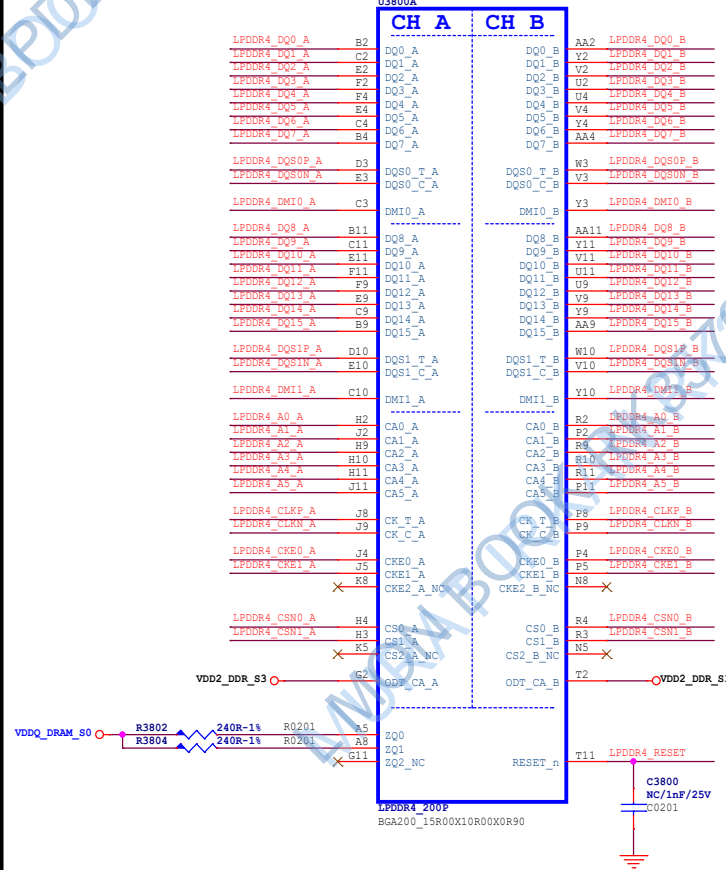


VDD_LOGIC_MEM EXT (Option for test)



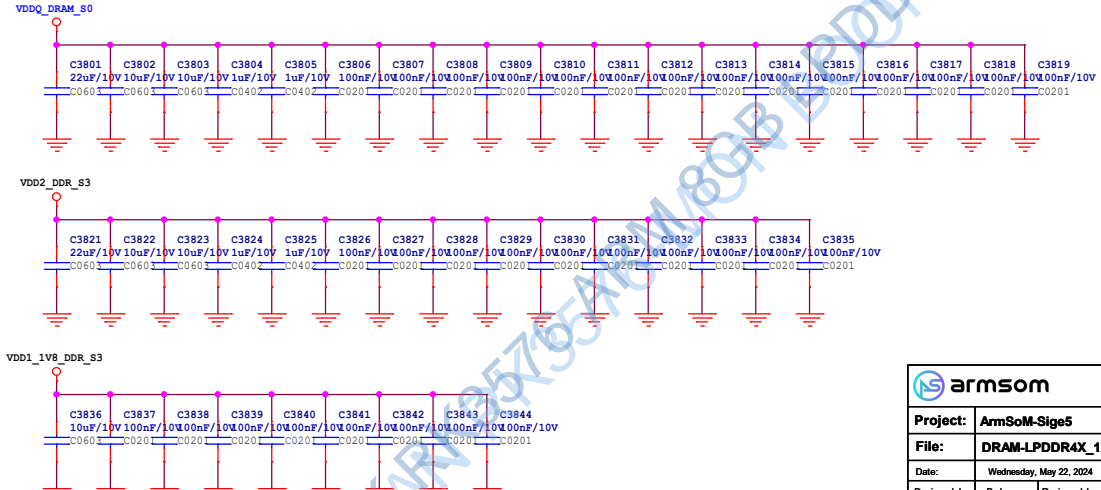
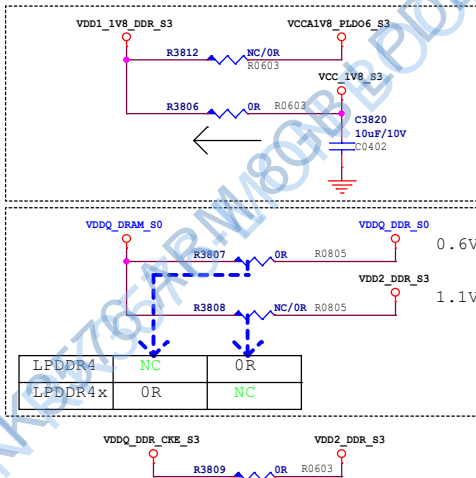
armsom		https://armsom.org/	
Project:	ArmSoM-Sig5		
File:	Power-Ext Discrete/RTC		
Date:	Wednesday, May 22, 2024	Rev:	V1.1
Designed by:	Park	Reviewed by:	<Checker>
Sheet:	13 of 25		


LPDDR4/4X



Note:
Sequence: VDD1-VDD2-VDDQ

	LPDDR4	LPDDR4X
VDD1:	1.70-1.95	1.70-1.95
VDD2:	1.06-1.17	1.06-1.17
VDDQ:	1.06-1.17	0.57-0.65



 armsom		https://armsom.org/	
Project:	ArmSoM-Sig5		
File:	DRAM-LPDDR4X_1X32bit_200P		
Date:	Wednesday, May 22, 2024		Rev: V1.1
Designed by:	Park	Reviewed by:	<Checker>
		Sheet:	14 of 25

eMMC FLASH

[5] eMMC_D0<<>>
[5] eMMC_D1<<>>
[5] eMMC_D2<<>>
[5] eMMC_D3<<>>
[5] eMMC_D4<<>>
[5] eMMC_D5<<>>
[5] eMMC_D6<<>>
[5] eMMC_D7<<>>

[5] eMMC_CMD<<>>

[5] eMMC_CLKOUT<<>>

[5] eMMC_DATA_STROBE<<>>

[5] eMMC_RSTn<<>>

VCCIO_1V8_S0

R4000
10K
R0201

R4001
NC/10K
R0201

eMMC_D0 A3
eMMC_D1 A4
eMMC_D2 A5
eMMC_D3 B2
eMMC_D4 B3
eMMC_D5 B4
eMMC_D6 B5
eMMC_D7 B6

eMMC_CMD M5

U4000A

DATA0
DATA1
DATA2
DATA3
DATA4
DATA5
DATA6
DATA7

CMD

CLK

Data Strobe

RST_n

VDDi

EMMC B153 2L
BGA153_13RX11R5X0R9_2L

Note:
This cap should be placed
close to the Pin M6 (400mi)

C4003
NC/10K
C0201

eMMC_DATA_STROBE R4004 OR R0201

VCCIO_1V8_S0

R4005
NC/47K
R0201

eMMC_RSTn

Note:
The capacity value depends on the
requirement of eMMC Specification

C4007
2.2uF/10V
C0402

VCCIO_1V8_S0

C4000
100nF/10V
C0201

C4001
100nF/10V
C0201

C4002
4.7uF/10V
C0402

VCC_3V3_S0

C4004
100nF/10V
C0201

C4005
100nF/10V
C0201

C4006
4.7uF/10V
C0402

U4000B


A2 NC2
A8 NC8
A9 NC9
A10 NC10
A11 NC11
A12 NC12
A13 NC13
A14 NC14
B1 NC15
B7 NC21
B8 NC22
B9 NC23
B10 NC24
B11 NC25
B12 NC26
B13 NC27
B14 NC28
C1 NC29
C3 NC31
C7 NC35
C8 NC36
C9 NC37
C10 NC38
C11 NC39
C12 NC40
C13 NC41
C14 NC42
D1 NC43
D2 NC44
D3 NC45
D4 NC46
D12 NC54
D13 NC55
D14 NC56
E1 NC57
E2 NC58
E3 NC59
E12 NC68
E13 NC69
E14 NC70
F1 NC71
F2 NC72
F3 NC73
F12 NC82
F13 NC83
F14 NC84
G2 NC85
G12 NC86
G13 NC96
G14 NC98

A7 RFU1
E5 RFU2
E8 RFU3
G3 RFU4
G10 RFU5

NC196
P13
NC195
P12
NC194
P11
NC193
P9
NC191
P8
NC190
P2
NC184
P1
NC183
N14
NC182
N13
NC181
N12
NC180
N11
NC179
N10
NC178
N9
NC177
N8
NC176
N7
NC175
N6
NC174
N3
NC171
N1
NC169
M14
NC168
M13
NC167
M12
NC166
M11
NC165
M10
NC164
M9
NC163
M8
NC162
M7
NC161
M3
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M2
NC156
M1
NC155
L14
NC154
L13
NC153
L12
NC152
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NC139
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NC129
K2
NC128
K1
NC127
J14
NC126
J13
NC125
J12
NC124
J3
NC115
J2
NC114
J1
NC113
H14
NC112
H13
NC111
H12
NC110
H3
NC101
H2
NC100
H1
NC99

P10
RFU9
RFU8
K7
RFU7
K6

EMMC B153 2L
BGA153_13RX11R5X0R9_2L



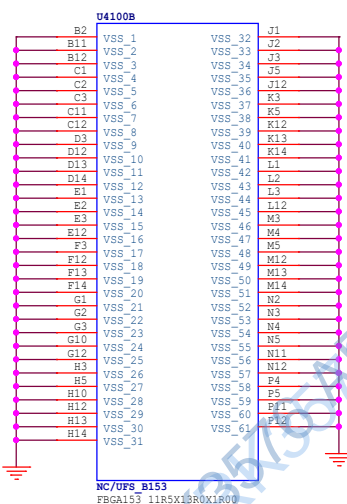
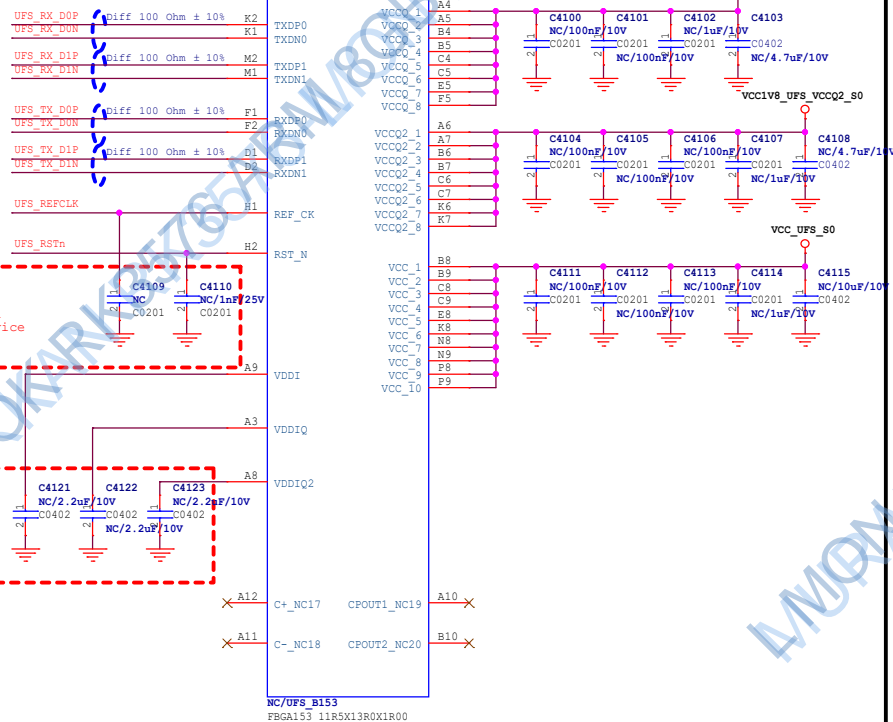
armsom

<https://armsom.org/>

Project:	ArmSoM-Sig5				
File:	Flash-eMMC				
Date:	Wednesday, May 22, 2024			Rev:	V1.1
Designed by:	Park	Reviewed by:	<Checker>	Sheet:	15 of 25

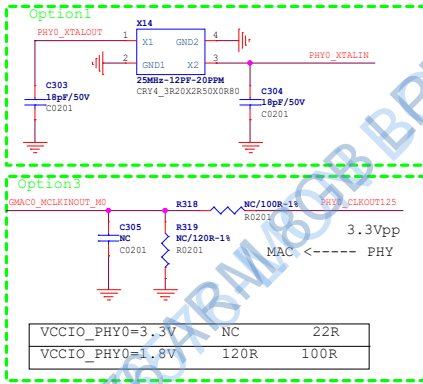
UFS Flash

UFS_TX_D0P
UFS_TX_D0N
UFS_TX_D1P
UFS_TX_D1N
UFS_RX_D0P
UFS_RX_D0N
UFS_RX_D1P
UFS_RX_D1N
(3) UFS_RSTn
UFS_REFCLK

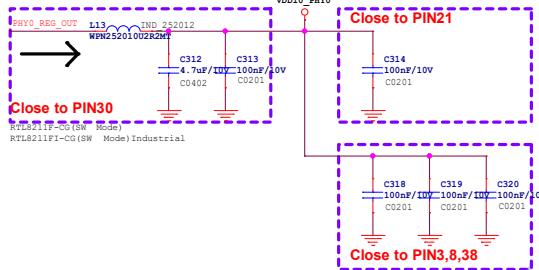
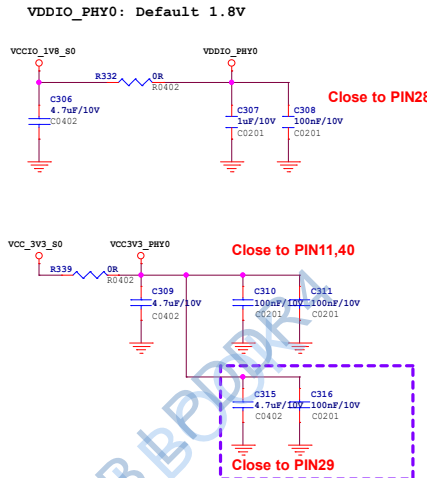
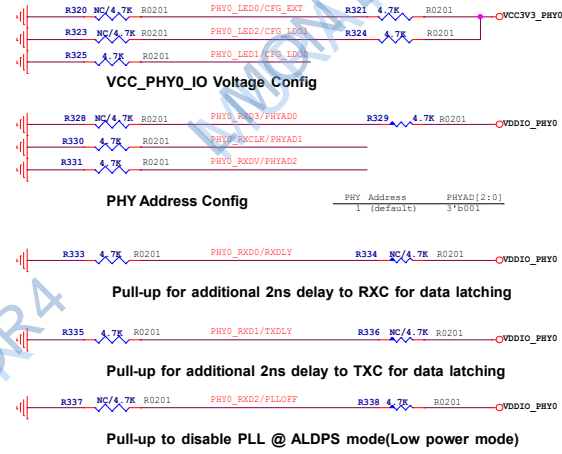
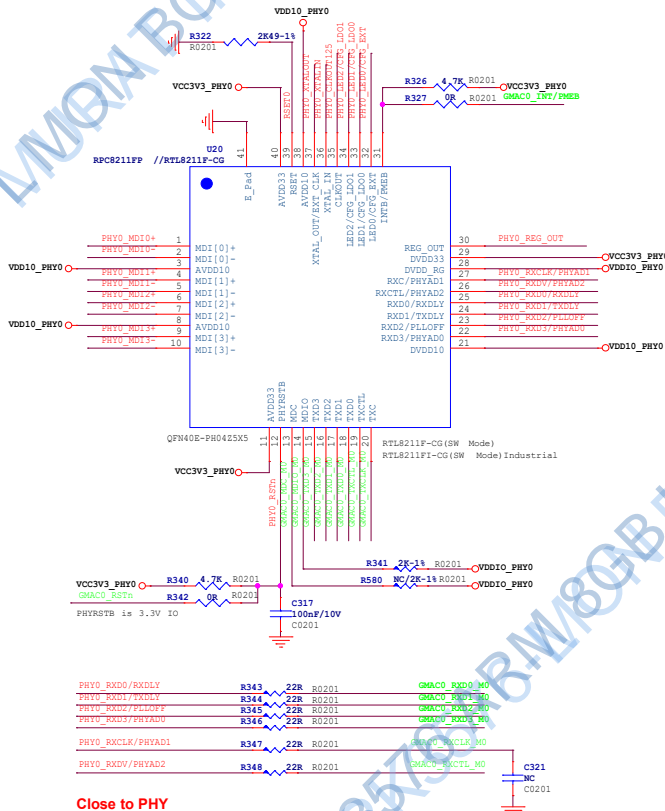
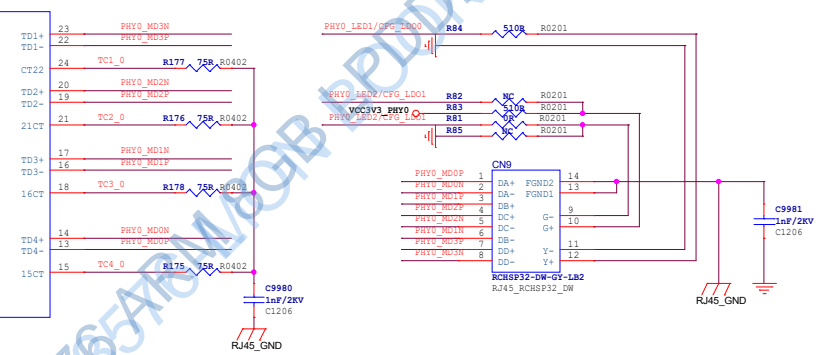
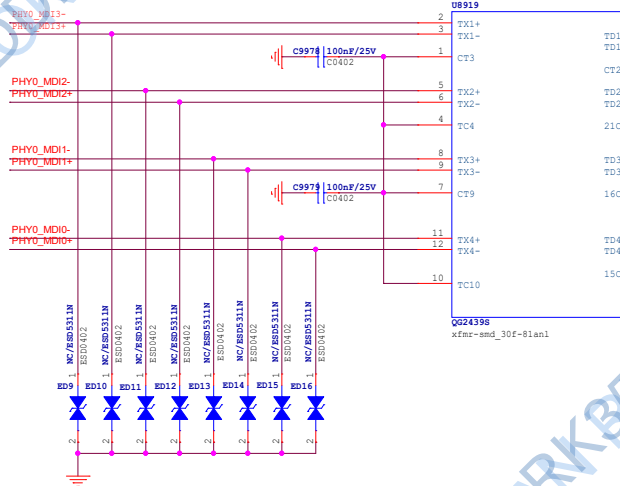


GPHY To JR45

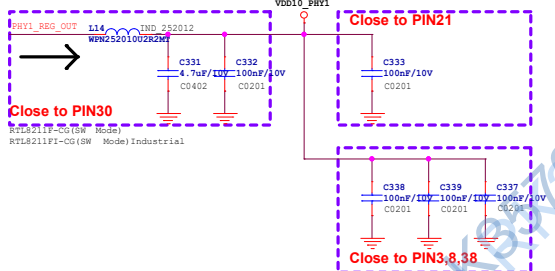
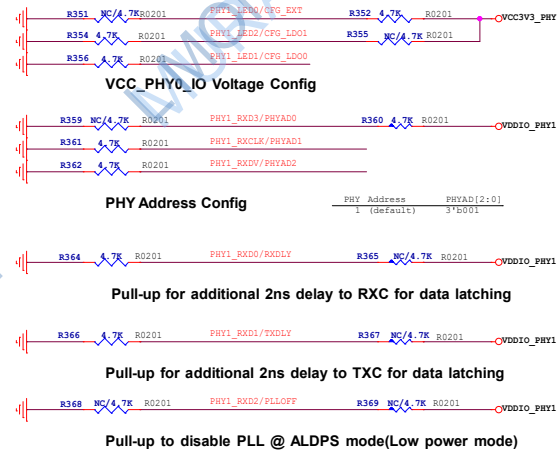
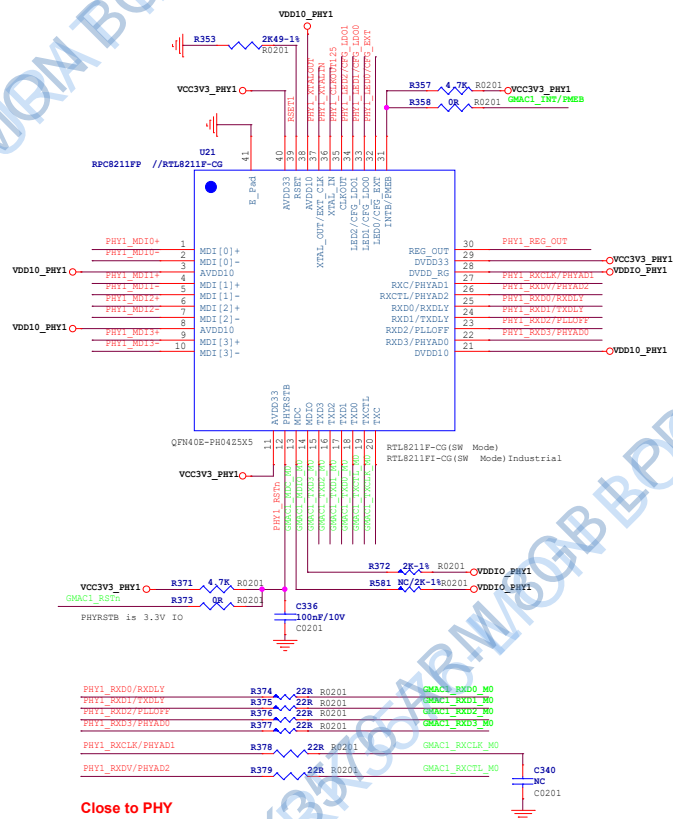
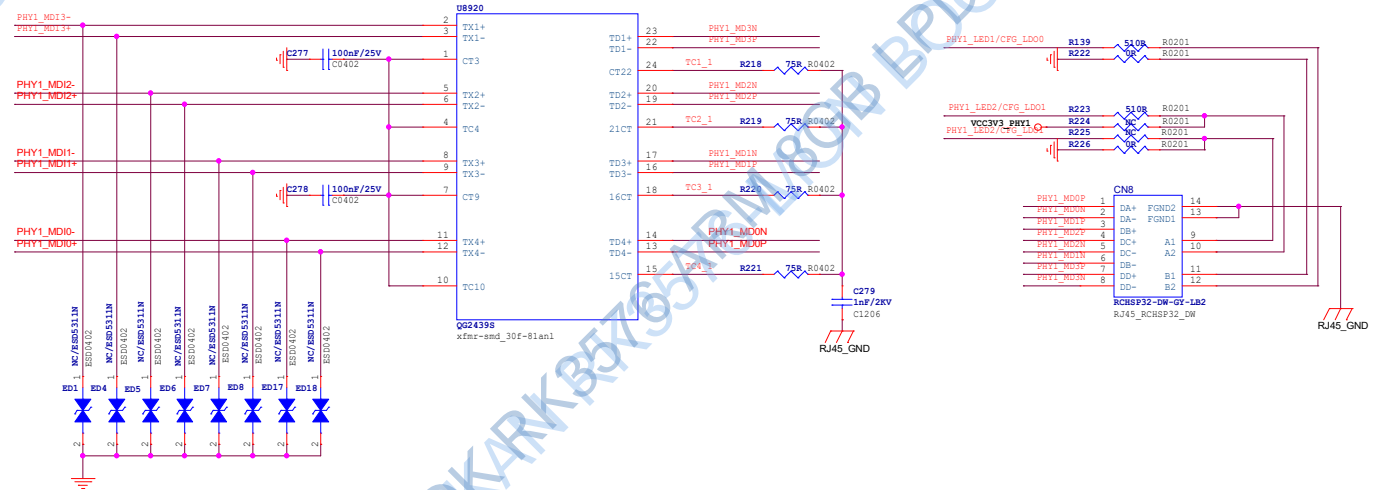
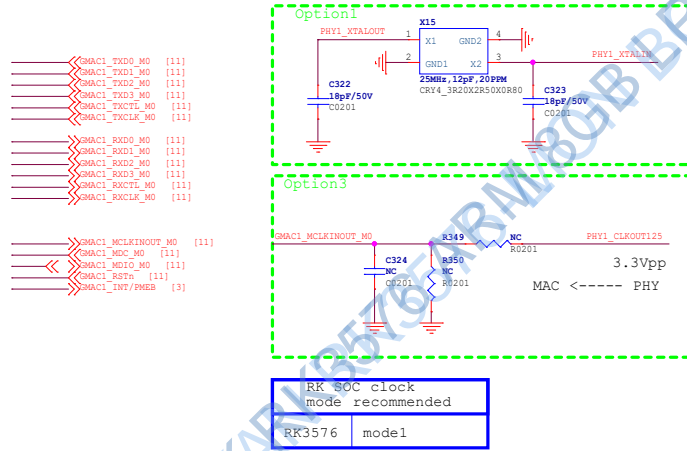
GMACO_TXD0_M0 [11]
GMACO_TXD1_M0 [11]
GMACO_TXD2_M0 [11]
GMACO_TXD3_M0 [11]
GMACO_TXCTL_M0 [11]
GMACO_RXD0_M0 [11]
GMACO_RXD1_M0 [11]
GMACO_RXD2_M0 [11]
GMACO_RXD3_M0 [11]
GMACO_RXCTL_M0 [11]
GMACO_RSTn [11]
GMACO_INT/PWRn [11]



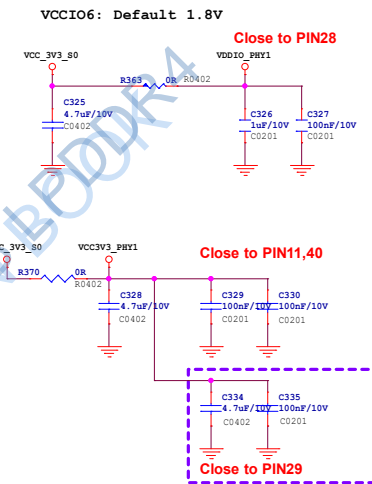
RK SOC clock
mode recommended
RK3576 model



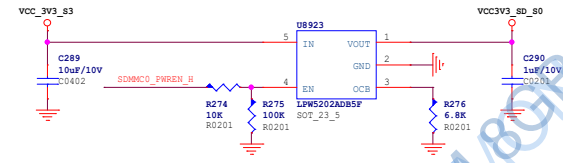
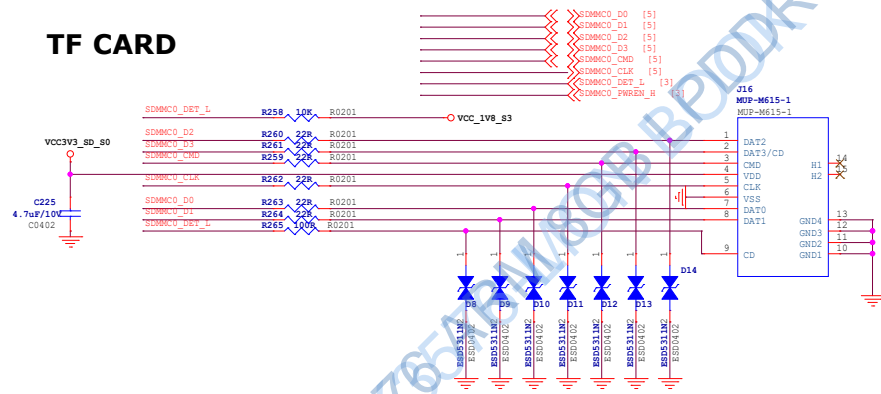
Giga PHY1_WAN



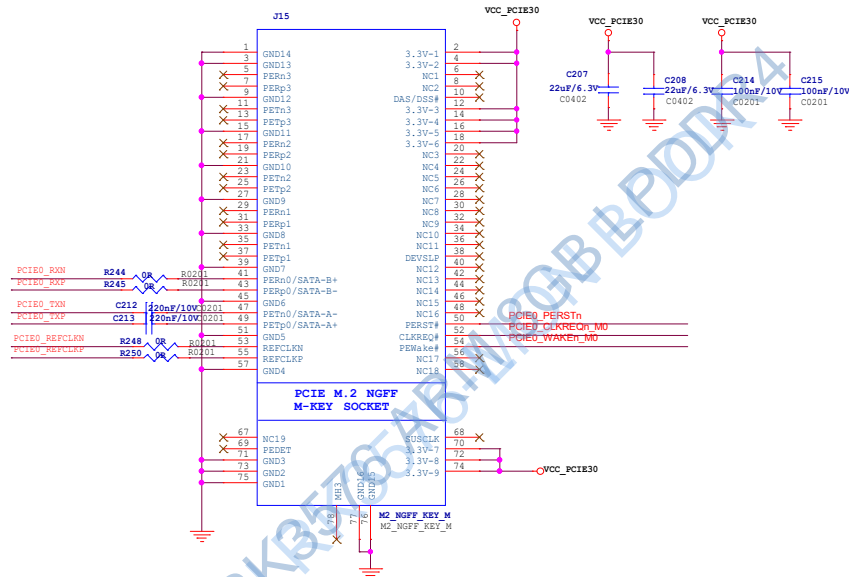
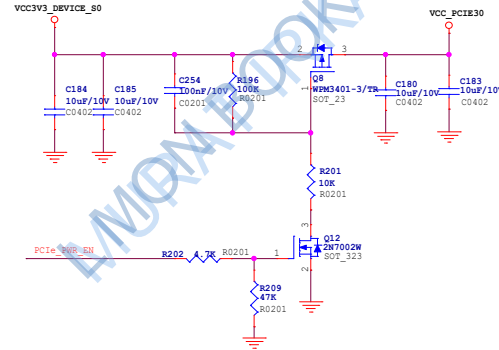
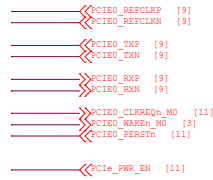
RGMI1 Power Source	CFG EXT	CFG LDO[1:0]	CFG EXT:
External 3.3V(default)	1'b1	2'b00	1:External Power Source for 10 pad. 0:Integrated LDO for 10 pad
External 1.8V	1'b1	2'b10	CFG LDO[1:0] 10:1.8V 00:3.3V
Internal 1.8V	1'b0	2'b10	



TF CARD



M.2_PCIE



```

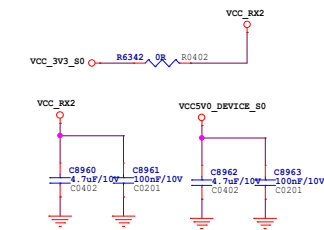
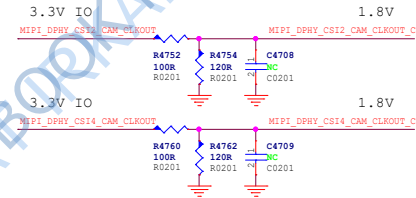
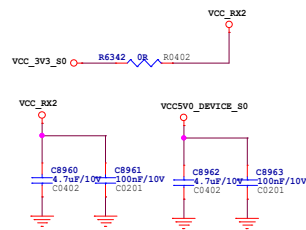
>>>MPLI_DPHY_CS11_RX_DOP_DIN [7]
>>>MPLI_DPHY_CS11_RX_DOP [7]
>>>MPLI_DPHY_CS11_RX_DIN [7]
>>>MPLI_DPHY_CS11_RX_DOP [7]
>>>MPLI_DPHY_CS11_RX_CLKEN [7]
>>>MPLI_DPHY_CS11_RX_CLKMP [7]

>>>MPLI_DPHY_CS11_RX_D2N/CS12_RX_DON [7]
>>>MPLI_DPHY_CS11_RX_D2P/CS12_RX_DOP [7]
>>>MPLI_DPHY_CS11_RX_D2N/CS12_RX_DIN [7]
>>>MPLI_DPHY_CS11_RX_D2P/CS12_RX_DOP [7]
>>>MPLI_DPHY_CS11_RX_D2N/CS12_RX_DOP [7]
>>>MPLI_DPHY_CS11_RX_CLKEN [7]
>>>MPLI_DPHY_CS12_RX_CLKMP [7]

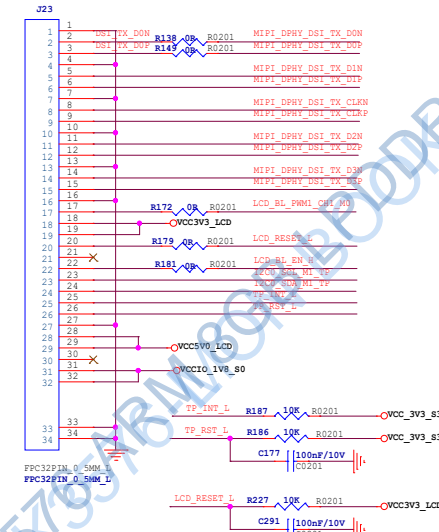
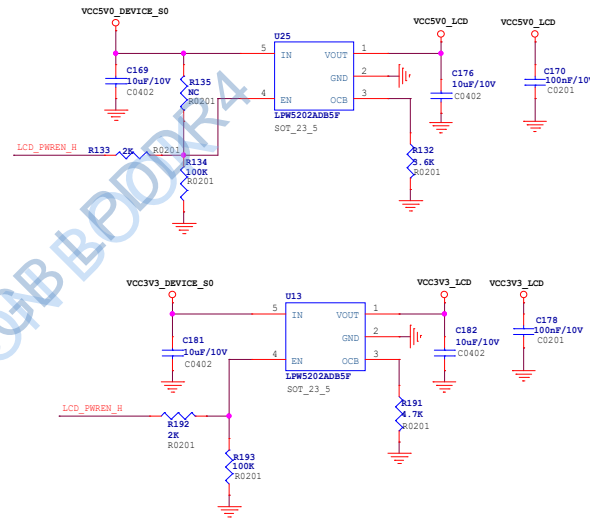
>>>MPLI_DPHY_CS11_CAM_CLKOUT [11]
>>>MPLI_DPHY_CS12_CAM_CLKOUT [11]

>>>C2CS_SDA_M3_MPLI_CS11 [11]
>>>C2CS_SCL_M3_MPLI_CS11 [11]
>>>MPLI_DPHY_CS11_PDN_N [11]
>>>MPLI_DPHY_CS12_PDN_N [11]
>>>MPLI_DPHY_CS11/2_RST [11]

```

[illegible]

⋈	MIPI_DPHY_DSI_TX_D0N	[7]
⋈	MIPI_DPHY_DSI_TX_D0P	[7]
⋈	MIPI_DPHY_DSI_TX_D1N	[7]
⋈	MIPI_DPHY_DSI_TX_D1P	[7]
⋈	MIPI_DPHY_DSI_TX_D2N	[7]
⋈	MIPI_DPHY_DSI_TX_D2P	[7]
⋈	MIPI_DPHY_DSI_TX_D3N	[7]
⋈	MIPI_DPHY_DSI_TX_D3P	[7]
⋈	MIPI_DPHY_DSI_TX_CLKN	[7]
⋈	MIPI_DPHY_DSI_TX_CLKP	[7]
⋈	C2C0_SCL_M1_TP	[3,22]
⋈	C2C0_SDA_M1_TP	[3,22]
⋈	F2C_FWE_L	[3]
⋈	F2C_RST_L	[3]
⋈	C2C0_BL_PWM1_CH1_M0	[3]
⋈	C2C0_PAREN_L	[1]
⋈	C2C0_RESET_L	[1]
⋈	C2C0_BL_EN_R	[1]
⋈	C2C0_BL_EN_L	[1]

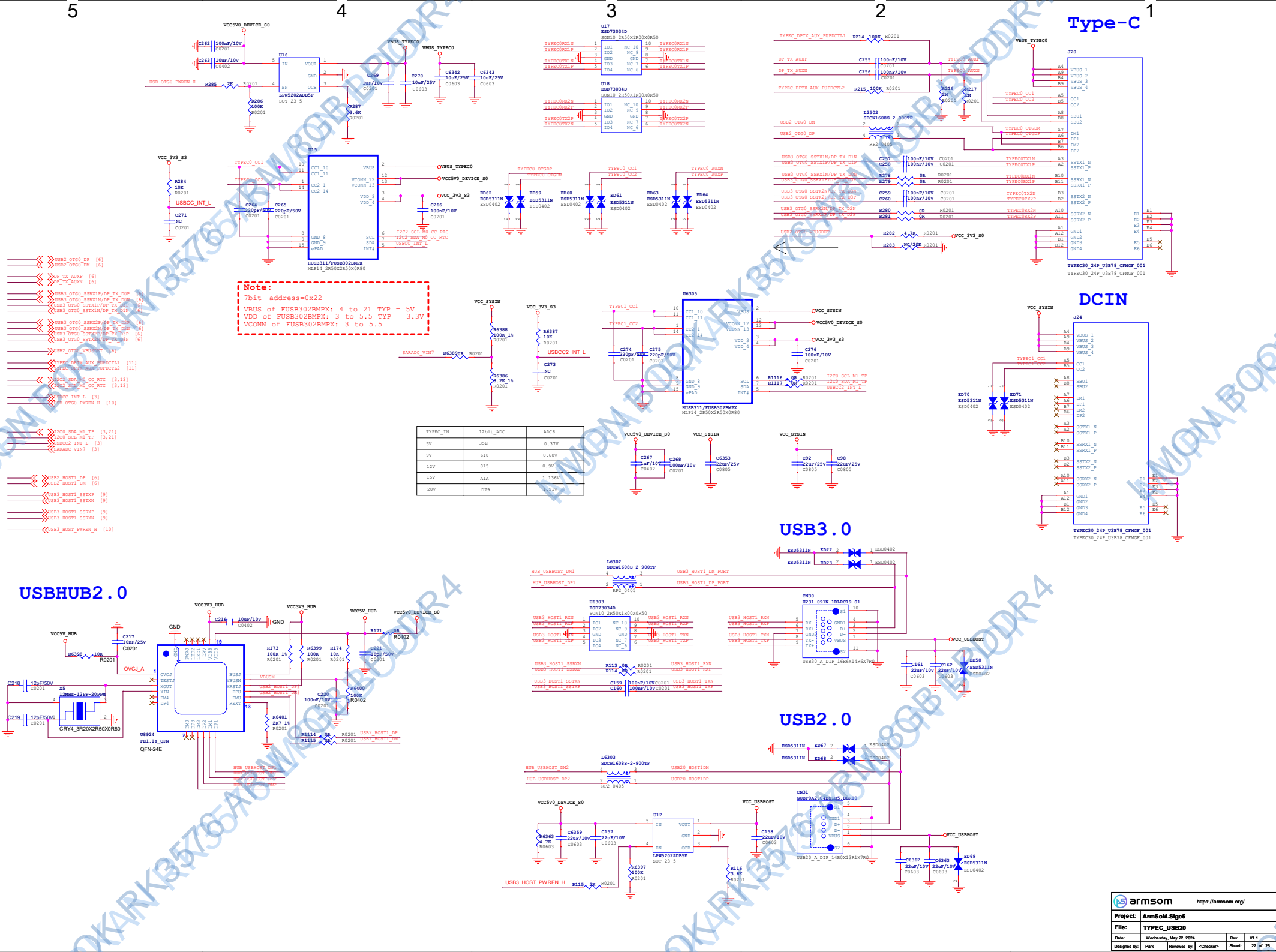


D

C

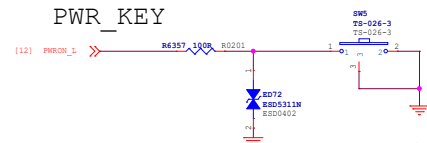
B

A

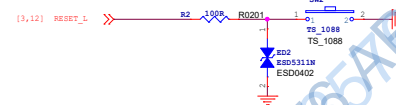


KEY

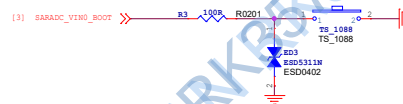
PWR_Key



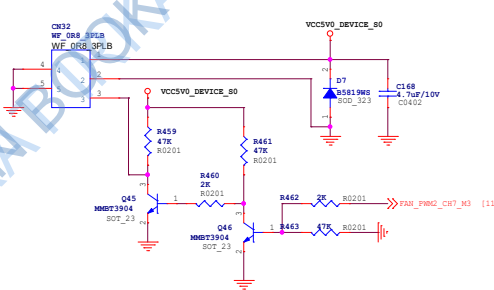
RESET_Key



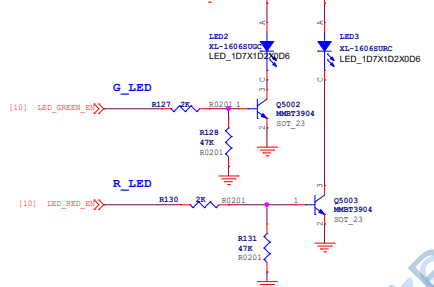
MASKROM_Key



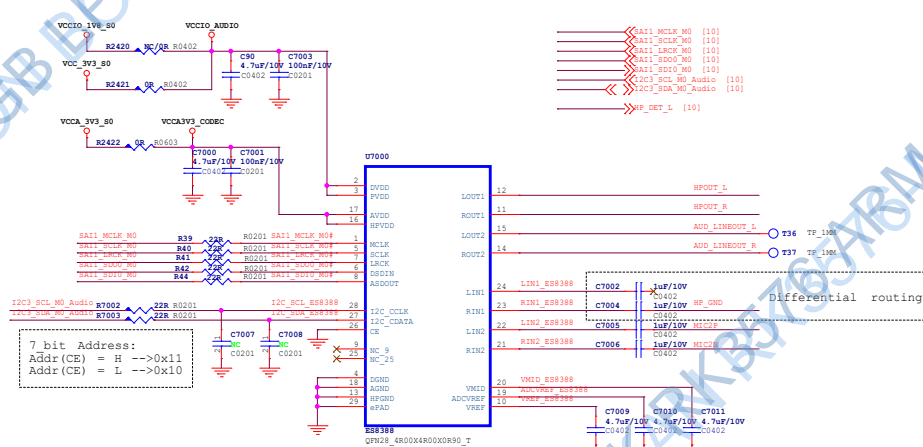
FAN



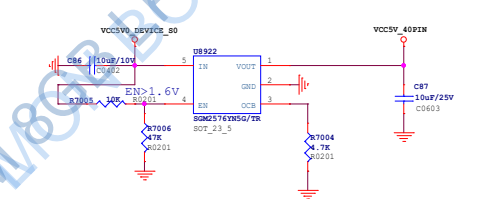
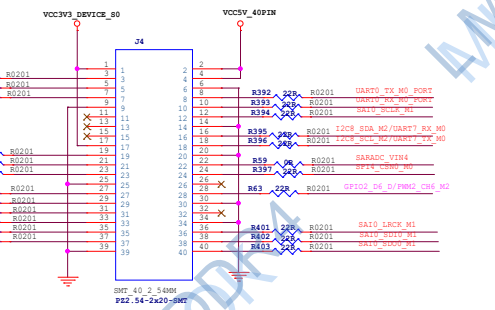
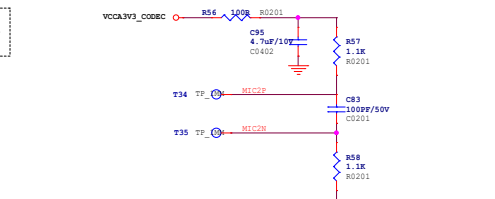
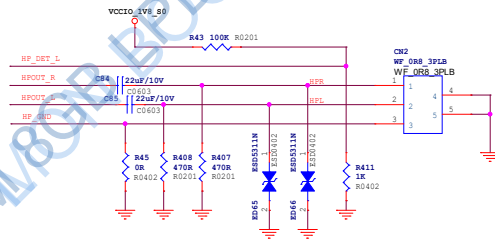
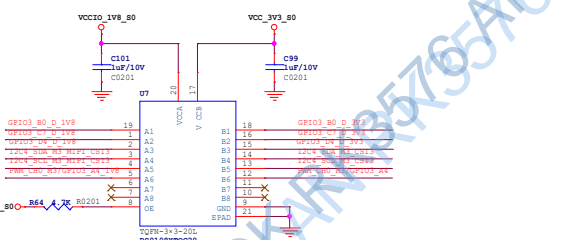
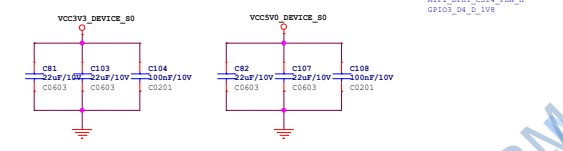
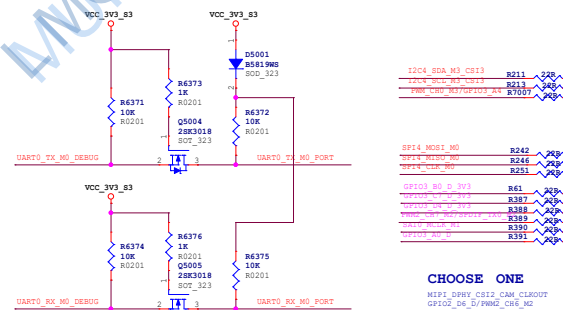
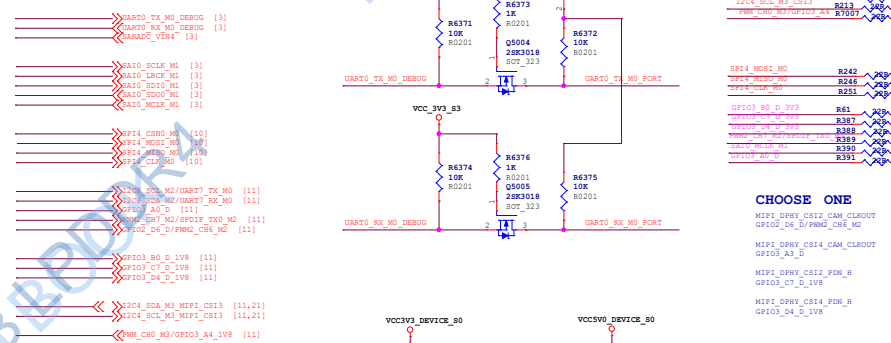
RGB_LED

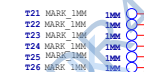
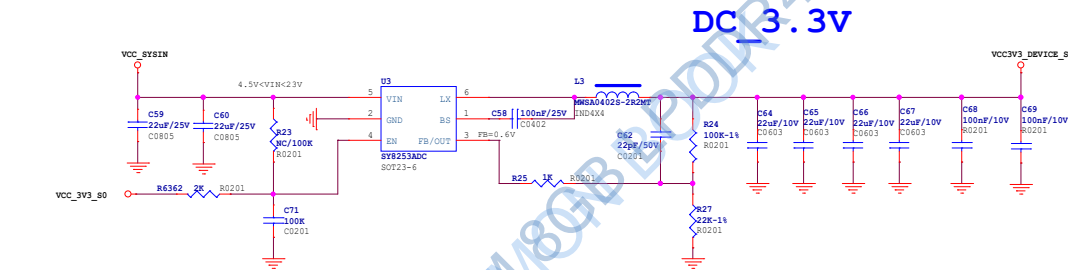
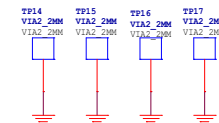
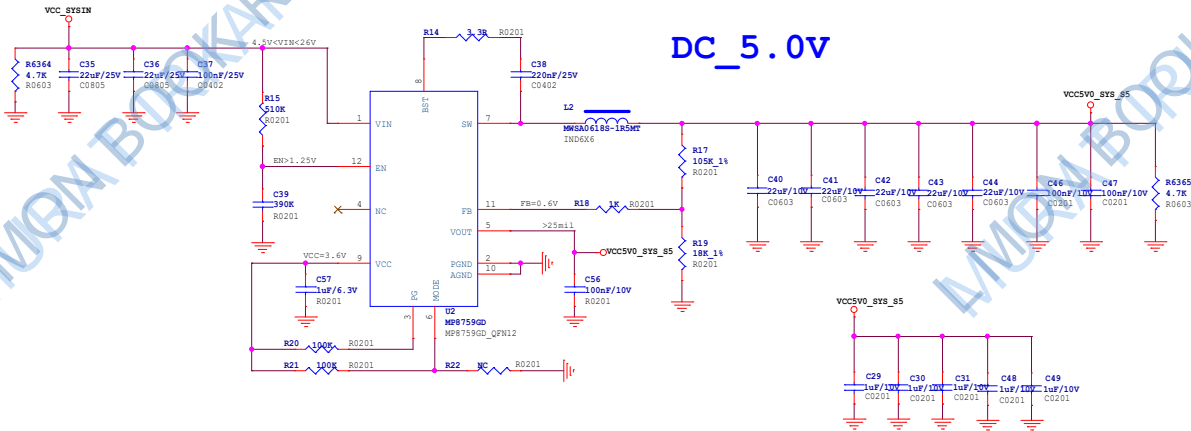
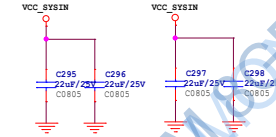
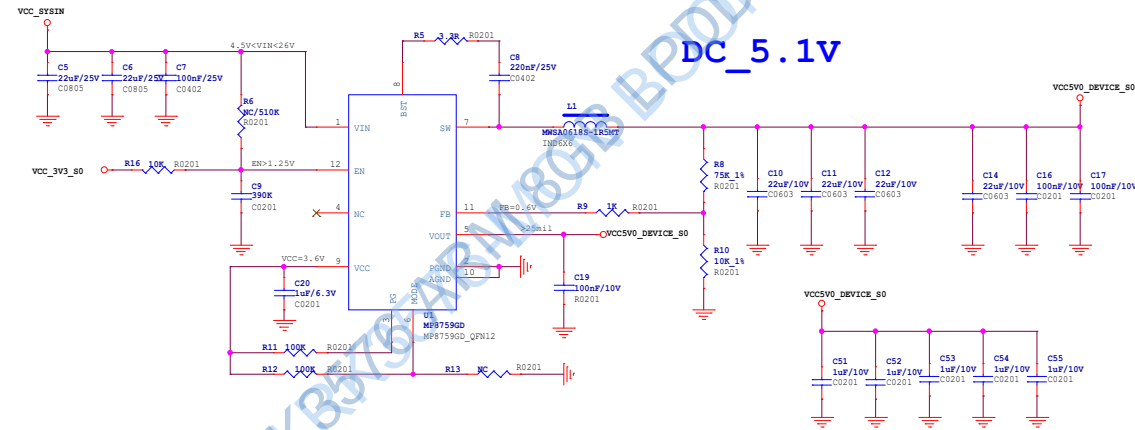


AUDIO CODEC



40PIN_GPIO





Revision History

Version	Date	By	Change Dscription	Approved
V1.0	2024-03-27	SL Chen	First release;	
V1.1	2024-05-15	SL Chen	1.U1/U2 Pin5 connect to output; TF_DET_L connect to VCC_1V8_S3; J23 Pin2&Pin3 change position; 2.J25 MIPI_DPHY_CSI3_CAM_CLKOUT&MIPI_DPHY_CSI4_CAM_CLKOUT_CON change position;	