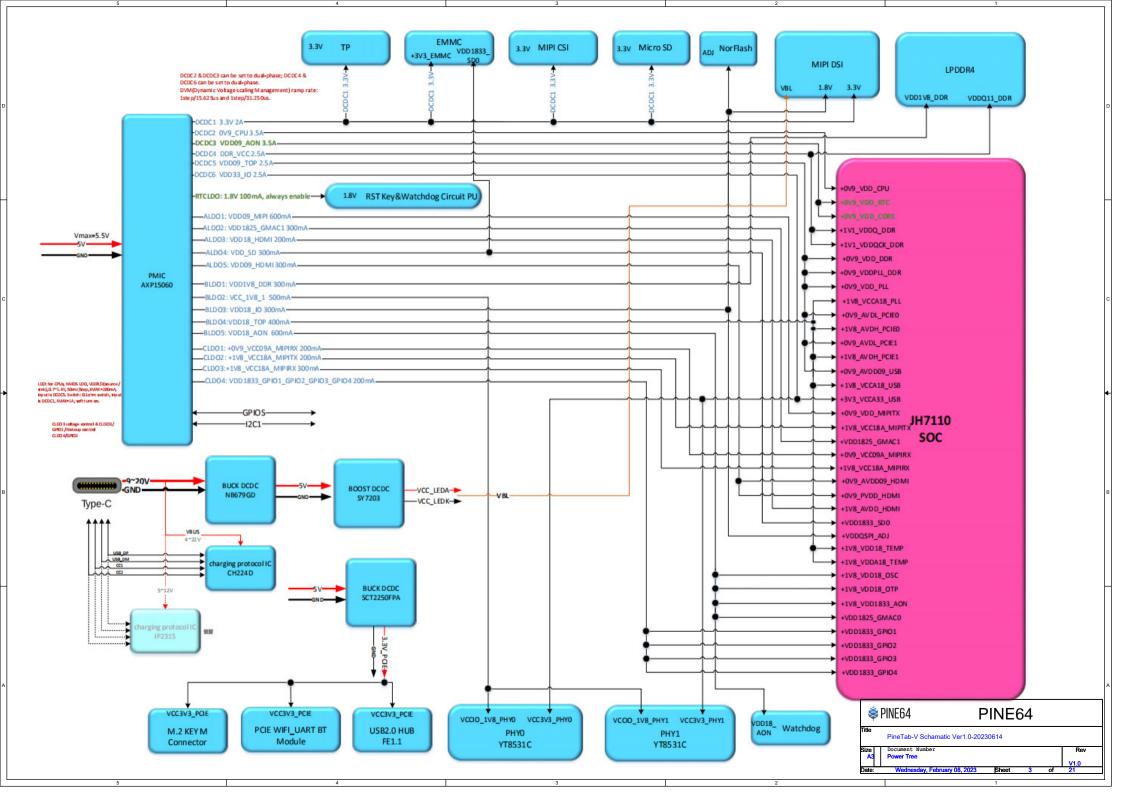


	0 :	L 2	2 3	3 4	1 5	5 (5 .	7 Default
	1ms	1ms	1ms	1ms	1ms	1ms	1ms	
DCDC1	<u>:</u> :							3.3V
SWOUT _	<u>: </u>							0V
DCDC2								0.9 v
DCDC3	<u> </u>							0.9 v
DCDC4								1.1v
DCDC5	<u> </u>							0.9 v
DCDC6								3.3V
								1.8V
RTCLDO								
ALDO1	<u>: </u>							0.9∇
ALDO2	<u>:</u>							1.8V
ALDO3								1.8V
ALDO4								1.8V
ALDO5								0.9V
								1 1 1
BLD01	<u>: </u>							1.8V
BLDO2								1.8V
BLDO3								1.8V
BLDO4	<u> </u>							1.8V
BLDO5								1.8V
CLDO1	<u> </u>							0.9 v
CLDO2	!							1.8V
CLDO3								1.8V
CLDO4								3.3V
5	•						!	

oup	Power pin in PMIC	Package Ball Name	Power pin index	Voltage (V)	Comments
	VDD09_AON	VDD			
		VDD_RTC	1	0.9	In standby mode, always on power domain, turn off other power domain
	VDD09_CPU	VDD_CPU	2	0.9~1.0	CPU core supply can adj to 1.0V. Note:0.9≤VDD_CPU≤1.0
		VDD18_OSC			
	VDD18_AON	VDD18_OTP	3	1.8	always on domain GPIO, RPIO, RESETN, TESTEN, 1.8v only
		VDD1833_AON			
	VDD1825_GMAC0	VDD1825_GMAC0	4	1.8/2.5	GMACO IO Power domain,1.8V/2.5V, Always on.
	VDD18_HDMI	AVDD_HDMI	5	1.8	HDMI & MIPITX 1.8v analog power supply, Power on first
	VDD09_HDMI	AVDD09_HDMI	6	0.9	
		PVDD_HDMI			0.9V power on after VDD18_HDMI_MIPITX,VDD09_HDMIand VOUT digital turn on/off together
	VDD18_MIPITX	VCC18A_MIPITX	7	1.8	
	VDD09_MIPITX	VDD_MIPITX	8	0.9	
	VDD18_MIPIRX	VCC18A_MIPIRX	9	1.8	MIPI RX 1.8v analog power supply, power on first
	VDD09_MIPIRX	VCC09A_MIPIRX	10	0.9	MIPI RX 0.9v power supply, power on after VDD18_MIPIRX
	VDD33_USB2	VCCA33_USB	11	3.3	3.3V for USB2.0, the seq with other power:power on after 0.9 & 1.8 , power off before
		AVDD09_USB			
		AVDL_PCIE0			
		AVDL_PCIE1			
	VDD09_TOP	VDD_PLL0	12	0.9	
		VDD_PLL1			
		VDD_PLL2			
		VDD_DDR			DDRPHY core power 0.9v
		VDDPLL_DDR			DDRPHY PLL power 0.9v
		VCCA18_PLL0			
		VCCA18_PLL1			
		VDD18_TEMP			
	VDD18_TOP	VDDA18_TEMP	13	1.8	
		VCCA18_PLL2			
		AVDH_PCIE0			
		AVDH_PCIE1			
		VCCA18_USB			
	VDDQ11_DDR	VDDQ_DDR			
		VDDQCK_DDR	14	1.1	DDRPHY IO and CLK IO voltage, 1.1v
	VDD1833_GPI01	VDD1833_GPIO1			
	VDD1833_GPIO2	VDD1833_GPI02	15		
	VDD1833_GPI03	VDD1833_GPIO3		1.8/3.3	
	VDD1833_GPIO4	VDD1833_GPIO4			
	VDD1833_SD0	VDD1833_SD0	16		
	VDD1833_QSPI	VDD1833_QSPI			
	VDD1825 GMAC1	VDD1825 GMAC1	17	1.8/2.5	

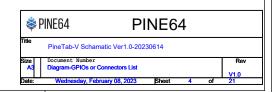
See the JH7110_Package_spec Excel for details.

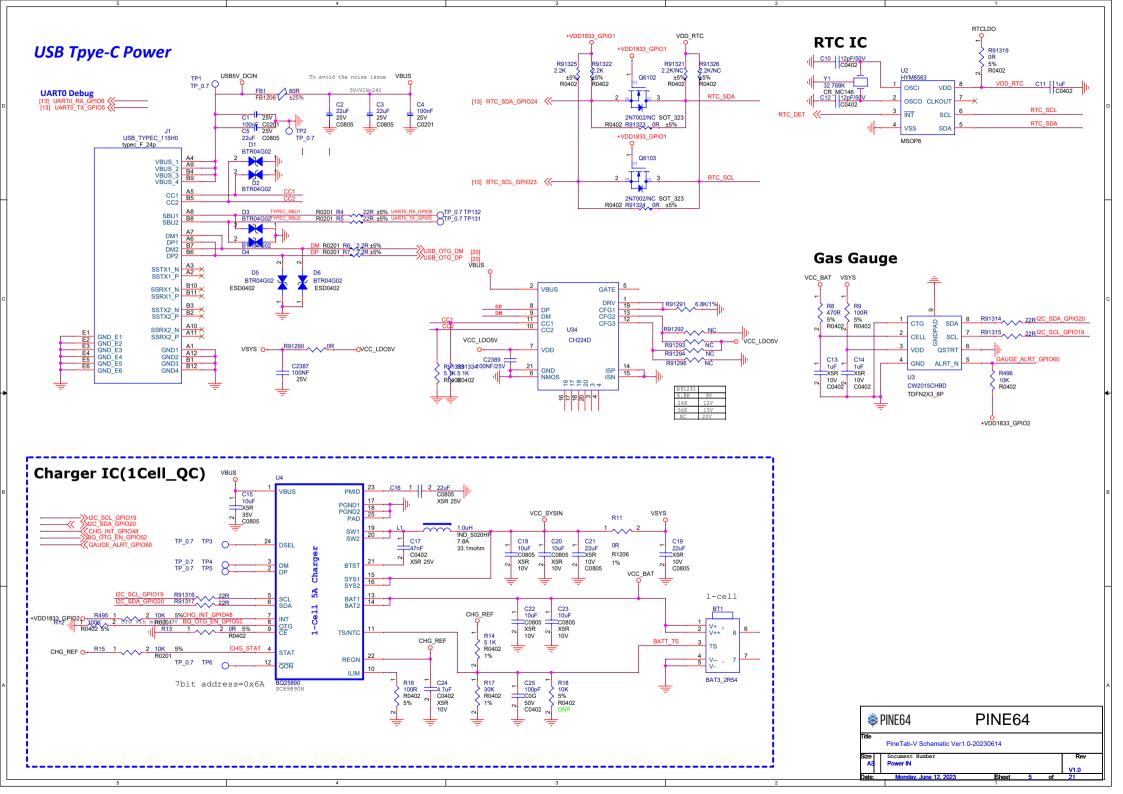
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Title	Title PineTab-V Schamatic Ver1.0-20230614				
Size B	Document Number Diagram-PD SEQ	Rev V1.0			
Date:	Wednesday, February 08, 2023 Sheet 2 of	21			

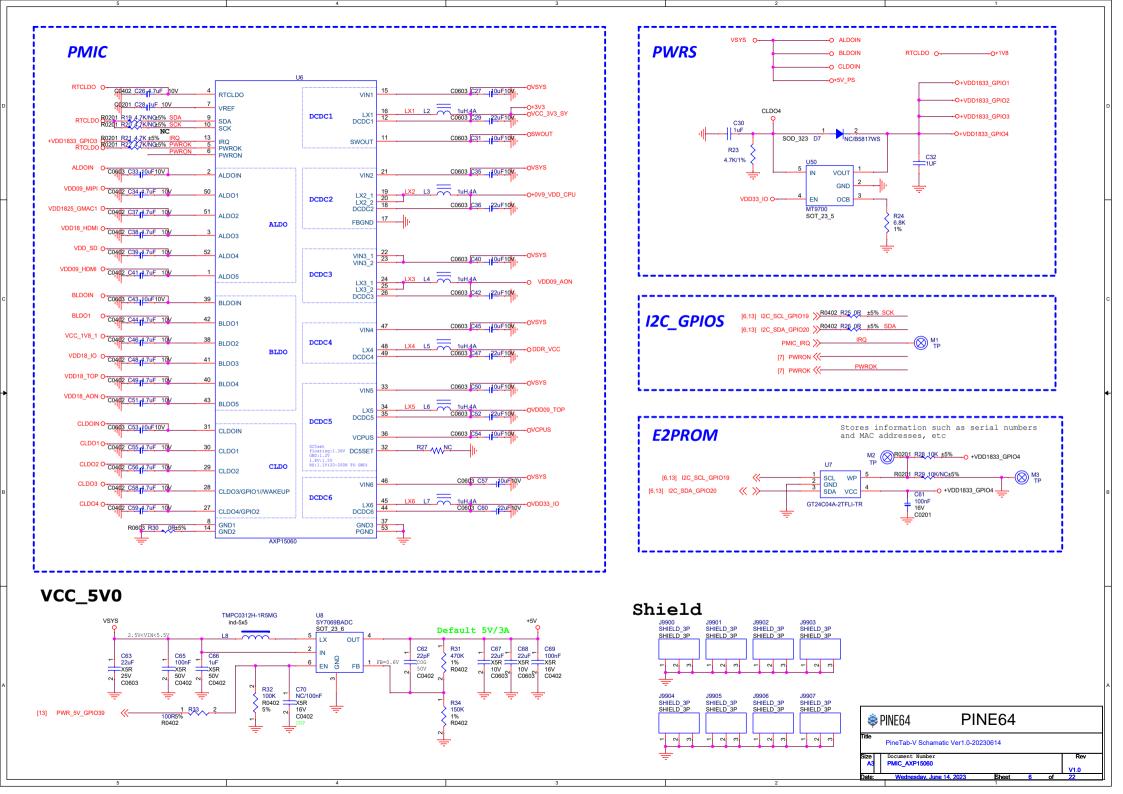


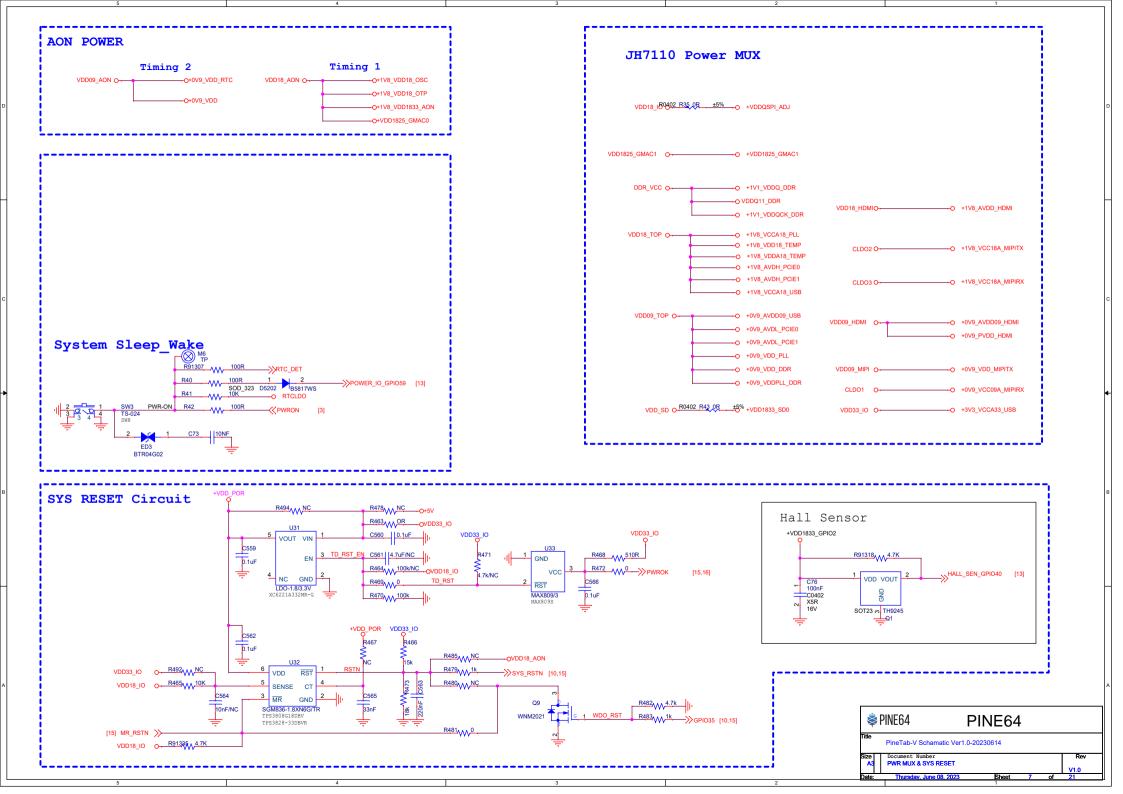
PAGE LIST PAGE00: Diagram-SYSTEM PAGE01: Diagram-PD SEQ PAGE02: Power Tree PAGE03:GPIOs or Connectors List PAGE04: Power IN PAGE05: PMIC_AXP15060 PAGE06: PWR MUX & SYS RESET PAGE07: JH7110 Power PAGE08: JH7110 DDR Ctrl PAGE09: LPDDR4 RAM PAGE10: JH7110 EMMC&QSPI&MicroSD PAGE11: JH7110 GMAC & SYS PAGE12: JH7110 GPIOs PAGE13: JH7110_HDMI_PCIE_MIPI_USB PAGE14: Ethernet_0 PAGE15: Ethernet 1 PAGE16: MIPI TX RX PAGE17: M.2 KEY M&WIFI BT MODULE PAGE18: MIPI_LCD&Digital Video PAGE19: USB2.0 HUB_Programming PAGE20: IO ConnectorUART Debug PAGE21: Audio ES8316

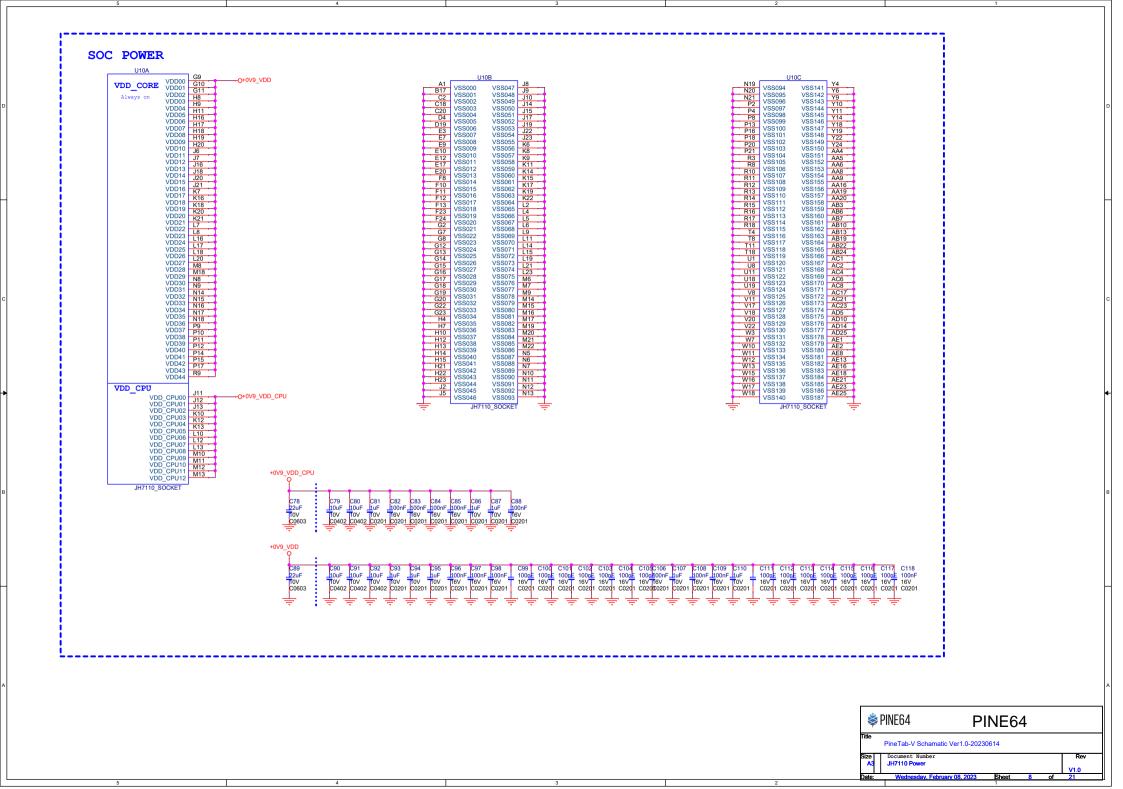
	GPIO Name	Default FUN			
Power Group	OI IO Manie		Connector or EXT Jumper	Function	
	GPIO00 GPIO01	HDMI I2C SCL HDMI I2C SDA DSI I2C SDA DSI I2C SCL			
	GP1001 GP1002	DST T2C SDA	J1	USB Tpye-C Power IN Connector	
VDD1833 GPIO3	GPIO03	DSI 12C SCL	01	OSB TPYE C TOWEL IN COMMECCOL	
.221000_01100	GPIO04	I2S MCLK			
	GPIO05	UARTO TX	**	W. Con and a second	
	GPIO06	UARTO_RX	J2	Micro SD Card Connector	
	GPIO07 GPIO08	SD SDIO1_D2			
	GPIO09	SD_SDIO1_D3 SD_SDIO1_CMD	J3	Giga PHY0 RJ45 Connector	
	GPIO10	SD SDIO1 CLK SD SDIO1 D0	•		
	GPIO11	SD SDIO1 DO			
	GPIO12	SD SDIO1 D1	J4	Ext POE Connector	
/DD1833_GPI04	GPIO13 GPIO14	GMAC PHYRSTN HDMI CEC GPIO			
	GPI014 GPI015	HDMI CEC GPIO HDMI HPDIN	J 5	Giga PHY1 RJ45 Connector	
	GPIO16	CSI I2C SCL	• • • • • • • • • • • • • • • • • • • •	diga inii no to connector	
	GPIO17 GPIO18	CSI_I2C_SDA CSI_PWDNO			
	GPIO18	CSI PWDN0	J6	MIPI DSI 1C2L 30PIN Connector	
	GPIO19 GPIO20	I2C SCL I2C SDA			
			J 7	MIPI CSI 1C2L 30PIN Connector	
	GPIO21 GPIO22	PCIE1 PWREN H MIPI PWR EN			
	GPIO23	LCD RESET	J8	WLO ANT :IPEX MHF1 Connector Receptacle, Male Pi	
	GPIO24	MIPI LCD BL		500hm Surface Mount Solder	
	GPIO25	TP DET			
	GPIO26 GPIO27	PCIEO PERSTN PCIEO PRSN	J9	WL1 ANT :IPEX MHF1 Connector Receptacle, Male Pin	
VDD1833_GPI01	GP1027 GP1028	PCIEU PRSN PCIE1 PERSTN		500hm Surface Mount Solder	
	GPIO29	PCIE1 PRSN	J10	PCIE1 M.2 KEY M screw Connector	
	GPIO30	TP INT			
	GPIO31	TP_RST		BT ANT :IPEX MHF1 Connector Receptacle, Male Pin 500hm Surface Mount Solder	
	GPIO32 GPIO33	PCTEO PWREN H	J11		
	GPI033	GPIO33> J16 GPIO34> J16 GPIO RST		Sooimi Surrace Mount Sorder	
	GPIO35	GPIO RST	J12	HDMI Connector	
	GPIO36	GPI036> J16			
	GPIO37	GPIO37> J16	J13	USB2.0 TYPE-A x2 Connector	
	GPIO38	GPIO38> J16			
	GPIO39 GPIO40	GPI039> J16 HP IRQ	J14	USB2.0 TYPE-A x2 Connector	
	GPIO40 GPIO41	SD SDIOO CD	014	USBZ.U TIPE-A XZ COMMECCUI	
	GPIO42	STS PWR		MicroUSB SYS Programming Connector	
	GPIO43	GPIO43> J16 SOC UART RX	J15		
	GPIO44	SOC_UART_RX			
	GPIO45 GPIO46	SOC UART TX SOC UART RTS	J16	20PIN GPIOS Header2x10-2 54mm Connecto	
	GPI046 GPI047	SOC UART RTS	010	ZULIN GLIOS REAGELZKIU-Z_SANMI CONNECCO	
VDD1833 GPIO2	GPIO48	I2S DSDIN			
_	GPIO49	GPIO49> J16	J17	3PIN UARTO HDR1X3-2p54-DIP-V Connector	
	GPI050 GPI051	PCIEO WL ON			
	GPI051 GPI052	I2S ASDOUT GPT052> J16	J18	MicroUSB UARTO Debug Connector	
	GPIO53	GPIO52> J16 I2S LRCK	010	increase water beday connector	
	GPIO54	I2S SCLK			
	GPIO55	SOC PCM OUT			
	GPIO56	PCIEO BT ON			
	GPIO57 GPIO58	GPIO57> J16			
	GP1058	GPI058> J16 GPI059> J16			
	GPIO60	SOC PCM SYNC			
	GPIO61	SOC PCM CLK			
	GPIO62	SDIOO RSTN			
	GPIO63	SOC_PCM_IN			
/DD18 AON	RGPIO0 RGPIO1	Boot Mode Boot Mode			

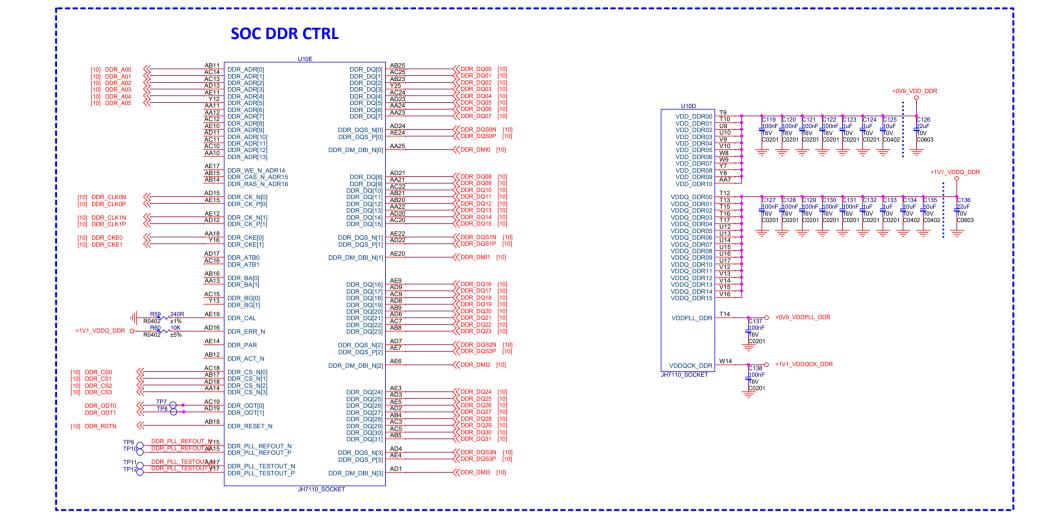




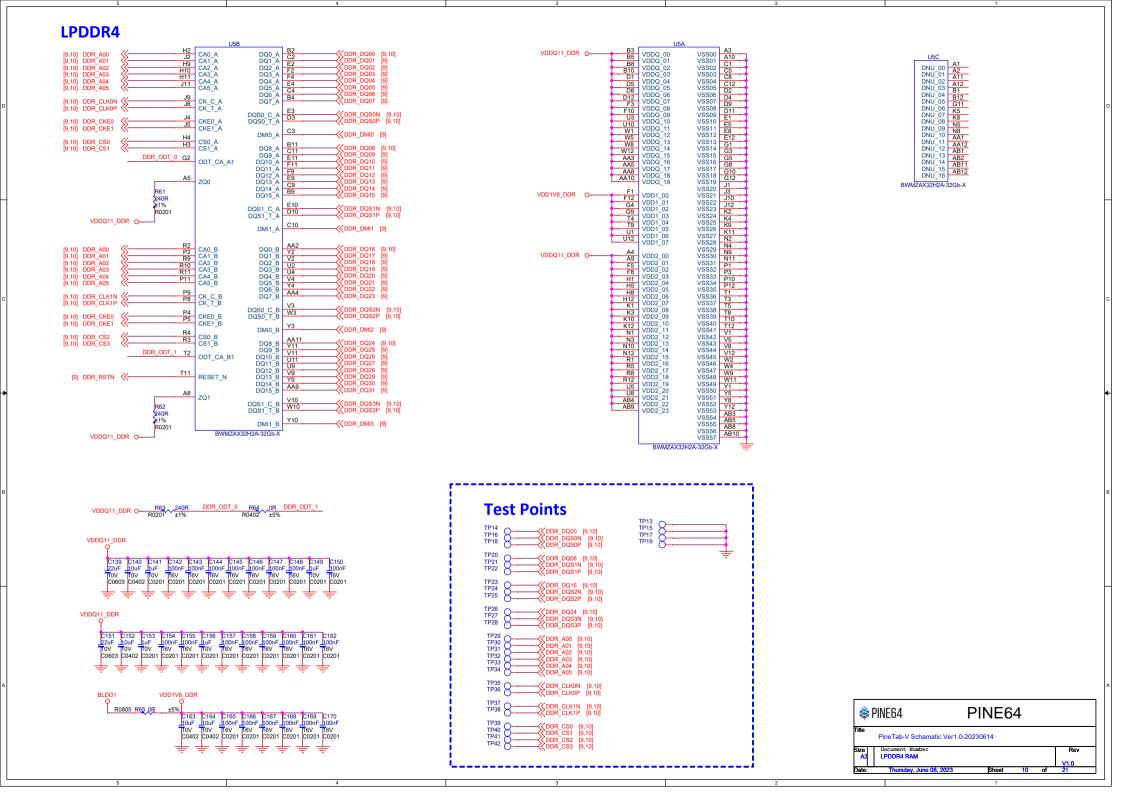


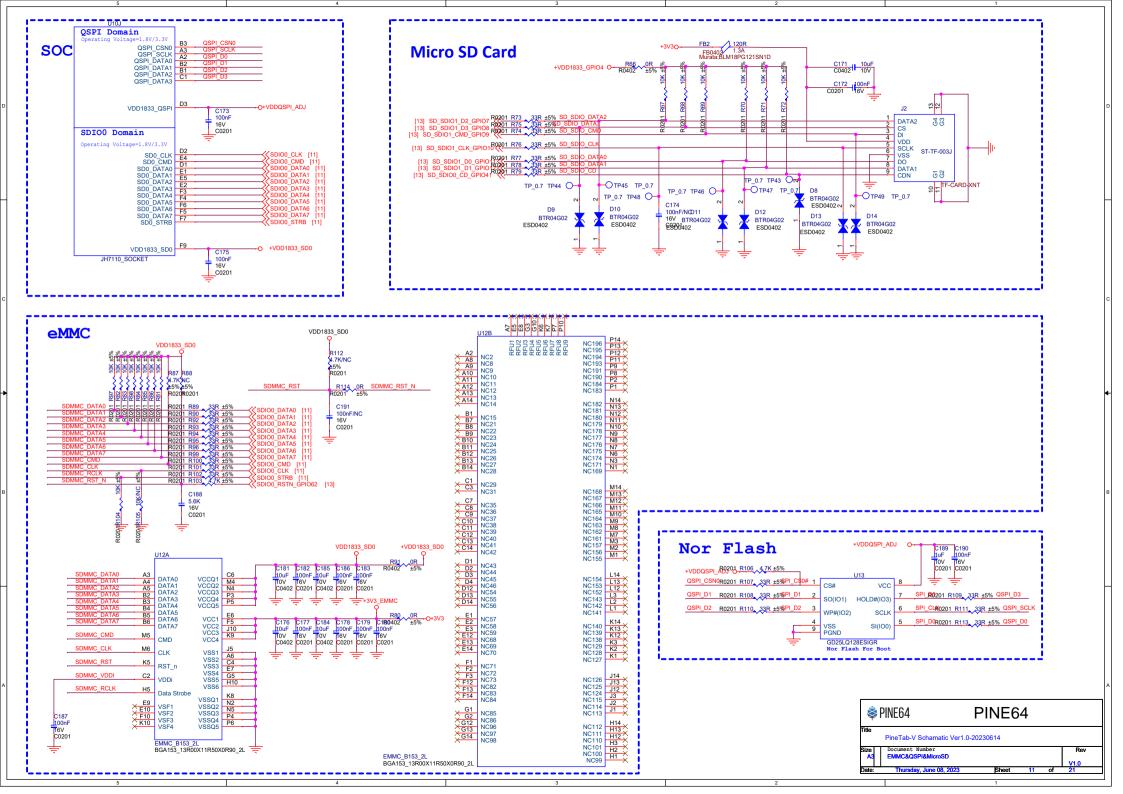


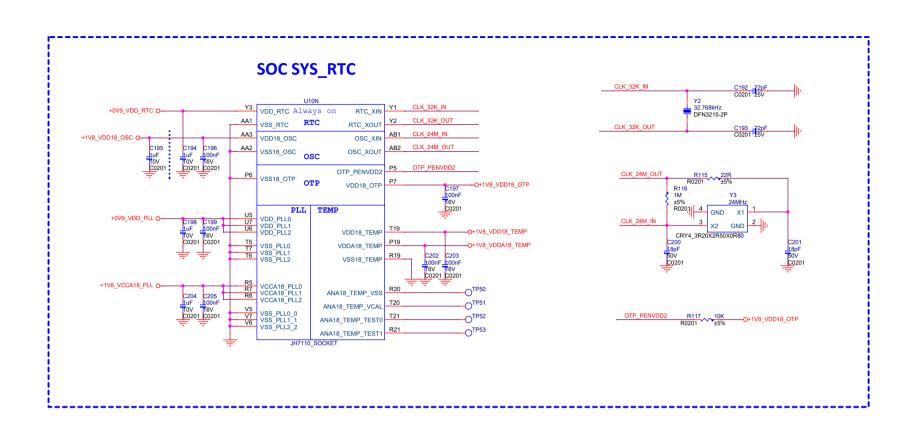




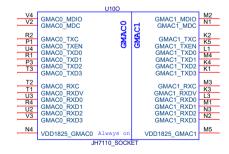




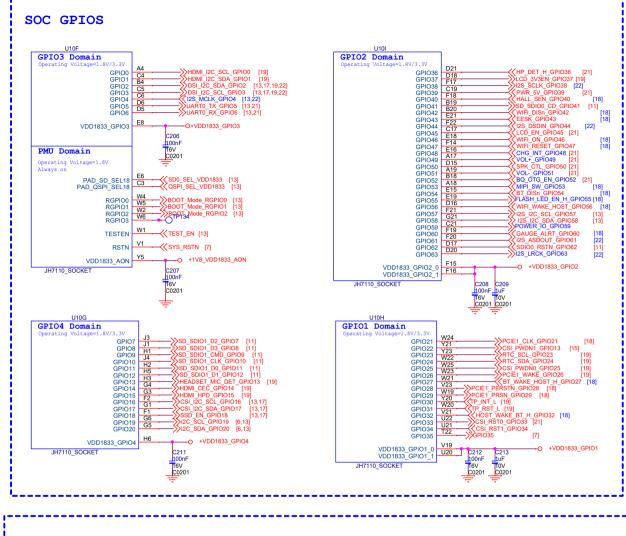




SOC GMAC



\$	PINE64 PINE64	
Title	PineTab-V Schamatic Ver1.0-20230614	
Size A3	Document Number JH7110 GMAC & SYS	Rev V1.0
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BOOT MODE

BOOT MODE (RGPIO2)	Boot Vector	Boot Selection (RGPIO1,0)	Note
0×1	0x00_2100_0000	XIP Flash	can not boot from XIP Flash, if disabled thru OTP Configuration.
0×0	0x00_2A00_0000	0x0: 1Bit QSPI Nor Flash 0x1: SDI03.0 0x2: eMMC5.0 0x3: UART	on-chip boot ROM(32KB)

