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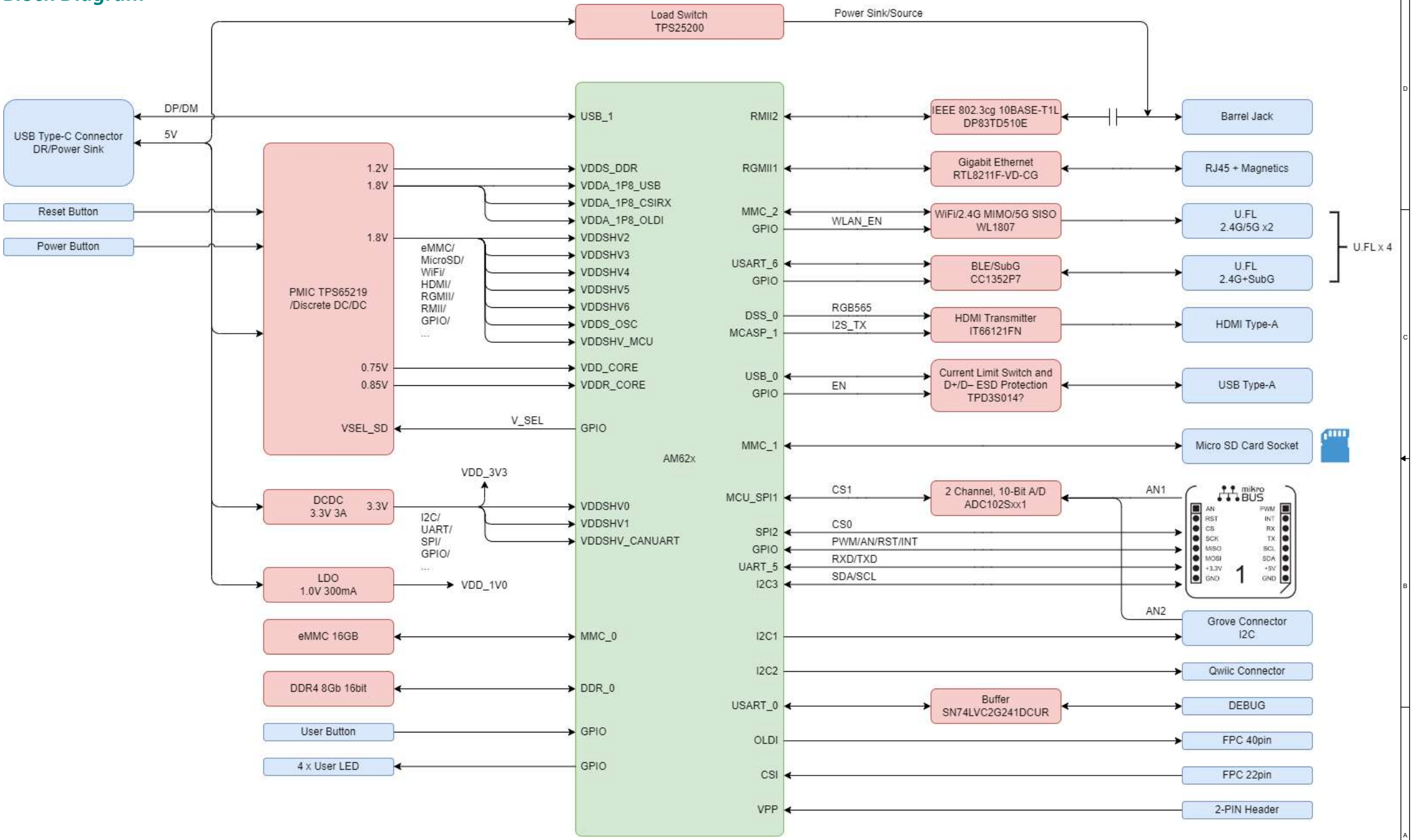
REVISION HISTORY

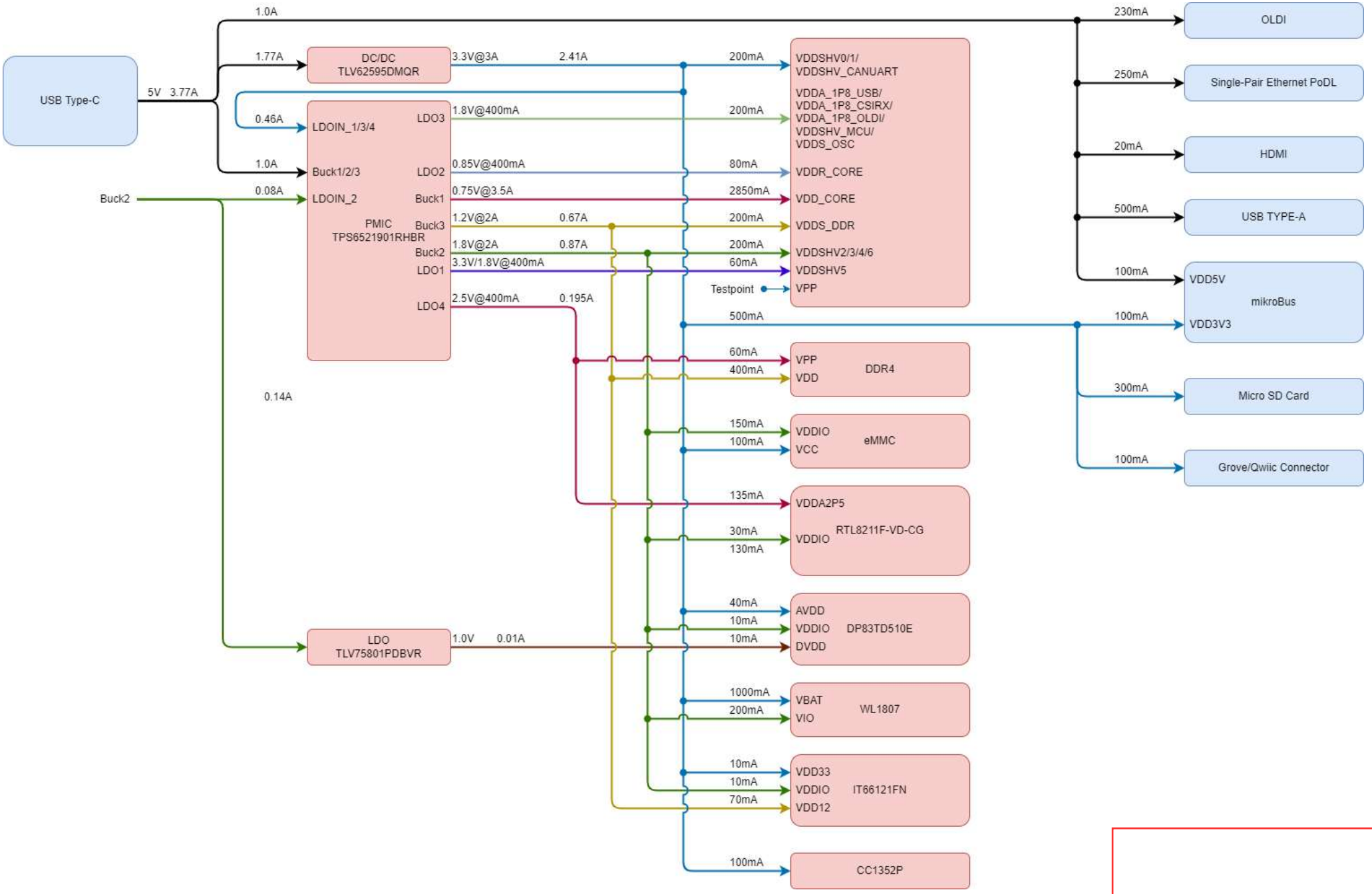
VER #	DATE	DESCRIPTION OF CHANGES	AUTHOR
0.20	15 Sep. 2022	DVT1 Board release	qxn
0.21	Nov. 10, 2022	1. Modify all crystal CL value to match frequency 2. Add serial termination resistors for RGB data lane 3. Add CMC on HDMI output 4. Use WKUP_CLKOUT0 for WiFi 5. Use MCU_OBSCLK0 for GBE 6. Increase resistance on all board LEDs 7. Remove R243 TP_INT Pulldown 8. Add ferrite bead on Grove and QWIIC 9. Add feed forward capacitor on TLV62595 10. Remove ESDs on HDMI TMDS signals	qxn
0.22	Dc. 12, 2022	1. Add feed forward capacitor on TLV62595 2. Remove ESDs on HDMI TMDS signals 3. Add ferrite bead on HDMI shield 4. Change R16 and R80 to 0R 5. Change pullup resistors to 2.2k on I2C0-I2C3 6. Change FB30 and FB31 to 0R	qxn
1.0	Dc. 27, 2022	1. Add testpoint to QWIIC 2. Add serial resistors on I2C0 - I2C3 SCL 3. Add more capacitors on VDD_1V2 4. Add 0R on HDMI shield 5. Add series resistor on SPE LED control	qxn

CC BY SA

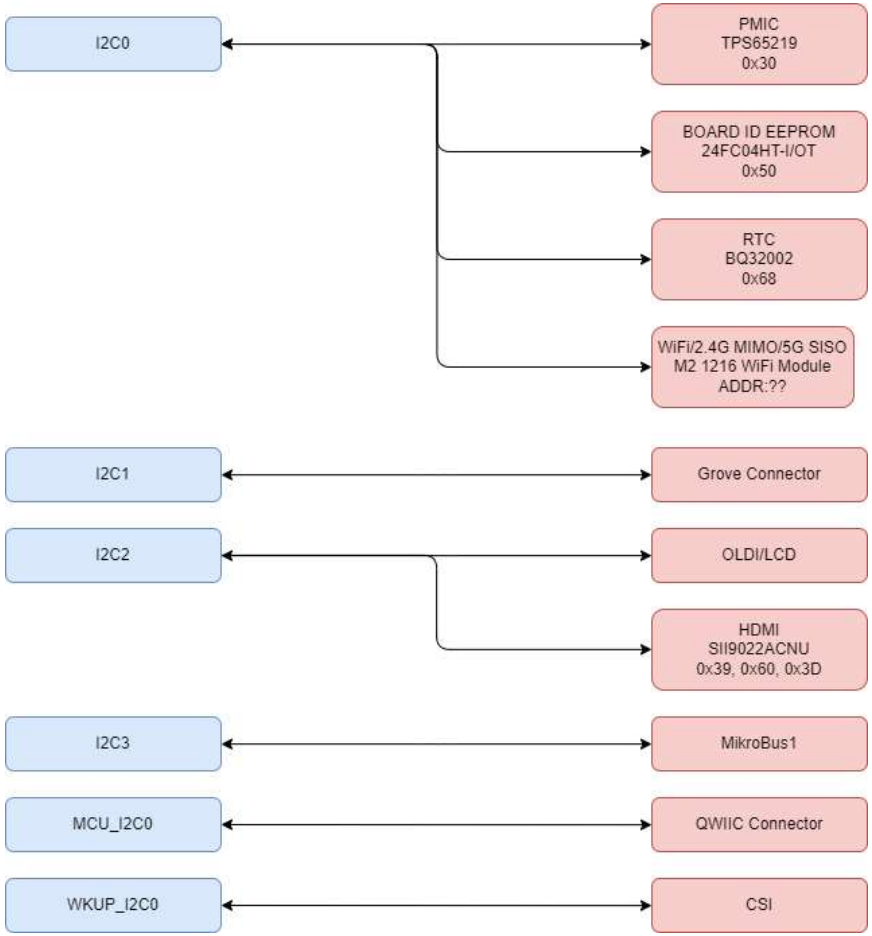
Block Diagram



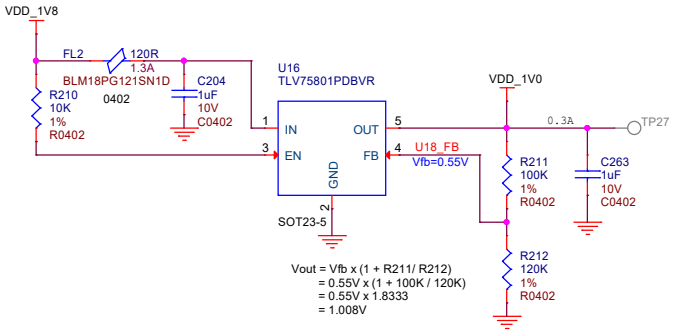
Power tree


USB Type-C
5V 3.77A
1.0A
1.77A
DC/DC
TLV62595DMQR
3.3V@3A
2.41A
0.46A
LDOIN_1/3/4
LDO3
1.8V@400mA
1.0A
Buck1/2/3
LDO2
0.85V@400mA
Buck2
0.08A
LDOIN_2
Buck1
0.75V@3.5A
PMIC
TPS6521901RHBR
Buck3
1.2V@2A
0.67A
Buck2
1.8V@2A
0.87A
LDO1
3.3V/1.8V@400mA
LDO4
2.5V@400mA
0.195A
0.14A
LDO
TLV75801PDBVR
1.0V
0.01A
200mA
VDDSHV0/1/
VDDSHV_CANUART
200mA
VDDA_1P8_USB/
VDDA_1P8_CSIRX/
VDDA_1P8_OLDI/
VDDSHV_MCU/
VDDS_OSC
80mA
VDDR_CORE
2850mA
VDD_CORE
200mA
VDDS_DDR
200mA
VDDSHV2/3/4/6
60mA
VDDSHV5
Testpoint
VPP
500mA
60mA
VPP
400mA
VDD
DDR4
150mA
VDDIO
100mA
VCC
eMMC
135mA
VDDA2P5
30mA
VDDIO
RTL8211F-VD-CG
130mA
40mA
AVDD
10mA
VDDIO
DP83TD510E
10mA
DVDD
1000mA
VBAT
200mA
VIO
WL1807
10mA
VDD33
10mA
VDDIO
IT66121FN
70mA
VDD12
100mA
CC1352P
230mA
OLDI
250mA
Single-Pair Ethernet PoDL
20mA
HDMI
500mA
USB TYPE-A
100mA
VDD5V
mikroBus
100mA
VDD3V3
300mA
Micro SD Card
100mA
Grove/Qwiic Connector

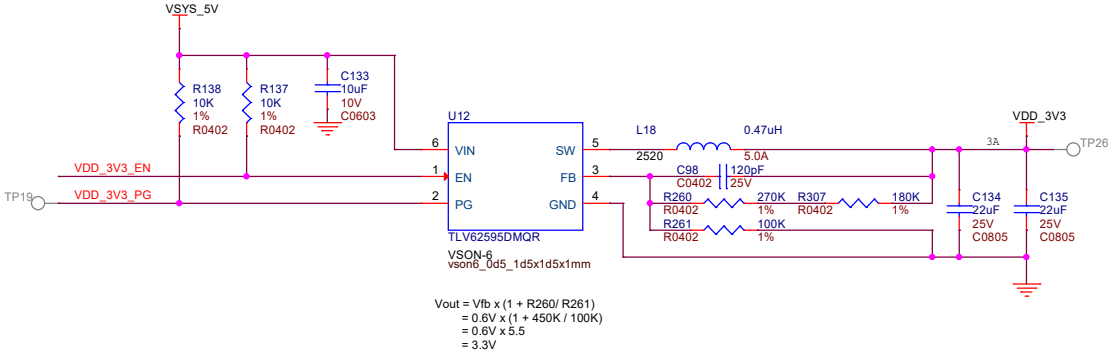
I2C Usage Diagram



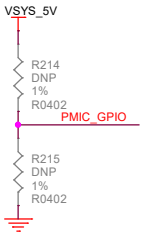
LDO 1V0



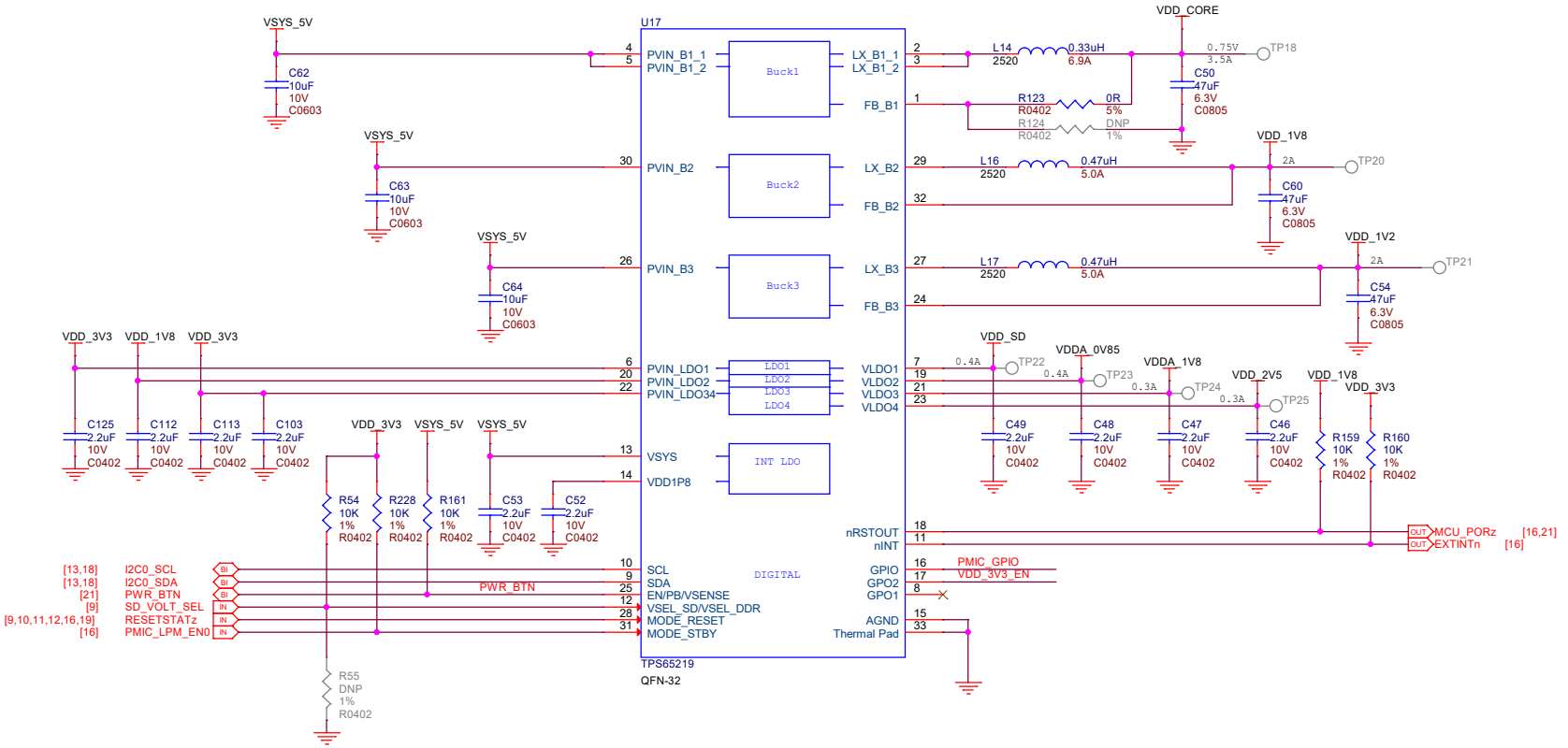
DCDC 3V3



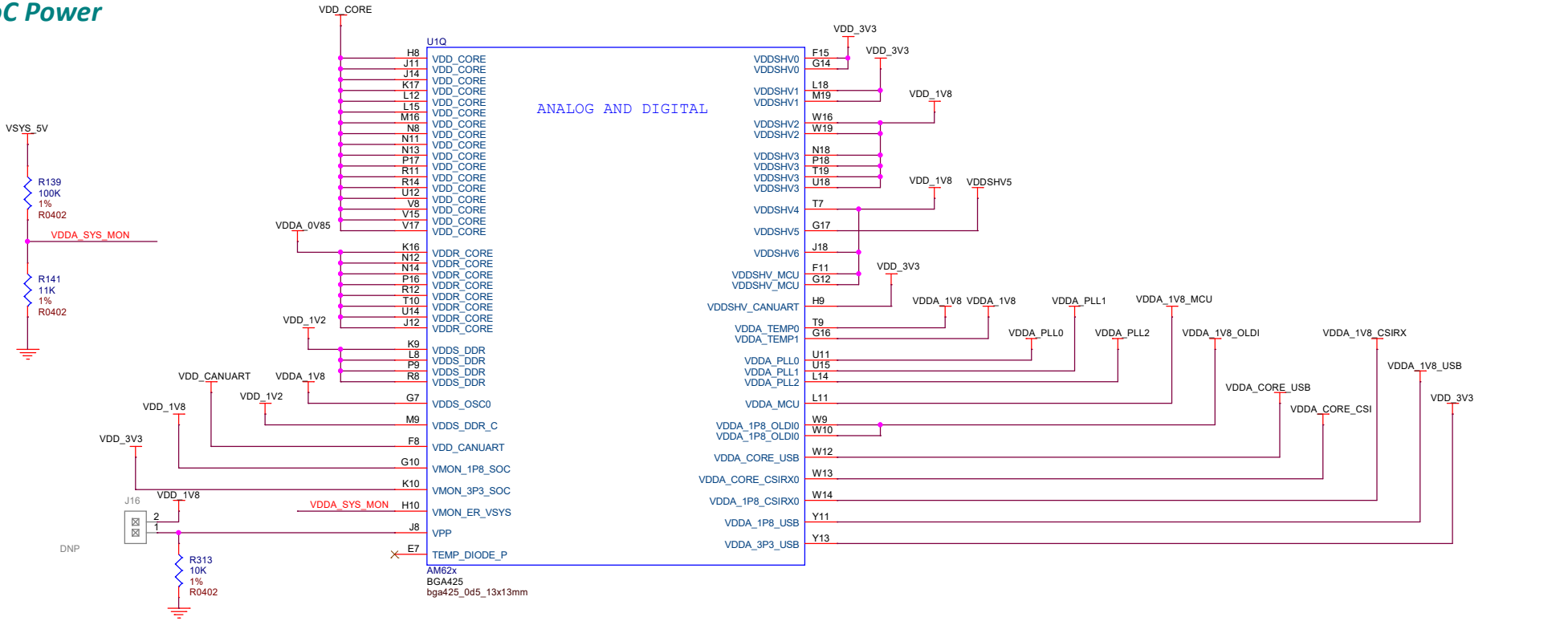
PMIC_Strap option

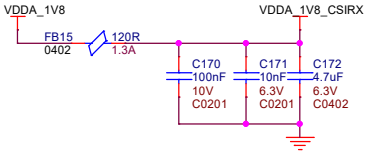


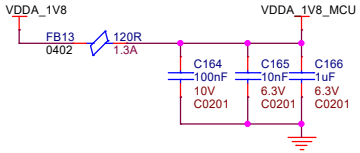
PMIC

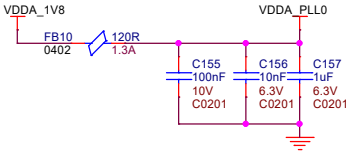


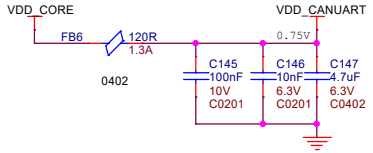
SoC Power

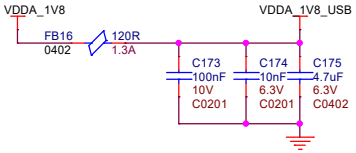

U1Q
ANALOG AND DIGITAL
VDD_CORE
VSYS_5V
R139
100K
1%
R0402
VDDA_SYS_MON
R141
11K
1%
R0402
VDDA_0V85
VDD_1V2
VDD_CANUART
VDDA_1V8
VDD_3V3
J16
DNP
R313
10K
1%
R0402
VDDSHV5
VDDA_1V8_MCU
VDDA_PLL0
VDDA_PLL1
VDDA_PLL2
VDDA_1V8_OLDI
VDDA_1V8_CSIRX
VDDA_1V8_USB
VDDA_CORE_USB
VDDA_CORE_CSI
AM62x
BGA425
bga425_0d5_13x13mm

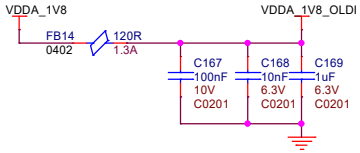

VDDA_1V8
VDDA_1V8_CSIRX
FB15
0402
120R
1.3A
C170
100nF
10V
C0201
C171
10nF
6.3V
C0201
C172
4.7uF
6.3V
C0402

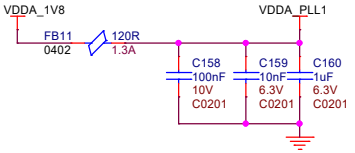

VDDA_1V8
VDDA_1V8_MCU
FB13
0402
120R
1.3A
C164
100nF
10V
C0201
C165
10nF
6.3V
C0201
C166
1uF
6.3V
C0201

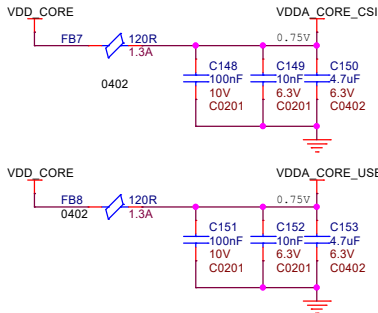

VDDA_1V8
VDDA_PLL0
FB10
0402
120R
1.3A
C155
100nF
10V
C0201
C156
10nF
6.3V
C0201
C157
1uF
6.3V
C0201

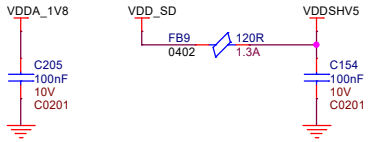

VDD_CORE
VDD_CANUART
FB6
0402
120R
1.3A
0.75V
C145
100nF
10V
C0201
C146
10nF
6.3V
C0201
C147
4.7uF
6.3V
C0402

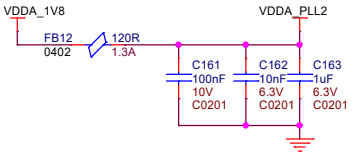

VDDA_1V8
VDDA_1V8_USB
FB16
0402
120R
1.3A
C173
100nF
10V
C0201
C174
10nF
6.3V
C0201
C175
4.7uF
6.3V
C0402


VDDA_1V8
VDDA_1V8_OLDI
FB14
0402
120R
1.3A
C167
100nF
10V
C0201
C168
10nF
6.3V
C0201
C169
1uF
6.3V
C0201


VDDA_1V8
VDDA_PLL1
FB11
0402
120R
1.3A
C158
100nF
10V
C0201
C159
10nF
6.3V
C0201
C160
1uF
6.3V
C0201

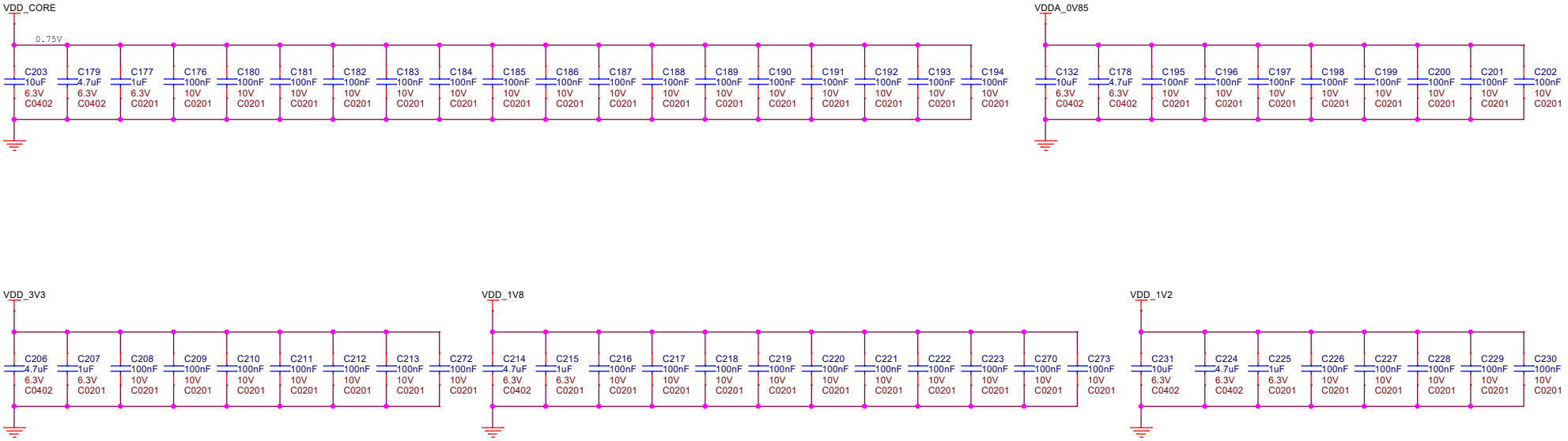

VDD_CORE
VDDA_CORE_CSI
FB7
0402
120R
1.3A
0.75V
C148
100nF
10V
C0201
C149
10nF
6.3V
C0201
C150
4.7uF
6.3V
C0402

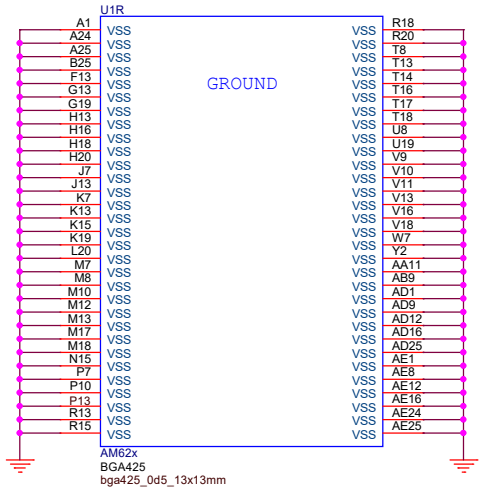

VDDA_1V8
VDD_SD
VDDSHV5
C205
100nF
10V
C0201
FB9
0402
120R
1.3A
C154
100nF
10V
C0201

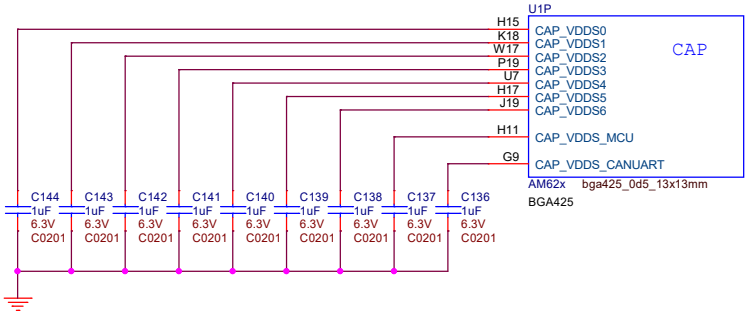

VDDA_1V8
VDDA_PLL2
FB12
0402
120R
1.3A
C161
100nF
10V
C0201
C162
10nF
6.3V
C0201
C163
1uF
6.3V
C0201

VDD_CORE
VDDA_CORE_USB
FB8
0402
120R
1.3A
0.75V
C151
100nF
10V
C0201
C152
10nF
6.3V
C0201
C153
4.7uF
6.3V
C0402

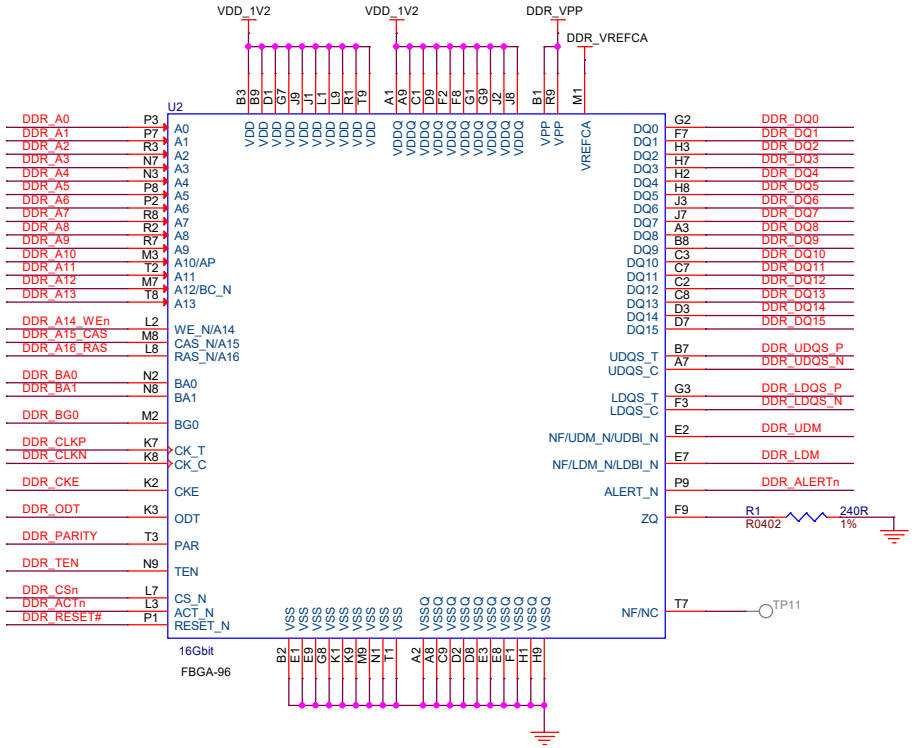
SoC DCAPs

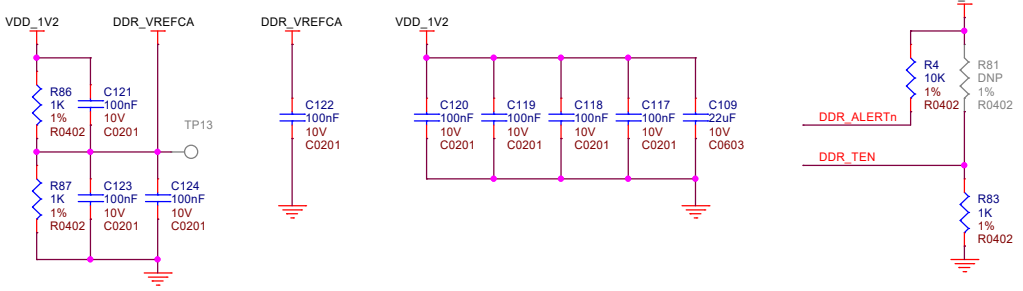

VDD_CORE
0.75V
C203 10uF 6.3V C0402
C179 4.7uF 6.3V C0402
C177 1uF 6.3V C0201
C176 100nF 10V C0201
C180 100nF 10V C0201
C181 100nF 10V C0201
C182 100nF 10V C0201
C183 100nF 10V C0201
C184 100nF 10V C0201
C185 100nF 10V C0201
C186 100nF 10V C0201
C187 100nF 10V C0201
C188 100nF 10V C0201
C189 100nF 10V C0201
C190 100nF 10V C0201
C191 100nF 10V C0201
C192 100nF 10V C0201
C193 100nF 10V C0201
C194 100nF 10V C0201
VDDA_0V85
C132 10uF 6.3V C0402
C178 4.7uF 6.3V C0402
C195 100nF 10V C0201
C196 100nF 10V C0201
C197 100nF 10V C0201
C198 100nF 10V C0201
C199 100nF 10V C0201
C200 100nF 10V C0201
C201 100nF 10V C0201
C202 100nF 10V C0201
VDD_3V3
C206 4.7uF 6.3V C0402
C207 1uF 6.3V C0201
C208 100nF 10V C0201
C209 100nF 10V C0201
C210 100nF 10V C0201
C211 100nF 10V C0201
C212 100nF 10V C0201
C213 100nF 10V C0201
C272 100nF 10V C0201
VDD_1V8
C214 4.7uF 6.3V C0402
C215 1uF 6.3V C0201
C216 100nF 10V C0201
C217 100nF 10V C0201
C218 100nF 10V C0201
C219 100nF 10V C0201
C220 100nF 10V C0201
C221 100nF 10V C0201
C222 100nF 10V C0201
C223 100nF 10V C0201
C270 100nF 10V C0201
C273 100nF 10V C0201
VDD_1V2
C231 10uF 6.3V C0402
C224 4.7uF 6.3V C0402
C225 1uF 6.3V C0201
C226 100nF 10V C0201
C227 100nF 10V C0201
C228 100nF 10V C0201
C229 100nF 10V C0201
C230 100nF 10V C0201

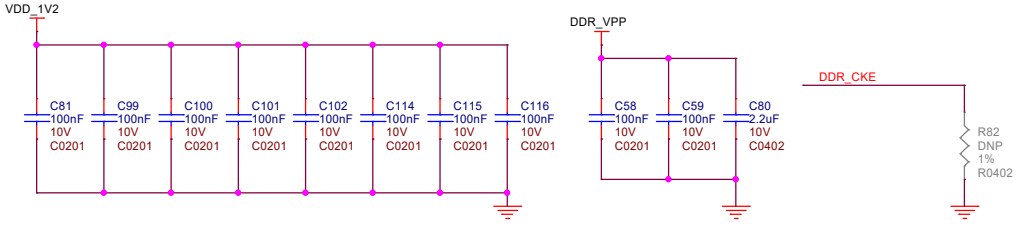

U1R
GROUND
A1 A24 A25 B25 F13 G13 G19 H13 H16 H18 H20 J7 J13 K7 K13 K15 K19 L20 M7 M8 M10 M12 M13 M17 M18 N15 P7 P10 P13 R13 R15
R18 R20 T8 T13 T14 T16 T17 T18 U8 U19 V9 V10 V11 V13 V16 V18 W7 Y2 AA11 AB9 AD1 AD9 AD12 AD16 AD25 AE1 AE8 AE12 AE16 AE24 AE25
VSS
AM62x
BGA425
bga425_0d5_13x13mm


U1P
CAP
H15 CAP_VDDS0
K18 CAP_VDDS1
W17 CAP_VDDS2
P19 CAP_VDDS3
U7 CAP_VDDS4
H17 CAP_VDDS5
J19 CAP_VDDS6
H11 CAP_VDDS_MCU
G9 CAP_VDDS_CANUART
AM62x bga425_0d5_13x13mm
BGA425
C144 1uF 6.3V C0201
C143 1uF 6.3V C0201
C142 1uF 6.3V C0201
C141 1uF 6.3V C0201
C140 1uF 6.3V C0201
C139 1uF 6.3V C0201
C138 1uF 6.3V C0201
C137 1uF 6.3V C0201
C136 1uF 6.3V C0201

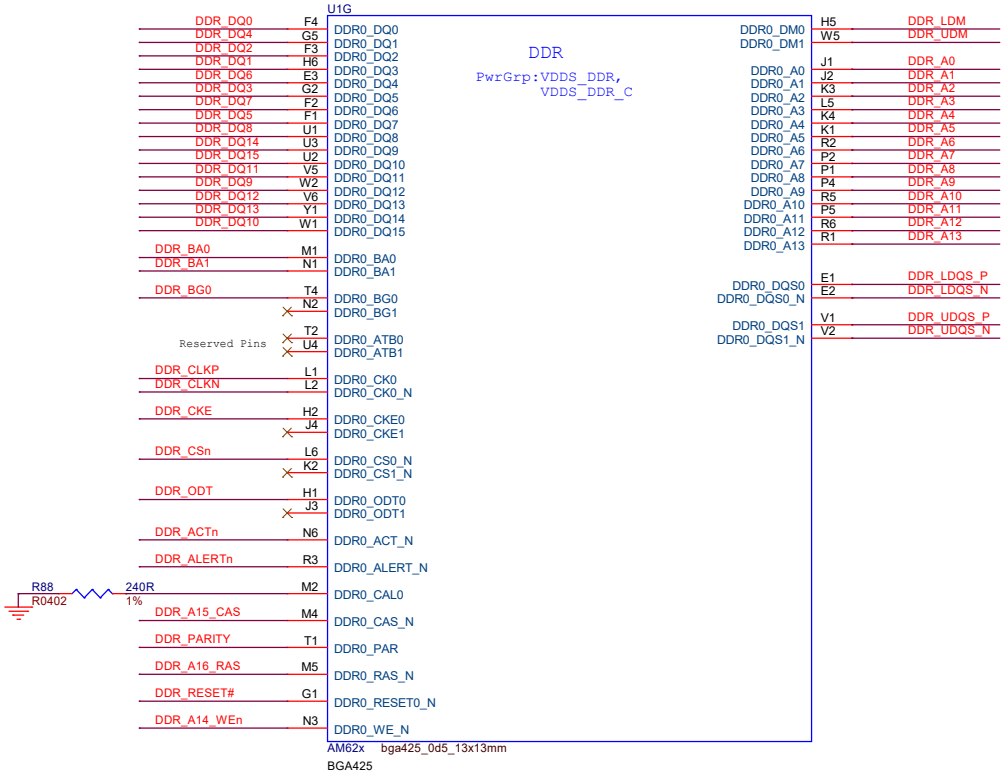
DDR4

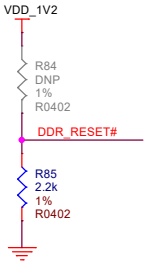




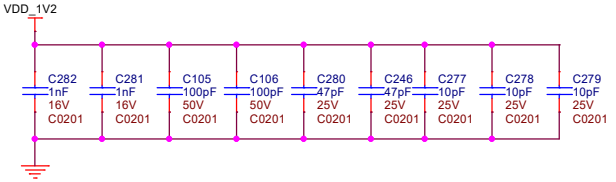


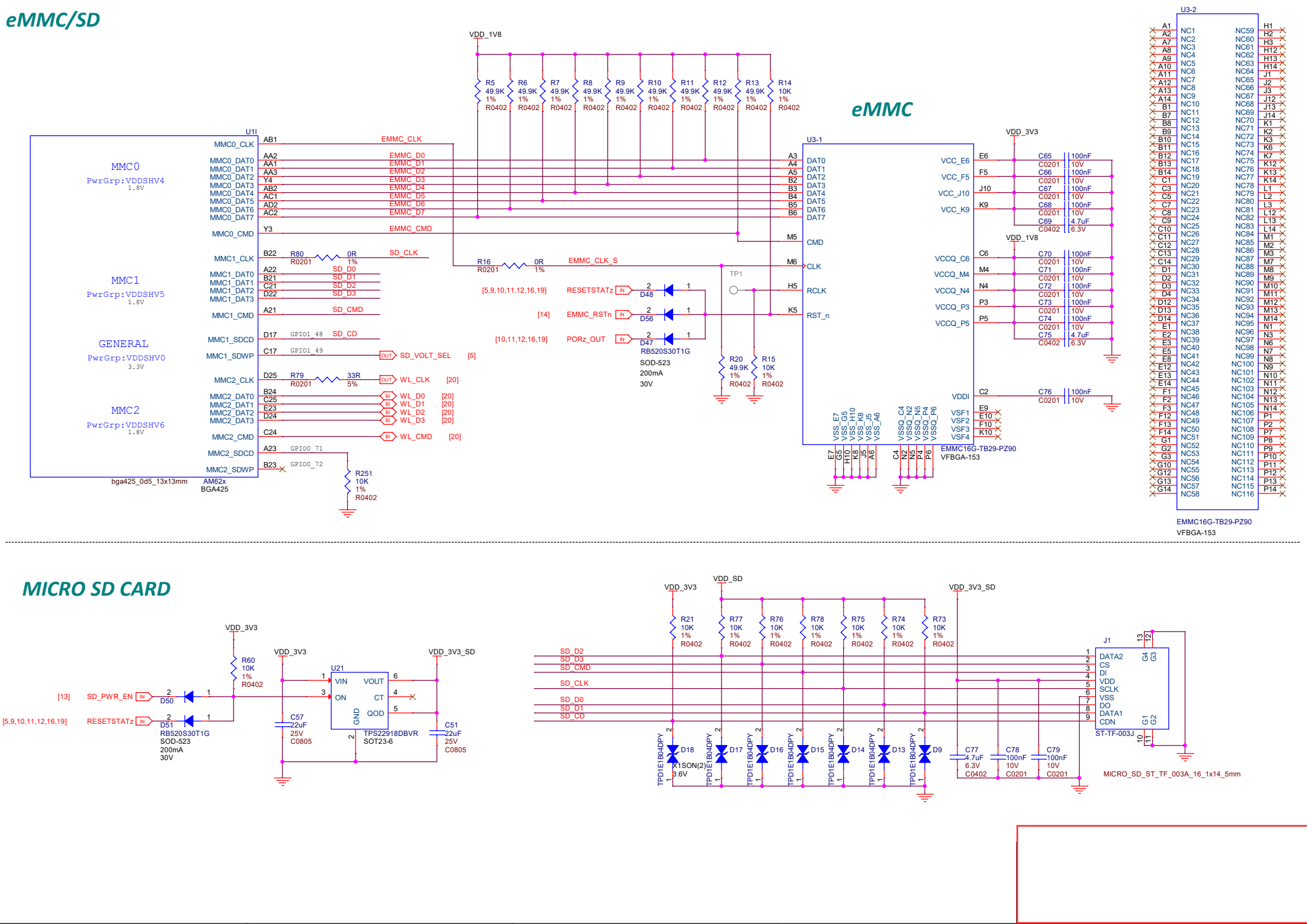
SoC DDR controller



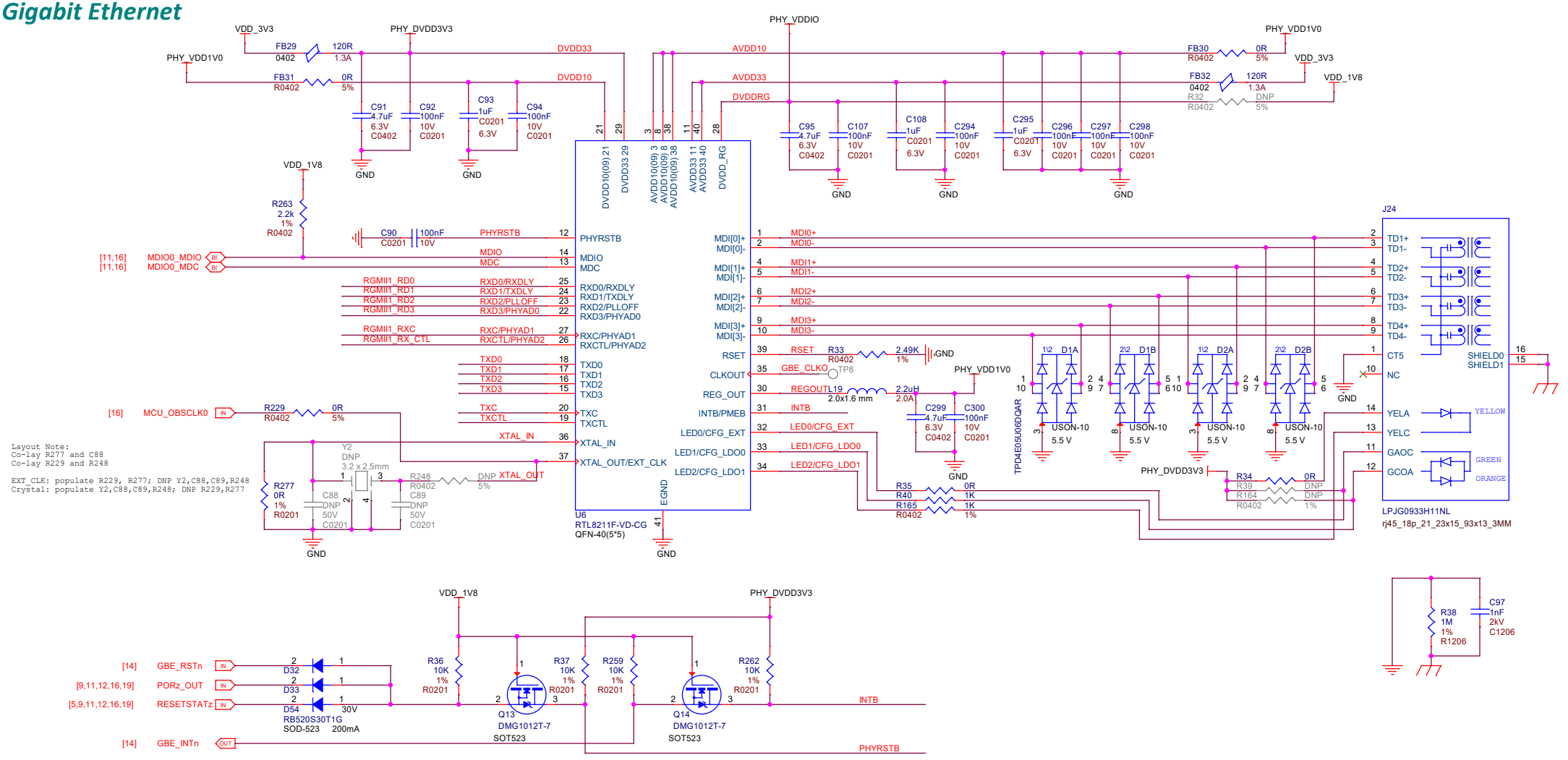


NOTE: DDR DQ Lines Swapped Within Data Byte

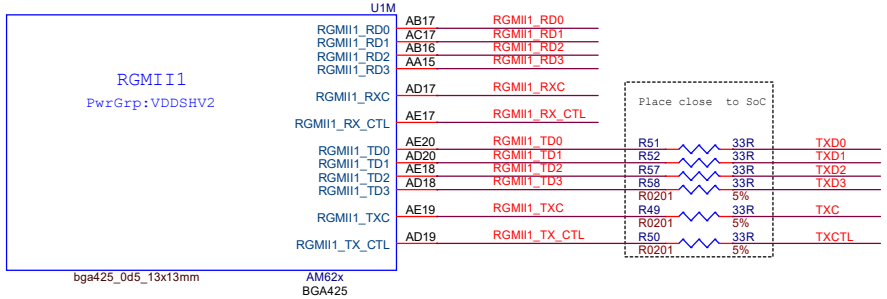



eMMC/SD
MMC0
PwrGrp:VDDSHV4
1.8V
MMC1
PwrGrp:VDDSHV5
1.8V
GENERAL
PwrGrp:VDDSHV0
3.3V
MMC2
PwrGrp:VDDSHV6
1.8V
U1I
MMC0_CLK
MMC0_DAT0
MMC0_DAT1
MMC0_DAT2
MMC0_DAT3
MMC0_DAT4
MMC0_DAT5
MMC0_DAT6
MMC0_DAT7
MMC0_CMD
MMC1_CLK
MMC1_DAT0
MMC1_DAT1
MMC1_DAT2
MMC1_DAT3
MMC1_CMD
MMC1_SDCD
MMC1_SDWP
MMC2_CLK
MMC2_DAT0
MMC2_DAT1
MMC2_DAT2
MMC2_DAT3
MMC2_CMD
MMC2_SDCD
MMC2_SDWP
bga425_0d5_13x13mm
AM62x
BGA425
VDD_1V8
EMMC_CLK
EMMC_D0
EMMC_D1
EMMC_D2
EMMC_D3
EMMC_D4
EMMC_D5
EMMC_D6
EMMC_D7
EMMC_CMD
SD_CLK
SD_D0
SD_D1
SD_D2
SD_D3
SD_CMD
SD_CD
SD_VOLT_SEL
WL_CLK
WL_D0
WL_D1
WL_D2
WL_D3
WL_CMD
GPIO1_48
GPIO1_49
GPIO0_71
GPIO0_72
R5 49.9K 1% R0402
R6 49.9K 1% R0402
R7 49.9K 1% R0402
R8 49.9K 1% R0402
R9 49.9K 1% R0402
R10 49.9K 1% R0402
R11 49.9K 1% R0402
R12 49.9K 1% R0402
R13 49.9K 1% R0402
R14 10K 1% R0402
R80 0R 1% R0201
R79 33R 5% R0201
R251 10K 1% R0402
R16 0R 1% R0201
EMMC_CLK_S
RESETSTATz
EMMC_RSTn
PORz_OUT
D48
D56
D47
RB520S30T1G
SOD-523
200mA
30V
TP1
R20 49.9K 1% R0402
R15 10K 1% R0402
eMMC
U3-1
DAT0
DAT1
DAT2
DAT3
DAT4
DAT5
DAT6
DAT7
CMD
CLK
RCLK
RST_n
VCC_E6
VCC_F5
VCC_J10
VCC_K9
VCCQ_C6
VCCQ_M4
VCCQ_N4
VCCQ_P3
VCCQ_P5
VDDI
VSF1
VSF2
VSF3
VSF4
VSS_E7
VSS_G5
VSS_H10
VSS_K8
VSS_J5
VSS_A6
VSSQ_C4
VSSQ_N2
VSSQ_N5
VSSQ_P4
VSSQ_P6
EMMC16G-TB29-PZ90
VFBGA-153
VDD_3V3
VDD_1V8
C65 100nF C0201 10V
C66 100nF C0201 10V
C67 100nF C0201 10V
C68 100nF C0201 10V
C69 4.7uF C0402 6.3V
C70 100nF C0201 10V
C71 100nF C0201 10V
C72 100nF C0201 10V
C73 100nF C0201 10V
C74 100nF C0201 10V
C75 4.7uF C0402 6.3V
C76 100nF C0201 10V
U3-2
EMMC16G-TB29-PZ90
VFBGA-153
MICRO SD CARD
VDD_3V3
VDD_SD
VDD_3V3_SD
R60 10K 1% R0402
SD_PWR_EN
RESETSTATz
D50
D51
RB520S30T1G
SOD-523
200mA
30V
U21
VIN
VOUT
ON
CT
GND
QOD
TPS22918DBVR
SOT23-6
C57 22uF 25V C0805
C51 22uF 25V C0805
R21 10K 1% R0402
R77 10K 1% R0402
R76 10K 1% R0402
R78 10K 1% R0402
R75 10K 1% R0402
R74 10K 1% R0402
R73 10K 1% R0402
SD_D2
SD_D3
SD_CMD
SD_CLK
SD_D0
SD_D1
SD_CD
D18
D17
D16
D15
D14
D13
D9
TPD1E1B04DPY
X1SON(2)
3.6V
C77 4.7uF 6.3V C0402
C78 100nF 10V C0201
C79 100nF 10V C0201
J1
DATA2
CS
DI
VDD
SCLK
VSS
DO
DATA1
CDN
ST-TF-003J
MICRO_SD_ST_TF_003A_16_1x14_5mm

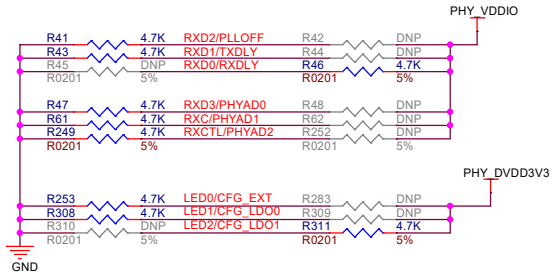
Gigabit Ethernet



SoC MAC 1.8V

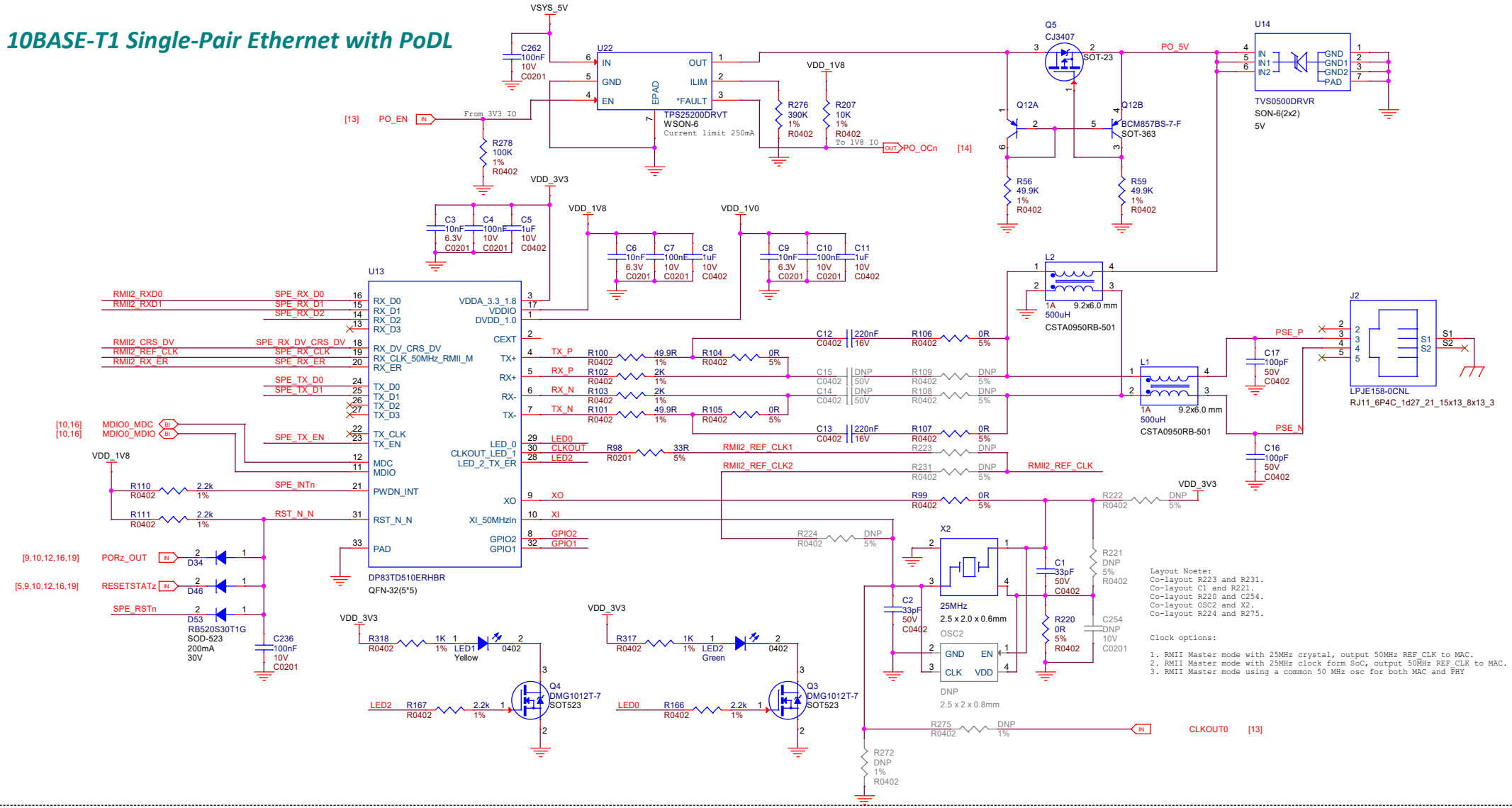


Strap options

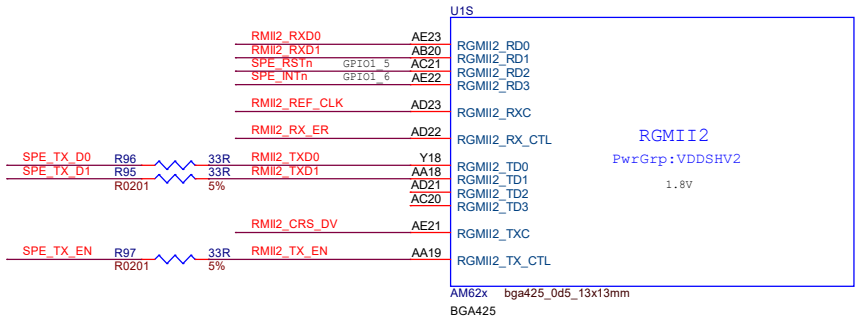



```
CFG_EXT = 0, CFG_LDO[1:0] = 10 :   Internal 1.8V
PHY_AD[2:0] = 000:                 PHY Addr 000
PHY_PLLOF = 0 :                    Enable PLL
PHY_TXDLY = 0 :                    TX delay: 0ns
PHY_RXDLY = 1 :                    RX delay: 2ns
```

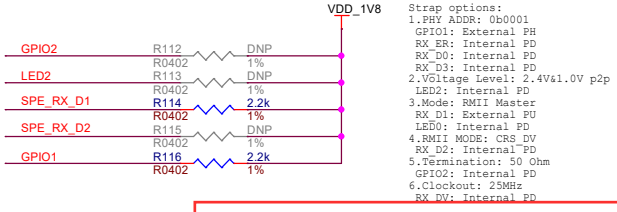
10BASE-T1 Single-Pair Ethernet with PoDL



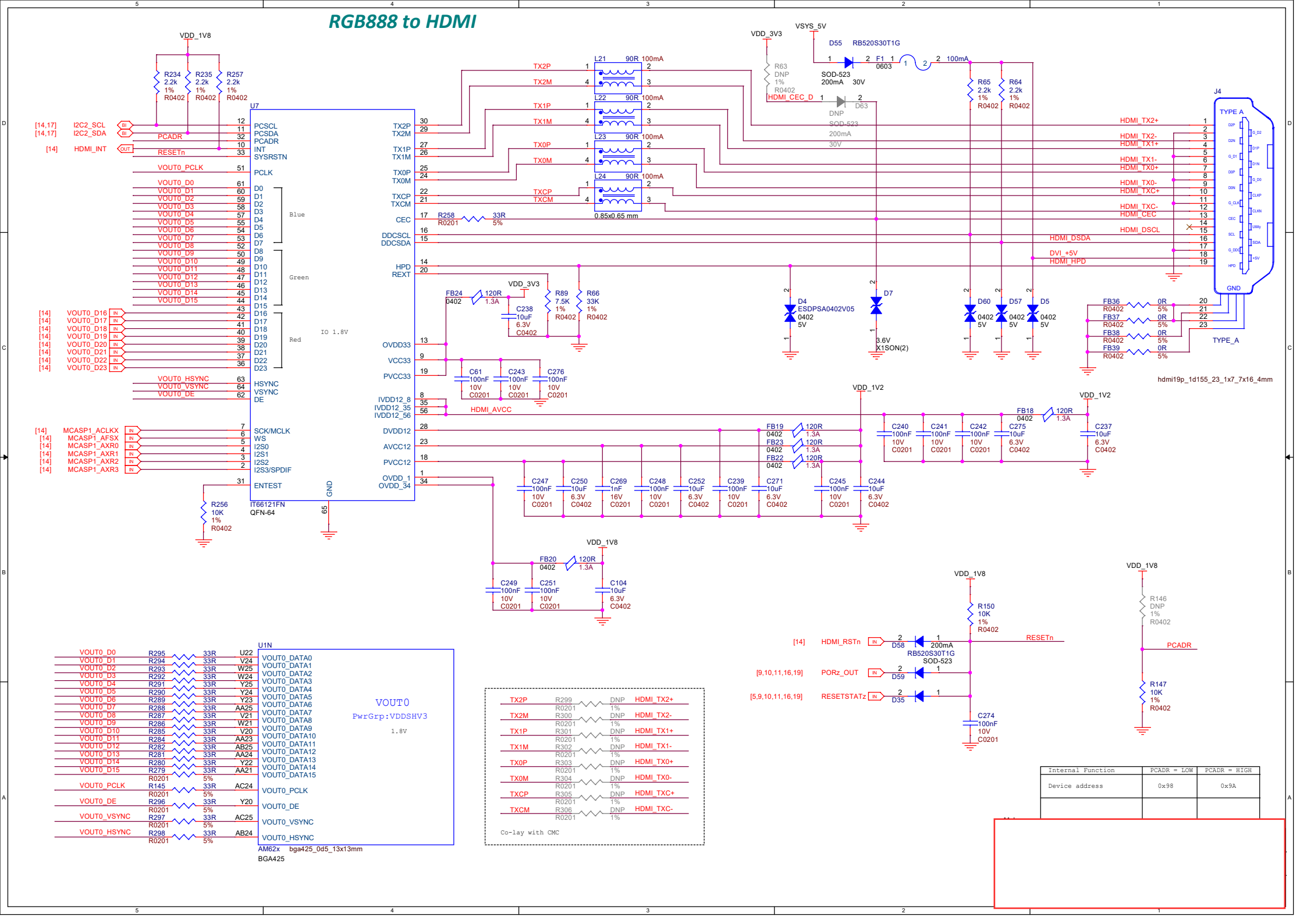
SoC MAC 1.8V



Strap options

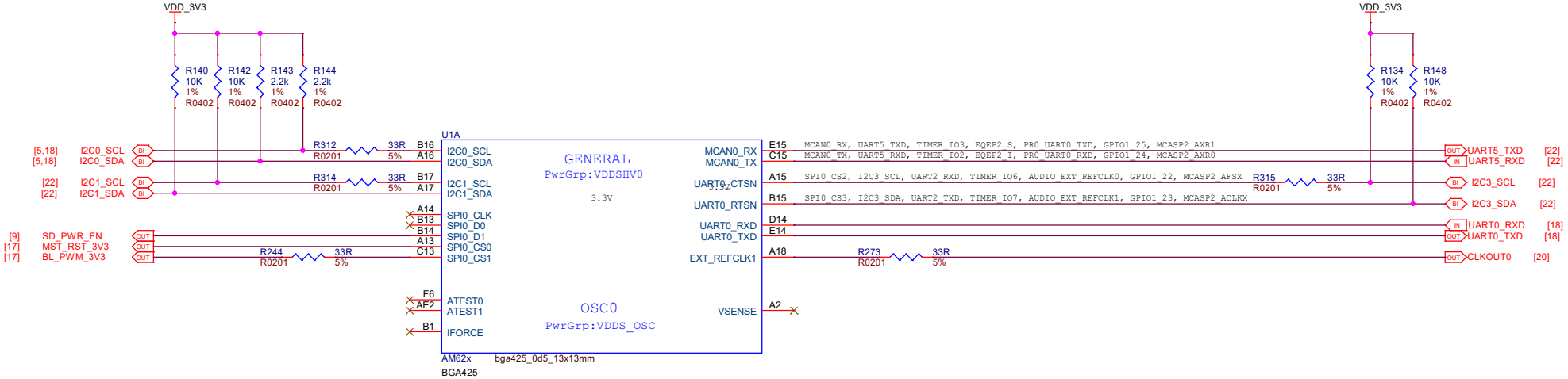


RGB888 to HDMI

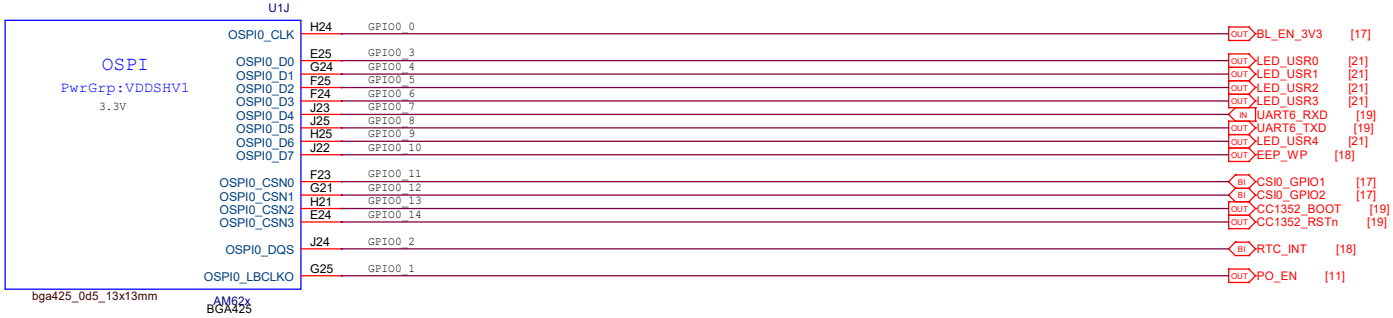


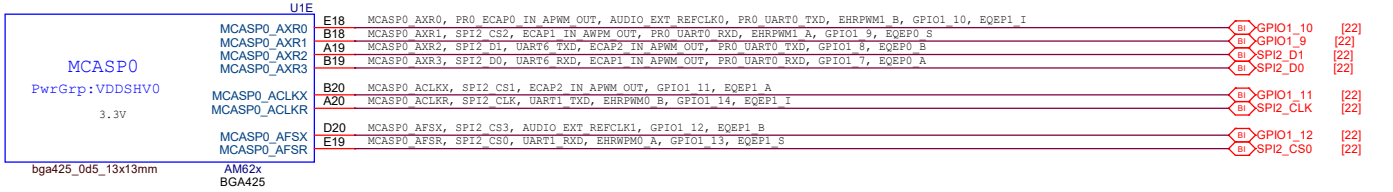
Internal Function	PCADR = LOW	PCADR = HIGH
Device address	0x98	0x9A

SoC GPIO 3.3V

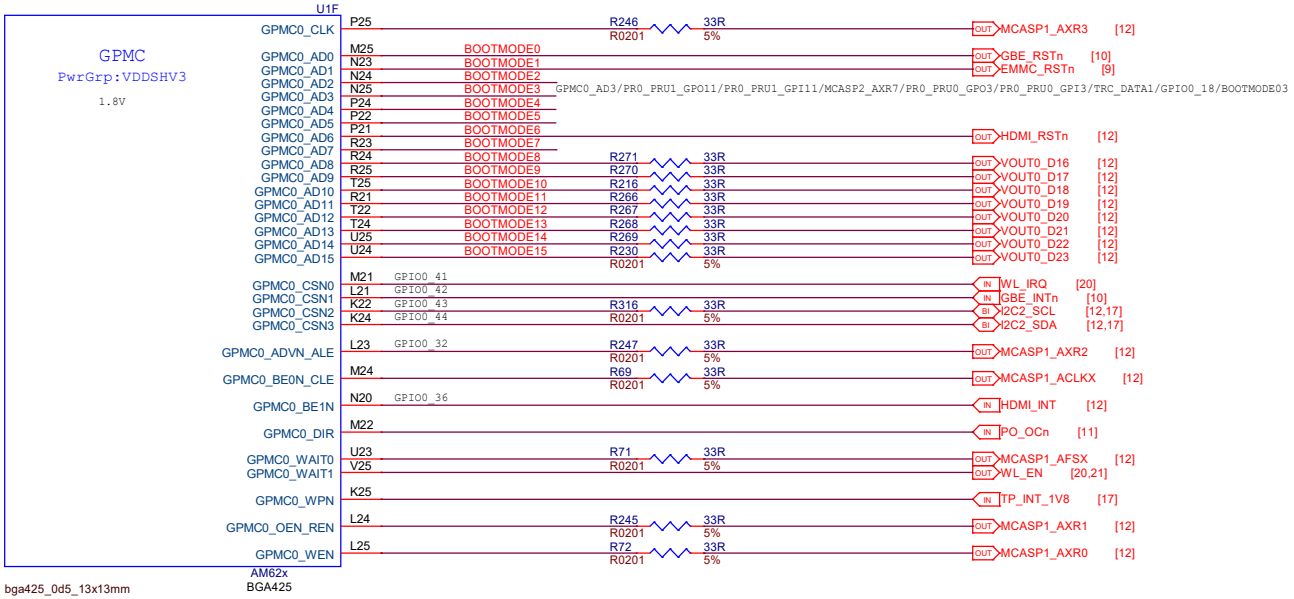


SoC GPIO 3.3V

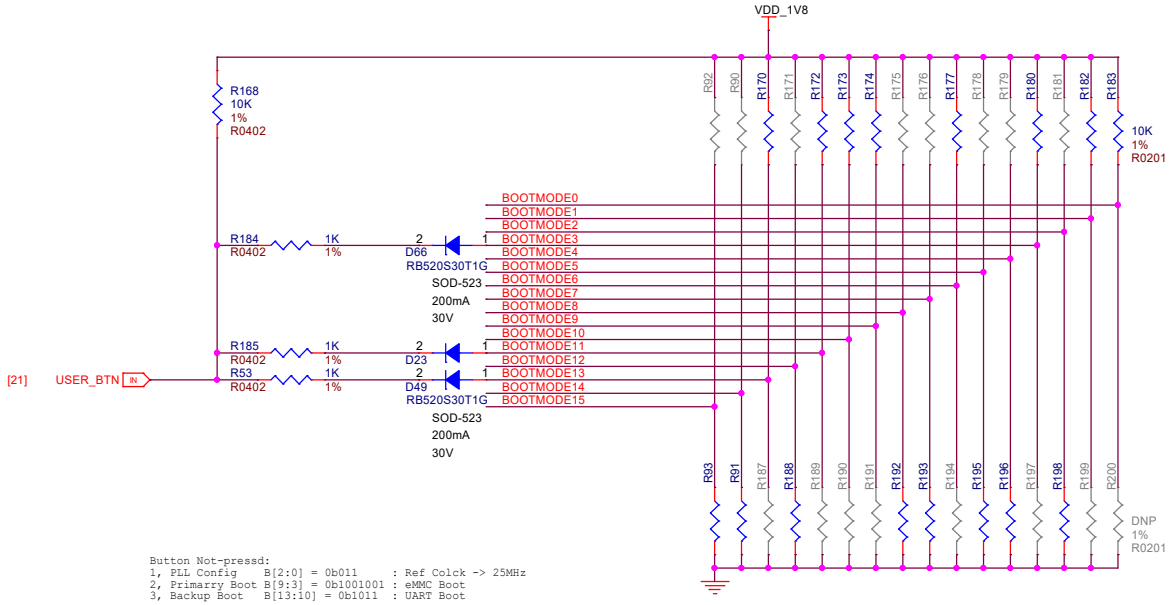




SoC GPIO 1.8V



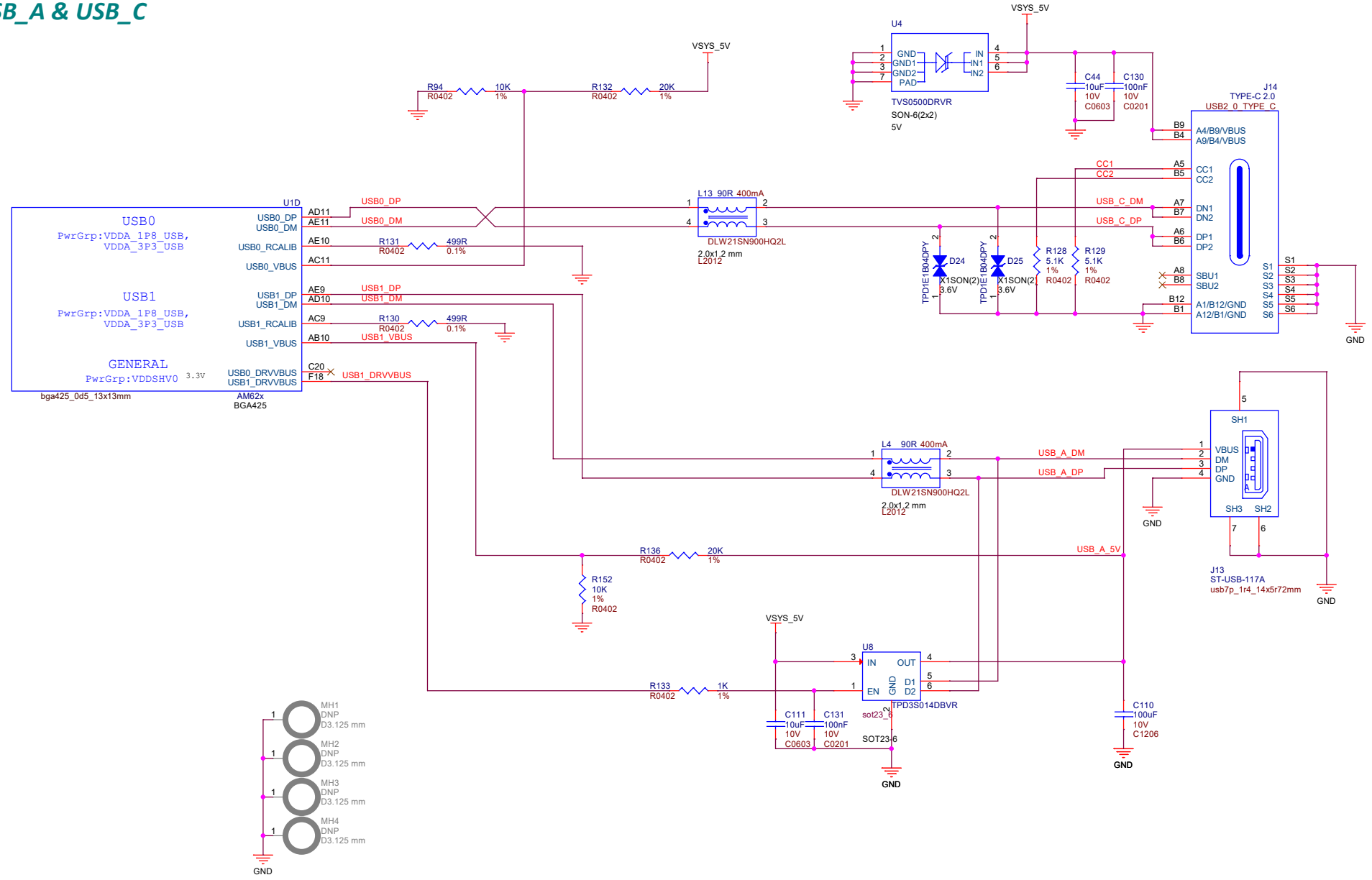
Bootstrap



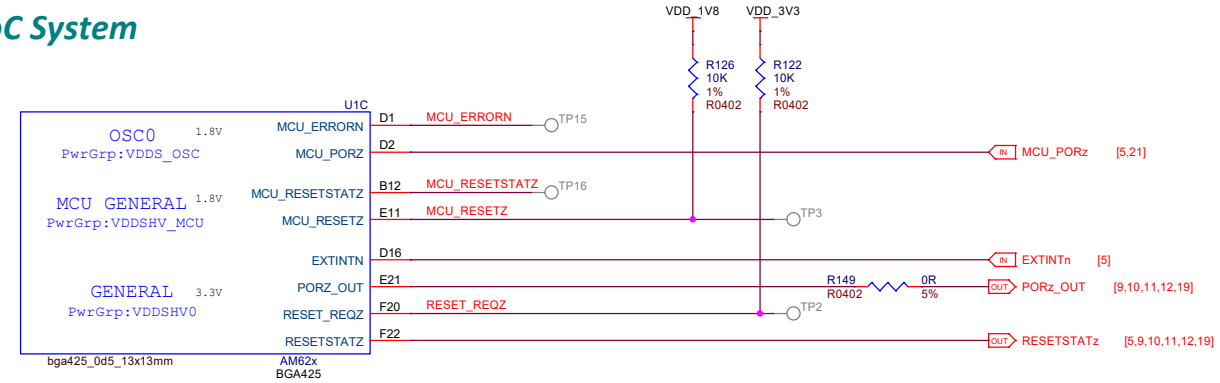

```
Button Not-pressd:
1, PLL Config    B[2:0] = 0b011     : Ref Colck -> 25MHz
2, Primarry Boot B[9:3] = 0b1001001 : eMMC Boot
3, Backup Boot   B[13:10] = 0b1011  : UART Boot

Button Pressd:
1, PLL Config    B[2:0] = 0b011     : Ref Colck -> 25MHz
2, Primarry Boot B[9:3] = 0b1001000 : SDCard FS Boot
3, Backup Boot   B[13:10] = 0b0001  : USB DFU Boot
```

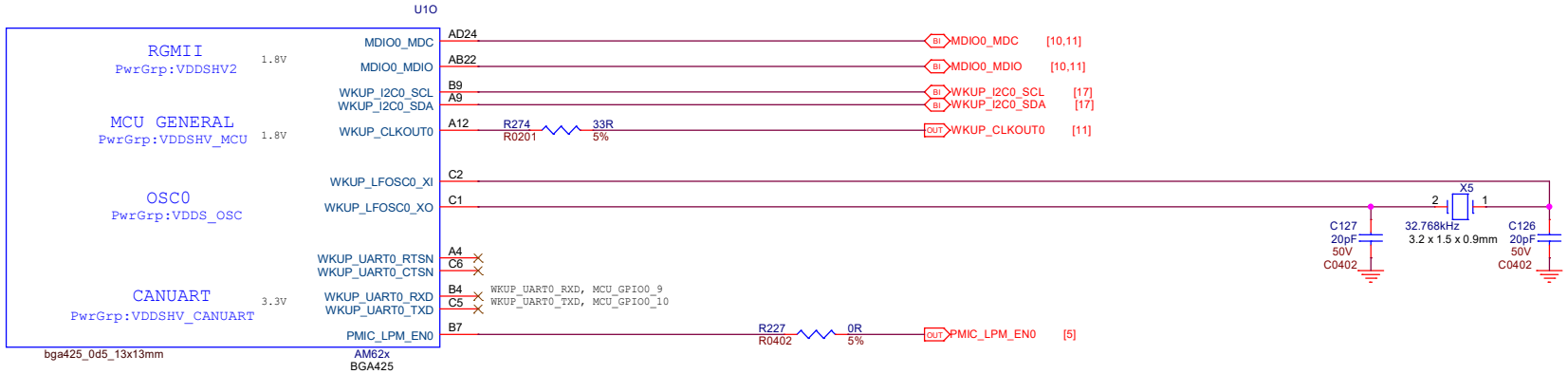
USB_A & USB_C


U1D
USB0
PwrGrp:VDDA_1P8_USB,
VDDA_3P3_USB
USB1
PwrGrp:VDDA_1P8_USB,
VDDA_3P3_USB
GENERAL
PwrGrp:VDDSHV0
3.3V
USB0_DP
USB0_DM
USB0_RCALIB
USB0_VBUS
USB1_DP
USB1_DM
USB1_RCALIB
USB1_VBUS
USB0_DRVVBUS
USB1_DRVVBUS
AD11
AE11
AE10
AC11
AE9
AD10
AC9
AB10
C20
F18
bga425_0d5_13x13mm
AM62x
BGA425
VSYS_5V
R94
R0402
10K
1%
R132
R0402
20K
1%
R131
R0402
499R
0.1%
R130
R0402
499R
0.1%
R136
R0402
20K
1%
R152
10K
1%
R0402
R133
R0402
1K
1%
L13 90R 400mA
DLW21SN900HQ2L
2.0x1.2 mm
L2012
L4 90R 400mA
DLW21SN900HQ2L
2.0x1.2 mm
L2012
U4
GND
GND1
GND2
PAD
IN
IN1
IN2
TVS0500DRVR
SON-6(2x2)
5V
C44
10uF
10V
C0603
C130
100nF
10V
C0201
J14
TYPE-C 2.0
USB2_0_TYPE_C
A4/B9/VBUS
A9/B4/VBUS
CC1
CC2
DN1
DN2
DP1
DP2
SBU1
SBU2
A1/B12/GND
A12/B1/GND
USB_C_DM
USB_C_DP
D24
D25
TPD1E1B04DPY
X1SON(2)
3.6V
R128
5.1K
1%
R0402
R129
5.1K
1%
R0402
SH1
VBUS
DM
DP
GND
SH3
SH2
J13
ST-USB-117A
usb7p_1r4_14x5r72mm
USB_A_DM
USB_A_DP
USB_A_5V
U8
IN
OUT
EN
GND
D1
D2
TPD3S014DBVR
sot23_6
SOT23-6
C111
10uF
10V
C0603
C131
100nF
10V
C0201
C110
100uF
10V
C1206
MH1
MH2
MH3
MH4
DNP
D3.125 mm
GND

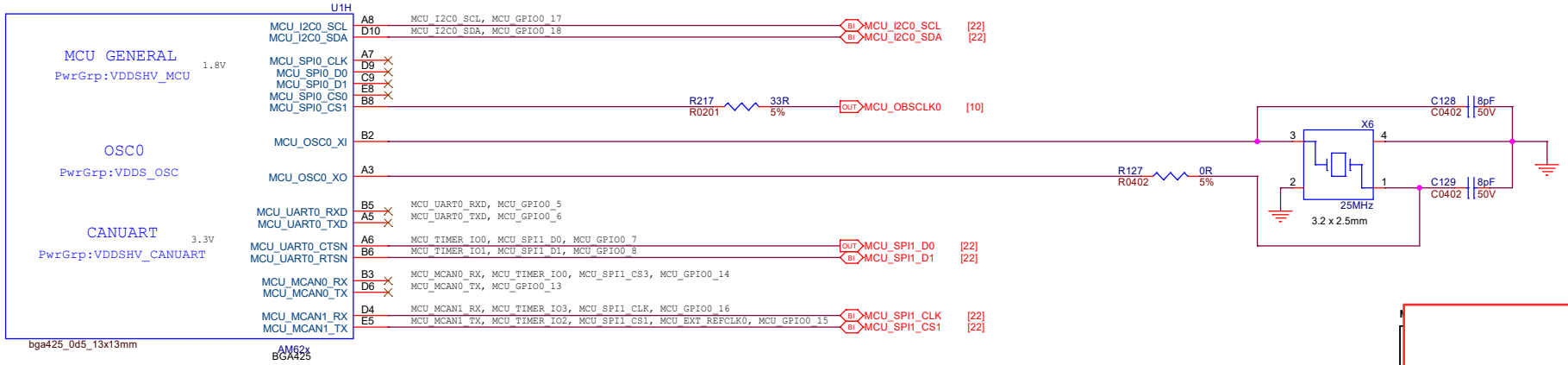
SoC System



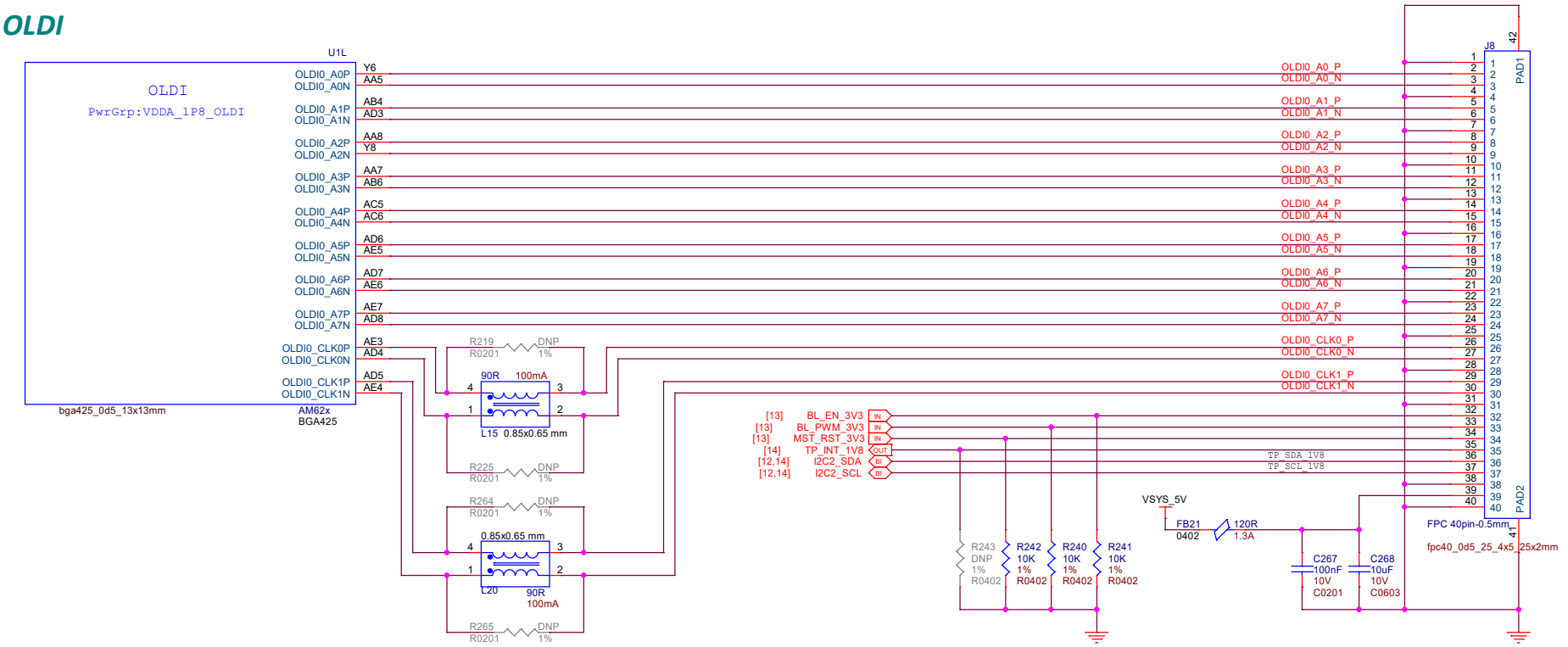
WKUP Domain



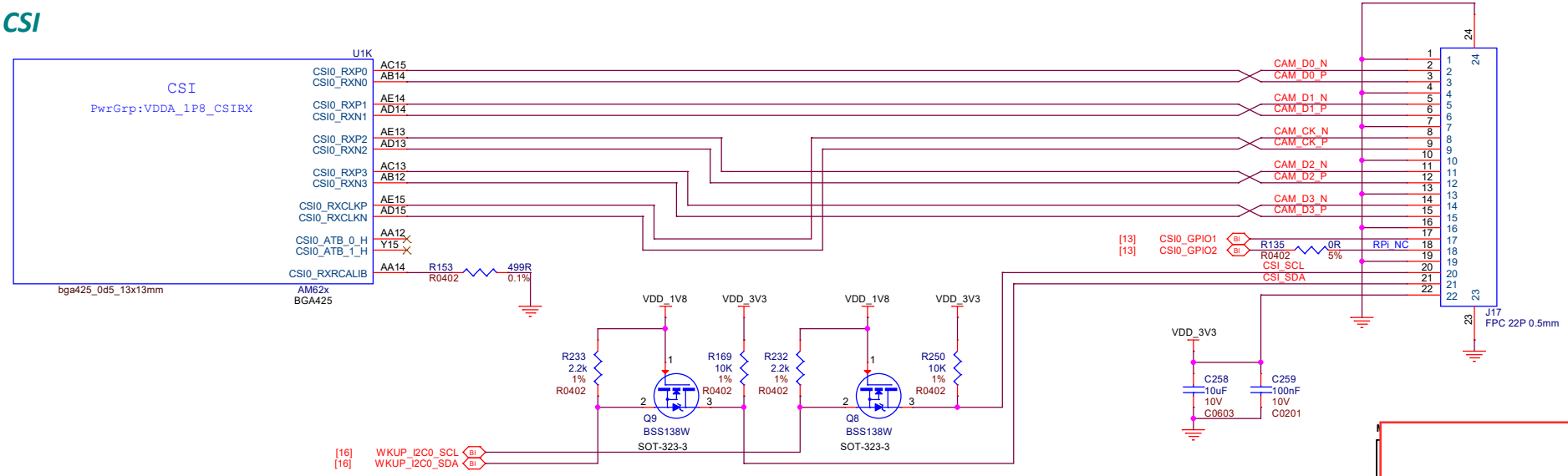
MCU Domain



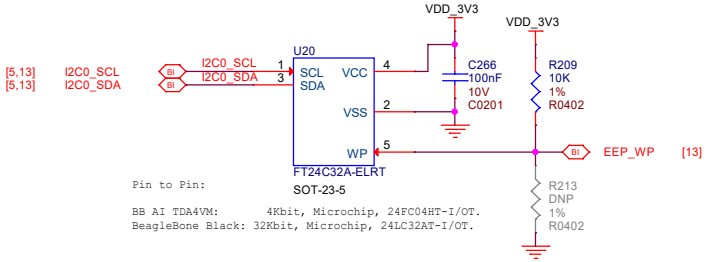
OLDI


U1L
OLDI
PwrGrp:VDDA_1P8_OLDI
OLDI0_A0P
OLDI0_A0N
OLDI0_A1P
OLDI0_A1N
OLDI0_A2P
OLDI0_A2N
OLDI0_A3P
OLDI0_A3N
OLDI0_A4P
OLDI0_A4N
OLDI0_A5P
OLDI0_A5N
OLDI0_A6P
OLDI0_A6N
OLDI0_A7P
OLDI0_A7N
OLDI0_CLK0P
OLDI0_CLK0N
OLDI0_CLK1P
OLDI0_CLK1N
Y6
AA5
AB4
AD3
AA8
Y8
AA7
AB6
AC5
AC6
AD6
AE5
AD7
AE6
AE7
AD8
AE3
AD4
AD5
AE4
bga425_0d5_13x13mm
AM62x
BGA425
R219
R0201
DNP
1%
90R 100mA
L15 0.85x0.65 mm
R225
R264
0.85x0.65 mm
L20
R265
OLDI0_A0_P
OLDI0_A0_N
OLDI0_A1_P
OLDI0_A1_N
OLDI0_A2_P
OLDI0_A2_N
OLDI0_A3_P
OLDI0_A3_N
OLDI0_A4_P
OLDI0_A4_N
OLDI0_A5_P
OLDI0_A5_N
OLDI0_A6_P
OLDI0_A6_N
OLDI0_A7_P
OLDI0_A7_N
OLDI0_CLK0_P
OLDI0_CLK0_N
OLDI0_CLK1_P
OLDI0_CLK1_N
[13] BL_EN_3V3
[13] BL_PWM_3V3
[13] MST_RST_3V3
[14] TP_INT_1V8
[12,14] I2C2_SDA
[12,14] I2C2_SCL
TP_SDA_1V8
TP_SCL_1V8
R243
R242
R240
R241
10K
1%
R0402
VSYS_5V
FB21
0402
120R
1.3A
C267
100nF
10V
C0201
C268
10uF
10V
C0603
J8
PAD1
PAD2
FPC 40pin-0.5mm
fpc40_0d5_25_4x5_25x2mm

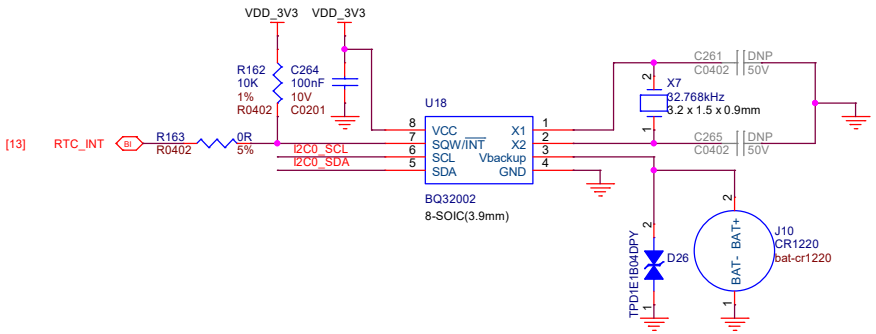
CSI


U1K
CSI
PwrGrp:VDDA_1P8_CSIRX
CSI0_RXP0
CSI0_RXN0
CSI0_RXP1
CSI0_RXN1
CSI0_RXP2
CSI0_RXN2
CSI0_RXP3
CSI0_RXN3
CSI0_RXCLKP
CSI0_RXCLKN
CSI0_ATB_0_H
CSI0_ATB_1_H
CSI0_RXRCALIB
AC15
AB14
AE14
AD14
AE13
AD13
AC13
AB12
AE15
AD15
AA12
Y15
AA14
bga425_0d5_13x13mm
AM62x
BGA425
R153
R0402
499R
0.1%
CAM_D0_N
CAM_D0_P
CAM_D1_N
CAM_D1_P
CAM_CK_N
CAM_CK_P
CAM_D2_N
CAM_D2_P
CAM_D3_N
CAM_D3_P
[13] CSI0_GPIO1
[13] CSI0_GPIO2
R135
0R
5%
RPi_NC
CSI_SCL
CSI_SDA
VDD_1V8
VDD_3V3
R233
2.2k
1%
R169
10K
R232
R250
Q9
BSS138W
SOT-323-3
Q8
[16] WKUP_I2C0_SCL
[16] WKUP_I2C0_SDA
C258
10uF
10V
C0603
C259
100nF
10V
C0201
J17
FPC 22P 0.5mm

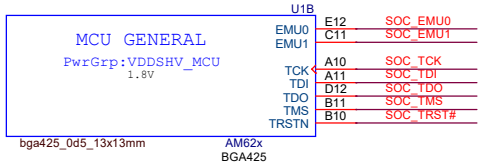
BOARD ID EEPROM 4Kbit



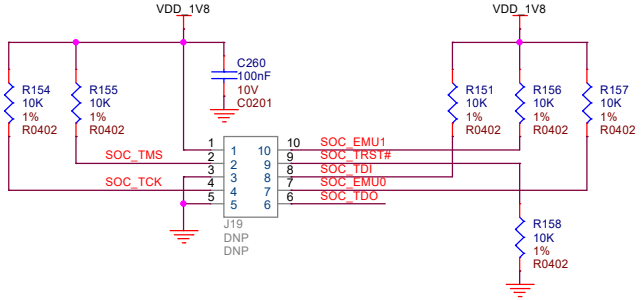
RTC



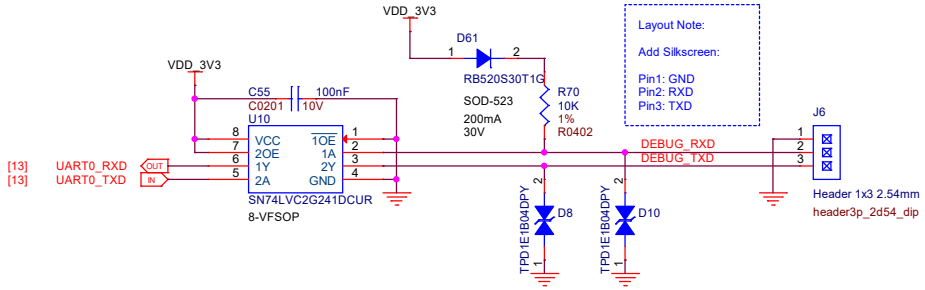
JTAG



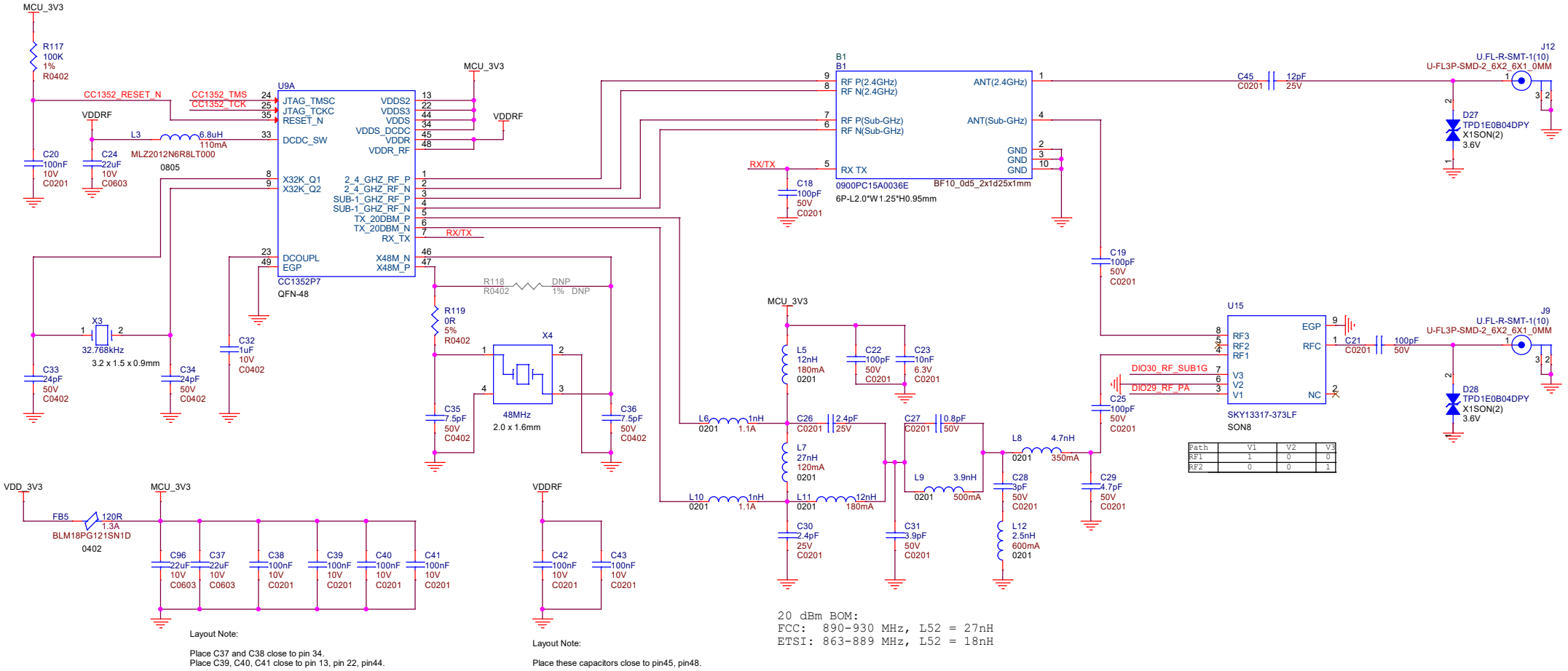
Tag-Connect



Debug



CC1352P RF



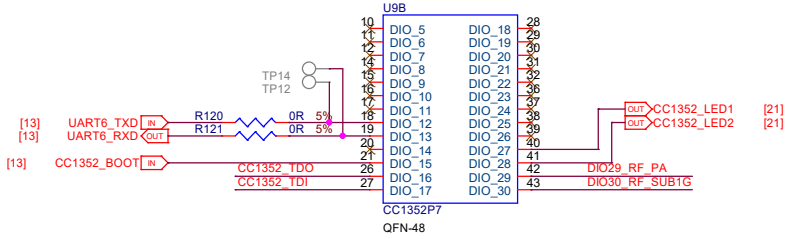
Layout Note:

Place C37 and C38 close to pin 34.
Place C39, C40, C41 close to pin 13, pin 22, pin44.

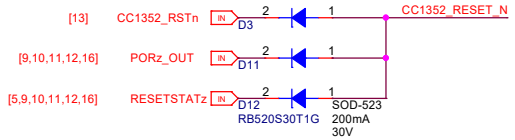
Layout Note:

Place these capacitors close to pin45, pin48.

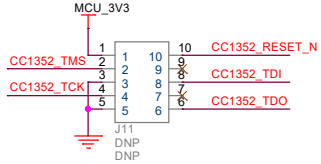
CC1352P GPIO



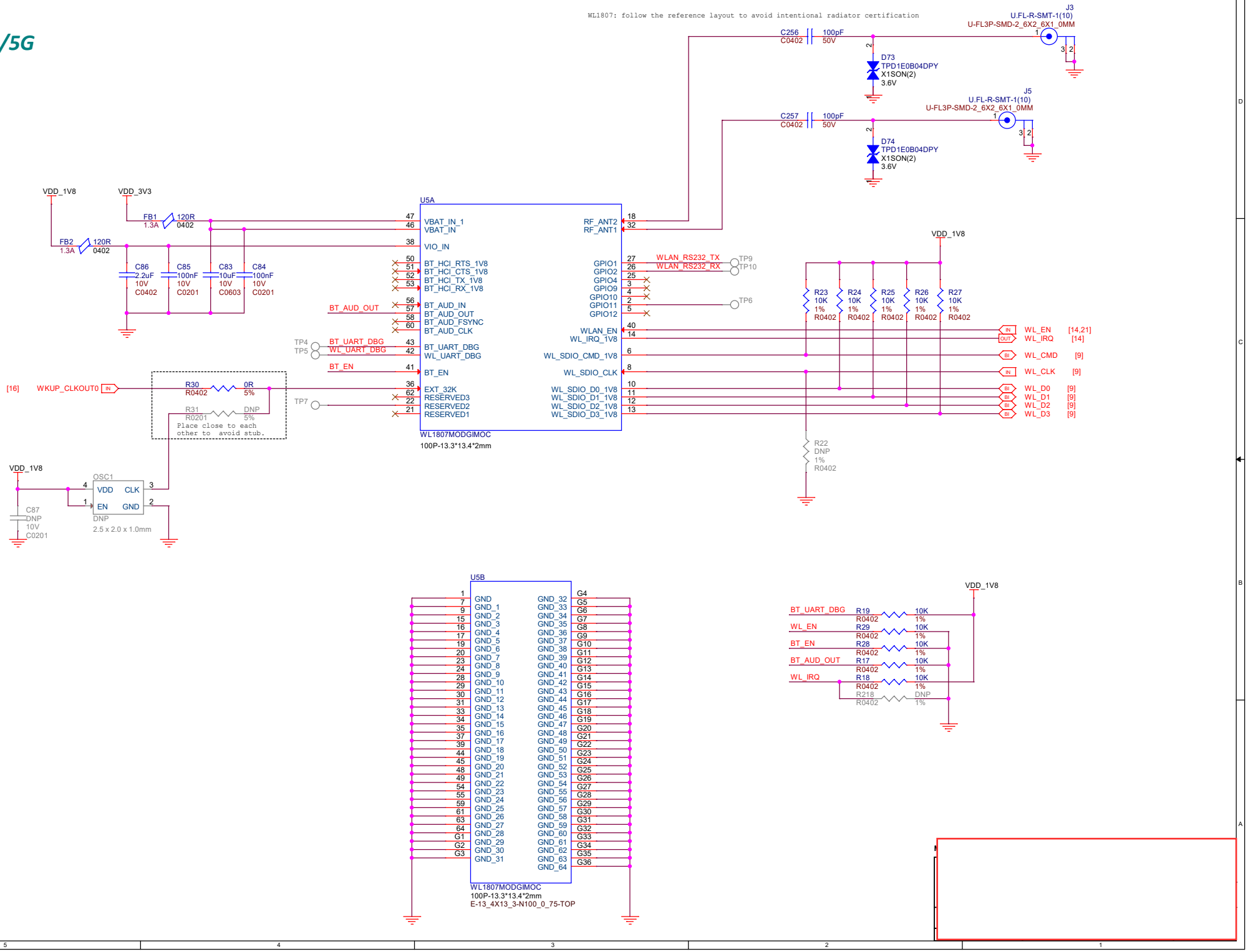
RESET



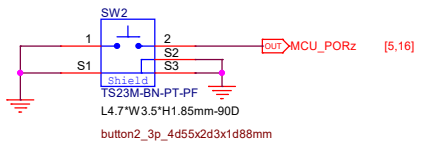
CC1352P JTAG Tag-Connect



WiFi 2.4G/5G

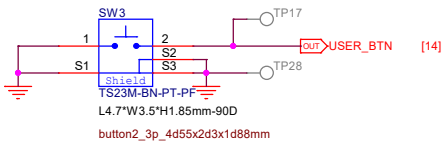

WL1807: follow the reference layout to avoid intentional radiator certification
J3
U.FL-R-SMT-1(10)
U-FL3P-SMD-2_6X2_6X1_0MM
C256
C0402
100pF
50V
D73
TPD1E0B04DPY
X1SON(2)
3.6V
J5
U.FL-R-SMT-1(10)
U-FL3P-SMD-2_6X2_6X1_0MM
C257
C0402
100pF
50V
D74
TPD1E0B04DPY
X1SON(2)
3.6V
VDD_1V8
VDD_3V3
FB1
1.3A
120R
0402
FB2
1.3A
120R
0402
C86
2.2uF
10V
C0402
C85
100nF
10V
C0201
C83
10uF
10V
C0603
C84
100nF
10V
C0201
U5A
VBAT_IN_1
VBAT_IN
VIO_IN
BT_HCI_RTS_1V8
BT_HCI_CTS_1V8
BT_HCI_TX_1V8
BT_HCI_RX_1V8
BT_AUD_IN
BT_AUD_OUT
BT_AUD_FSYNC
BT_AUD_CLK
BT_UART_DBG
WL_UART_DBG
BT_EN
EXT_32K
RESERVED3
RESERVED2
RESERVED1
RF_ANT2
RF_ANT1
GPIO1
GPIO2
GPIO4
GPIO9
GPIO10
GPIO11
GPIO12
WLAN_EN
WL_IRQ_1V8
WL_SDIO_CMD_1V8
WL_SDIO_CLK
WL_SDIO_D0_1V8
WL_SDIO_D1_1V8
WL_SDIO_D2_1V8
WL_SDIO_D3_1V8
WLAN_RS232_TX
WLAN_RS232_RX
TP9
TP10
TP6
TP4
TP5
TP7
BT_AUD_OUT
BT_UART_DBG
WL_UART_DBG
BT_EN
WL1807MODGIMOC
100P-13.3*13.4*2mm
[16]
WKUP_CLKOUT0
R30
R0402
0R
5%
R31
R0201
DNP
5%
Place close to each
other to avoid stub.
VDD_1V8
OSC1
VDD
CLK
EN
GND
DNP
2.5 x 2.0 x 1.0mm
C87
DNP
10V
C0201
R23
R24
R25
R26
R27
10K
1%
R0402
R22
DNP
1%
R0402
WL_EN
WL_IRQ
WL_CMD
WL_CLK
WL_D0
WL_D1
WL_D2
WL_D3
[14,21]
[14]
[9]
[9]
[9]
[9]
[9]
[9]
U5B
GND
GND_1
GND_2
GND_3
GND_4
GND_5
GND_6
GND_7
GND_8
GND_9
GND_10
GND_11
GND_12
GND_13
GND_14
GND_15
GND_16
GND_17
GND_18
GND_19
GND_20
GND_21
GND_22
GND_23
GND_24
GND_25
GND_26
GND_27
GND_28
GND_29
GND_30
GND_31
GND_32
GND_33
GND_34
GND_35
GND_36
GND_37
GND_38
GND_39
GND_40
GND_41
GND_42
GND_43
GND_44
GND_45
GND_46
GND_47
GND_48
GND_49
GND_50
GND_51
GND_52
GND_53
GND_54
GND_55
GND_56
GND_57
GND_58
GND_59
GND_60
GND_61
GND_62
GND_63
GND_64
WL1807MODGIMOC
100P-13.3*13.4*2mm
E-13_4X13_3-N100_0_75-TOP
VDD_1V8
BT_UART_DBG
WL_EN
BT_EN
BT_AUD_OUT
WL_IRQ
R19
R29
R28
R17
R18
R218
10K
1%
R0402
DNP

Reset Button

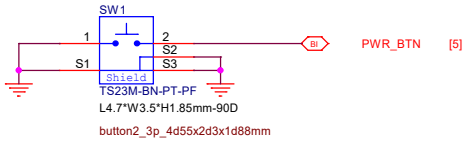


User Button

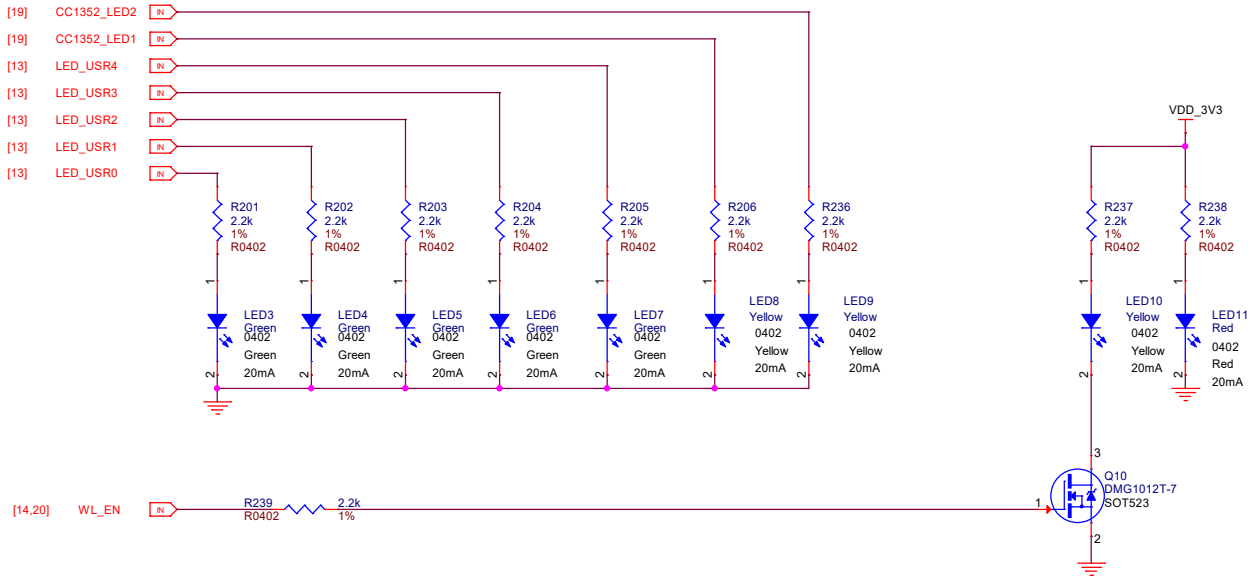
Layout Noete:
Place TP17 and TP28 close to each othter, keep 2.54mm spacing.



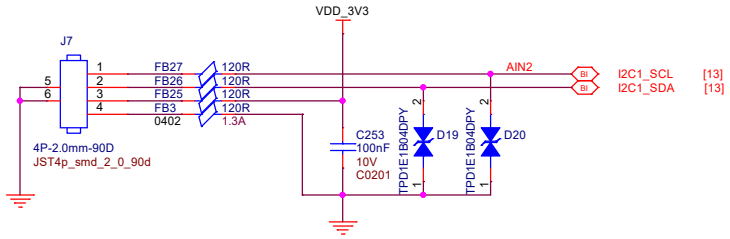
Power Button



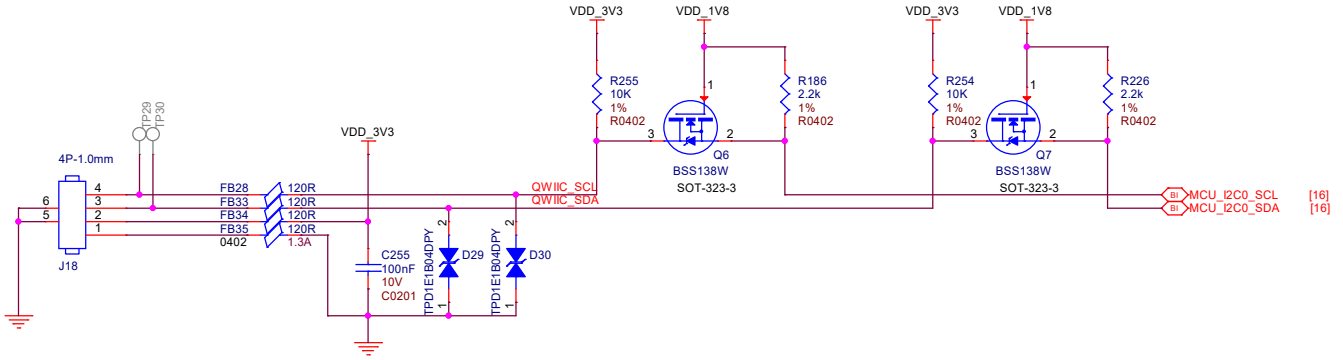
LEDs



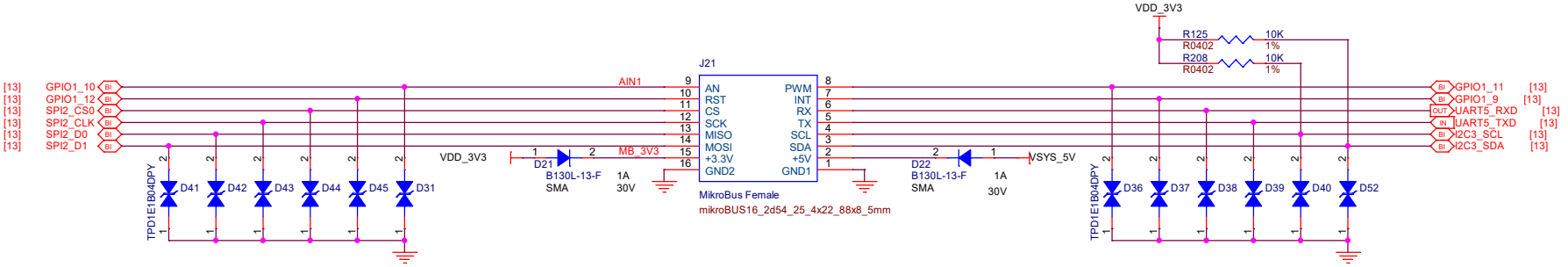
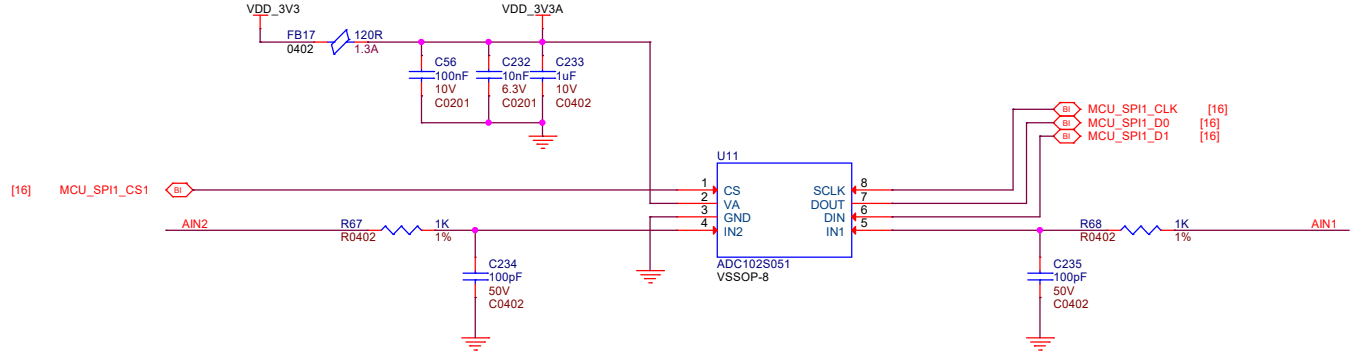
Grove Connector



QWIIC Connector



MikroBus



ADC