


PAGE LIST	
PAGE	PAGE NAME
001	Revision History
002	002_Block Diagram
003	003_Power Tree
004	004_I2C Usage Diagram
005	005_PMIC & Charger
006	006_SoC Power
007	007_SoC DCAPs
008	008_SoC DDR & DDR4
009	009_SoC MMC & eMMC & SD
010	010_SoC WKUP/MCU/System
011	011_SoC Bootstrap
012	012_SoC RGMII/OSPI
013	013_SoC GPIO/MCASP/VOUT
014	014_SoC USB/OLDI/CSI & USB C
015	015_SoC JTAG & MISC
016	016_BP P1 & P2

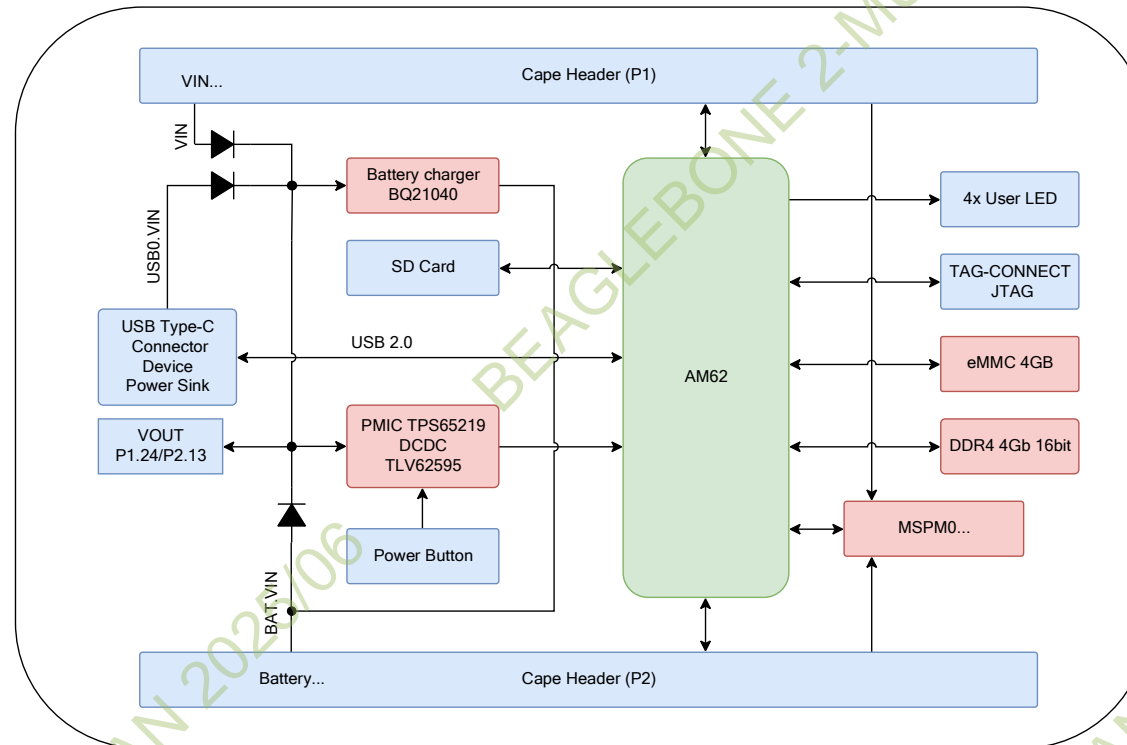
REVISION HISTORY			
VER #	DATE	DESCRIPTION OF CHANGES	AUTHOR
0.1	28 Oct. 2022	First release	qxn
0.2	1 Nov. 2022	1. Remove unused nets 2. Use WKUP_I2C0 for PMIC 3. Add pull-down to VPP 4. Move eMMC_RSTn to GPIO0_7 5. Add USER button for boot mode seletion 6. Try to add a CSI connector 7. Using processor boall numbers for signal name on P1 and P2 8. Change SoC to AM6232, change eMMC to 4GB, change DDR to 512MB	qxn
0.3	2 Nov. 2022	1. Remove CSI 2. Add SD card, move LDO0 to VDD_SD 3. Power the VPP with VDD_1V8 through a jumper 4. Connect P2.19 to AC20 instead	qxn
0.4	23 Dec.2022	1. Replace the ideal diode with LM73100RPWR 2. Replace EEPROM and ADC with MSPM0 3. Correct the boot settings 4. Connect AD24(MDC) to P2.17 and AB22(MDIO) to P2.20 which is same as pocketbeagle.	qxn

Main

		https://www.seeedstudio.com	
		Title: PocketBeagle2	
Size: A3	Document Number:	001_Revision History	Rev: v0.3
Draw By: qxn	Date: Friday, December 23, 2022	Sheet: 1 of 16	

BEAGLE BONE 2-MURAT IRKAN AM62xx TEXAS INSTRUMENTS

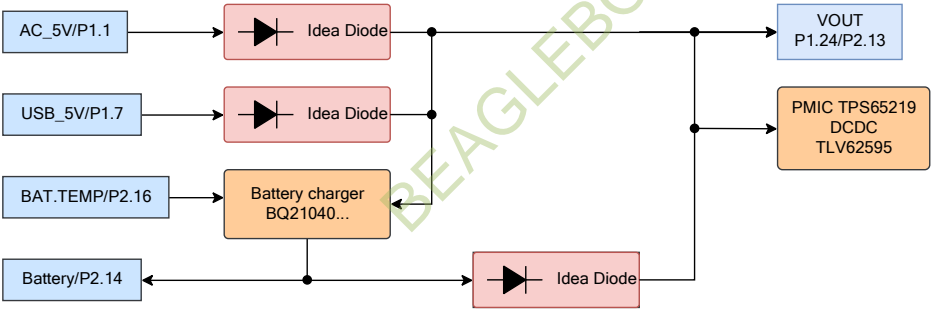
DEBIAN LINUX 12 /13-PC



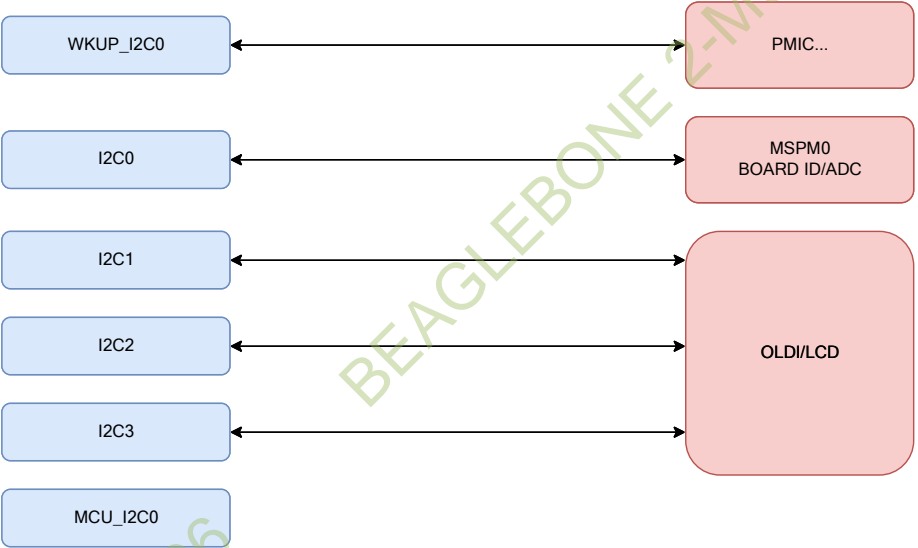
Main

seeed studio		https://www.seeedstudio.com	
Size: A3		Title: PocketBeagle2	
Document Number: 002_Block Diagram		Rev: v0.3	
Draw By: qxm		Date: Friday, December 23, 2022	
		Sheet: 2 of 16	

Power tree



I2C Usage Diagram

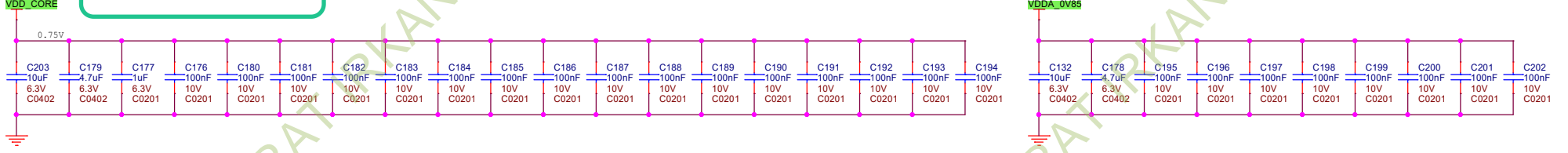


BATERİ VOLTAJ GİRİŞ

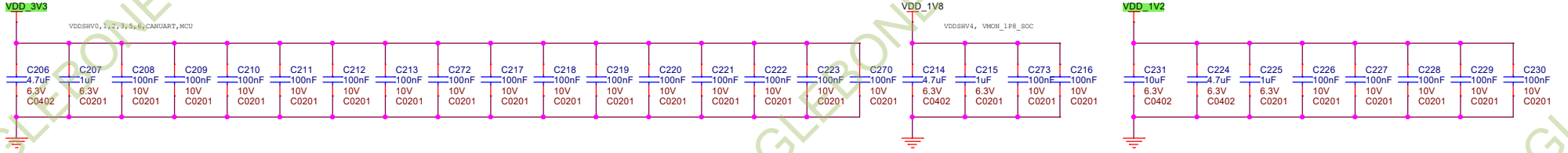
Battery Charger

Standard RISC Gateway AM62x Sitara™ Processors

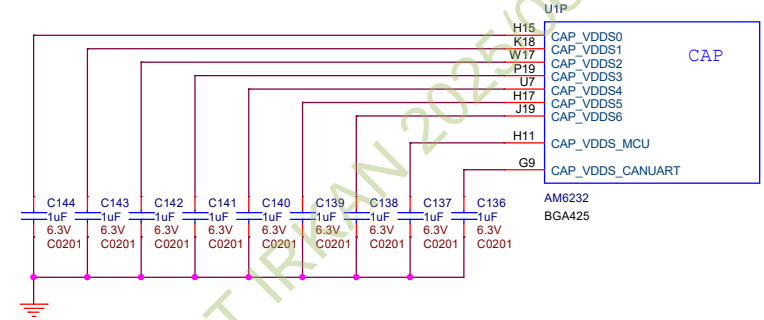
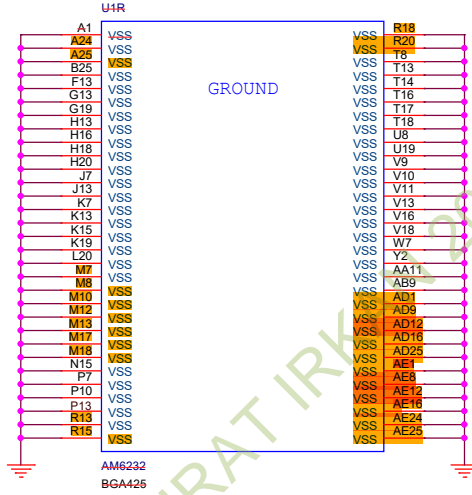
ARM-CPU ÇEKİRDEK VOLTAJI



3.3VOLT



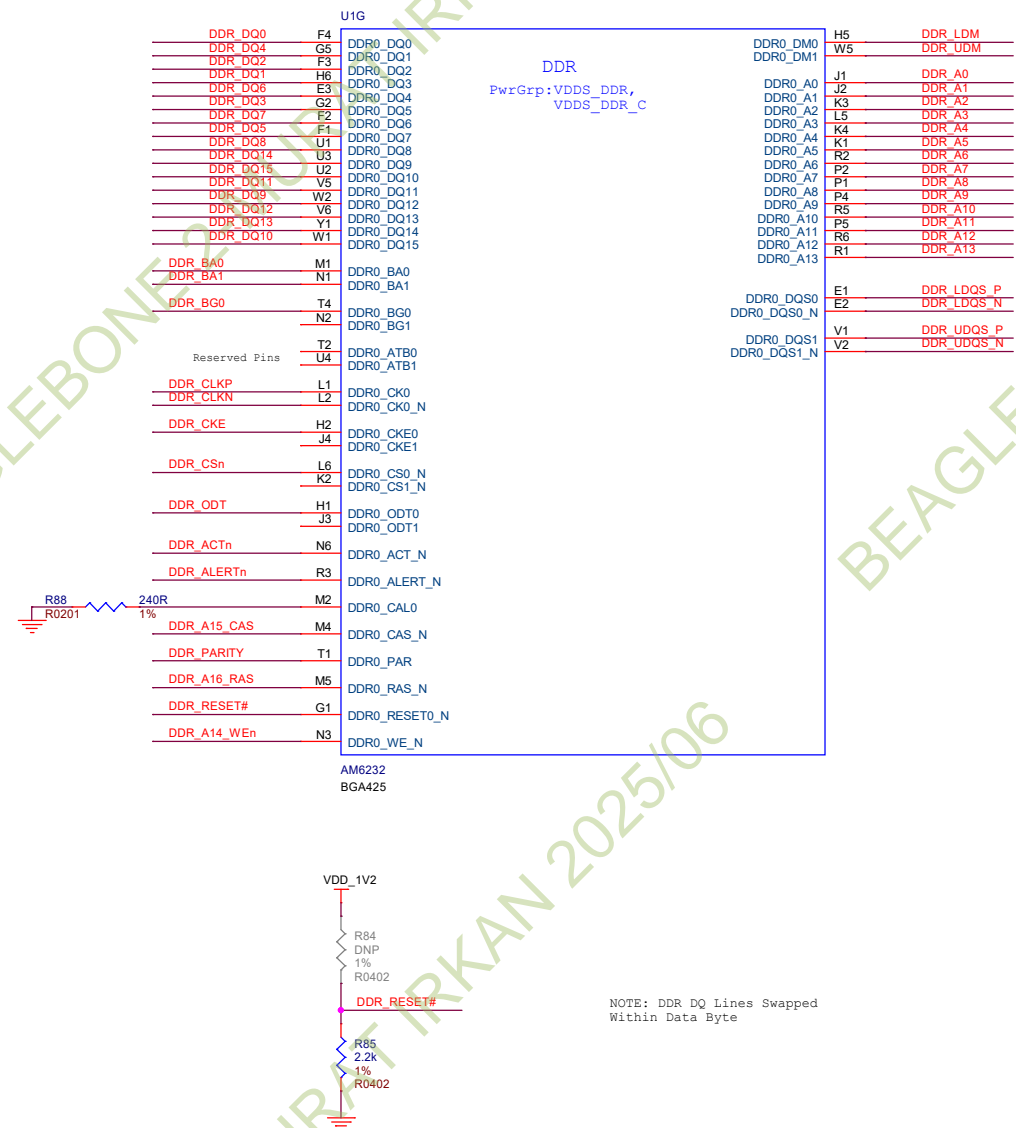
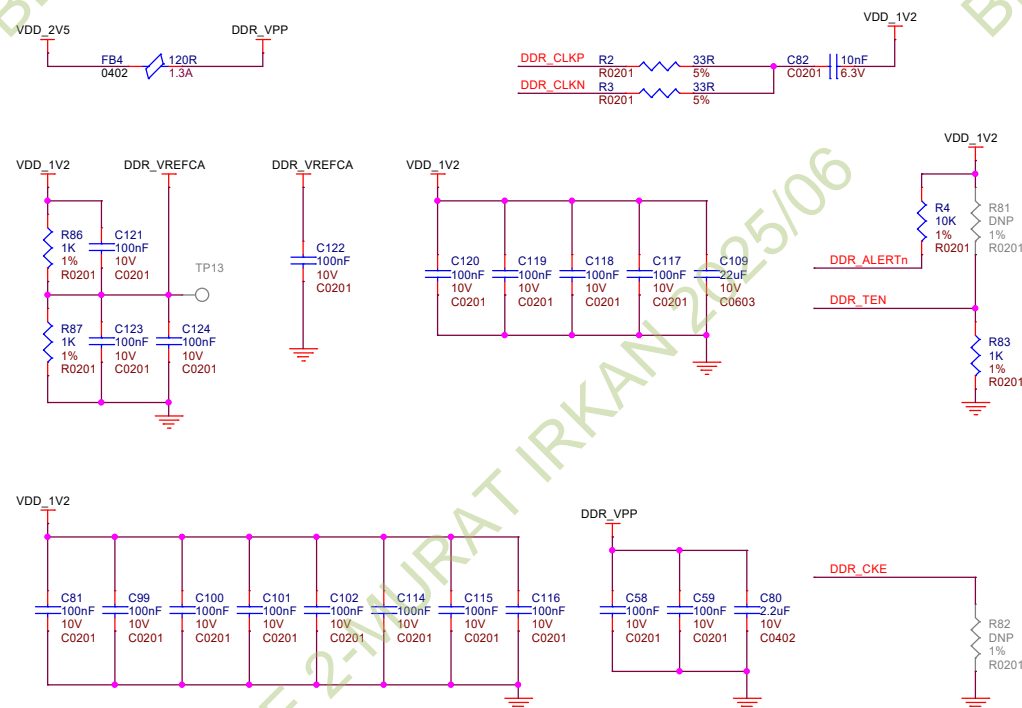
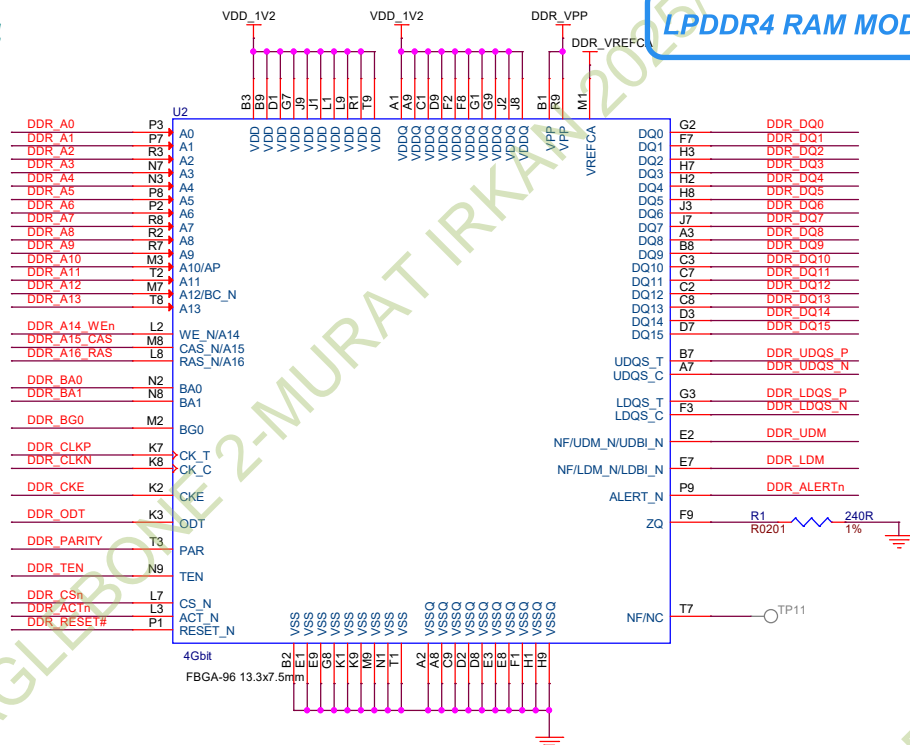
ARM-CPU TÜM ŞASELERİ (-)volt



DDR4

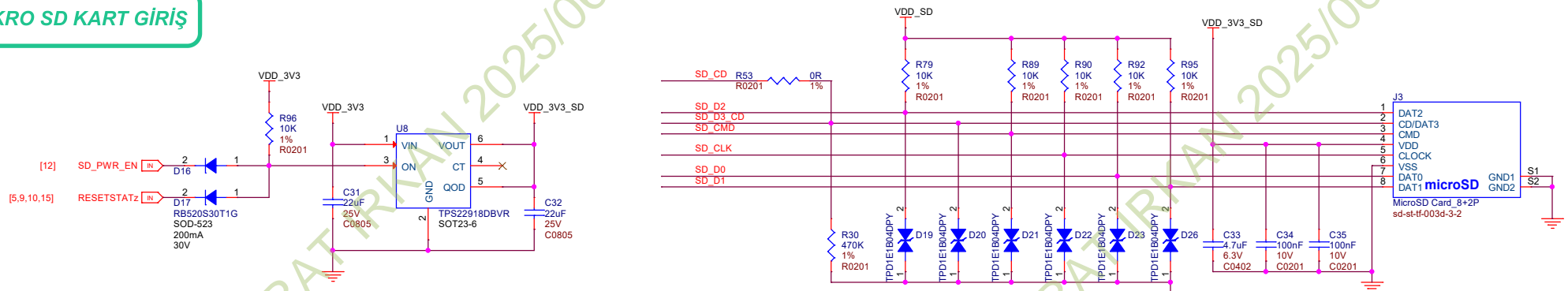
LPDDR4 RAM MODÜLÜ

SoC DDR controller

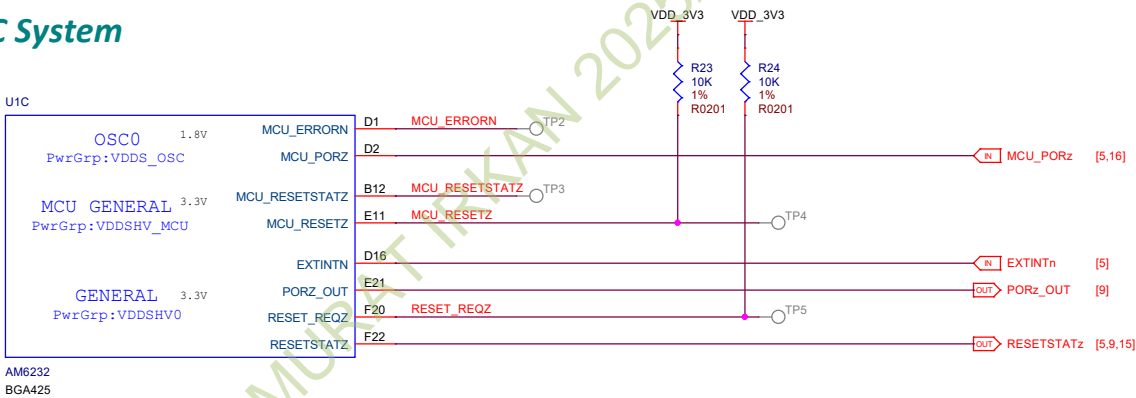


NOTE: DDR DQ Lines Swapped
Within Data Byte

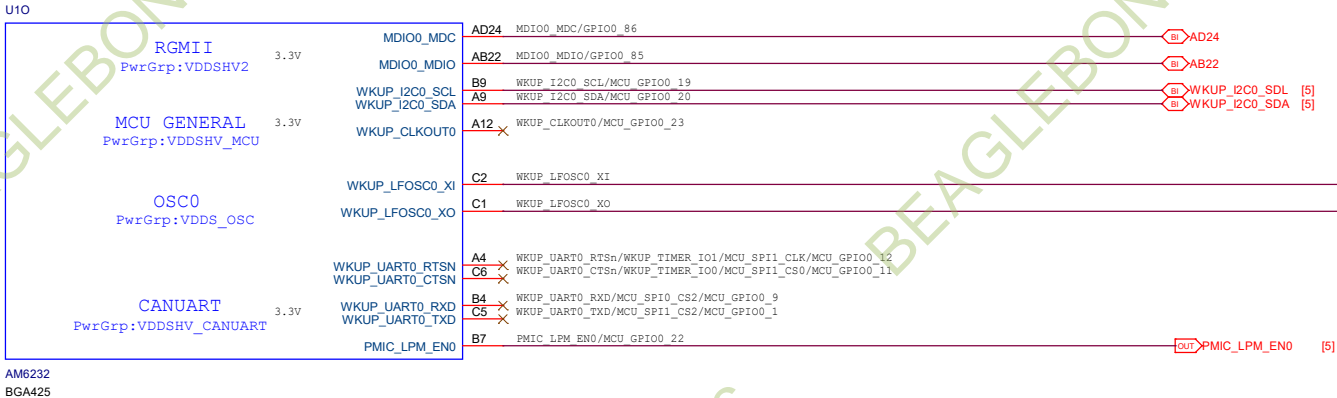
MULTİ-MEDYA KART GİRİŞLERİ



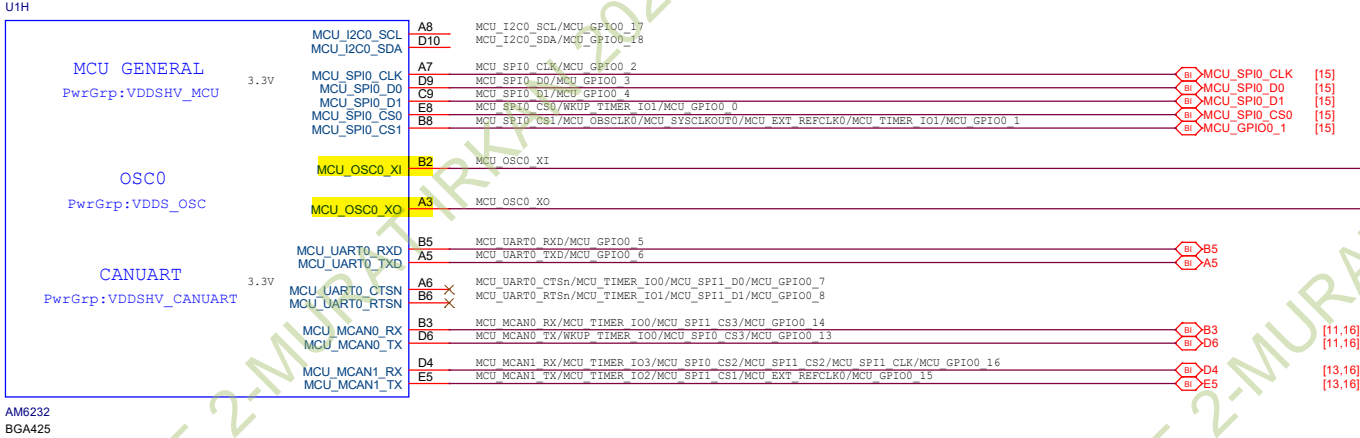
SoC System



WKUP Domain



MCU Domain

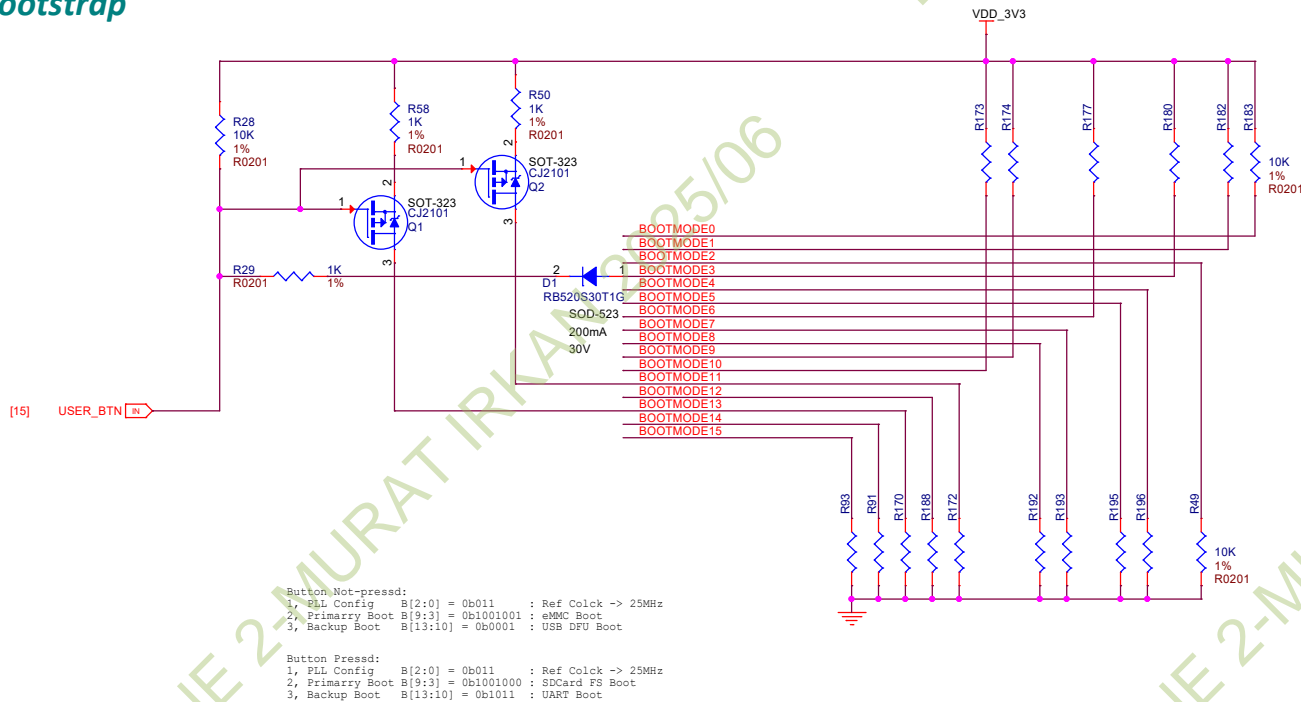


U1F

GPMC PwrGrp: VDDSHV3 3.3V	GPMCO_CLK	P25	GPMCO_CLK/MCASPI_AXR3/GPMCO_FCLK_MUX/PRO_PRU0_GPO8/PRO_PRU0_GPI8/TRC_DATA6/GPIO0_31		
	GPMCO_AD0	M25	GPMCO_AD0/PRO_PRU1_GPO8/PRO_PRU1_GPI8/MCASPI2_AXR4/PRO_PRU0_GPO0/PRO_PRU0_GPI0/TRC_CLK/GPIO0_15/BOOTMODE00		BOOTMODE0
	GPMCO_AD1	N23	GPMCO_AD1/PRO_PRU1_GPO9/PRO_PRU1_GPI9/MCASPI2_AXR5/PRO_PRU0_GPO1/PRO_PRU0_GPI1/TRC_CTL/GPIO0_17/BOOTMODE01		BOOTMODE1
	GPMCO_AD2	N24	GPMCO_AD2/PRO_PRU1_GPO10/PRO_PRU1_GPI10/MCASPI2_AXR6/PRO_PRU0_GPO2/PRO_PRU0_GPI2/TRC_DATA0/GPIO0_17/BOOTMODE02		BOOTMODE2
	GPMCO_AD3	N25	GPMCO_AD3/PRO_PRU1_GPO11/PRO_PRU1_GPI11/MCASPI2_AXR7/PRO_PRU0_GPO3/PRO_PRU0_GPI3/TRC_DATA1/GPIO0_18/BOOTMODE03		BOOTMODE3
	GPMCO_AD4	P24	GPMCO_AD4/PRO_PRU1_GPO12/PRO_PRU1_GPI12/MCASPI2_AXR8/PRO_PRU0_GPO4/PRO_PRU0_GPI4/TRC_DATA2/GPIO0_19/BOOTMODE04		BOOTMODE4
	GPMCO_AD5	P22	GPMCO_AD5/PRO_PRU1_GPO13/PRO_PRU1_GPI13/MCASPI2_AXR9/PRO_PRU0_GPO5/PRO_PRU0_GPI5/TRC_DATA3/GPIO0_20/BOOTMODE05		BOOTMODE5
	GPMCO_AD6	P21	GPMCO_AD6/PRO_PRU1_GPO14/PRO_PRU1_GPI14/MCASPI2_AXR10/PRO_PRU0_GPO6/PRO_PRU0_GPI6/TRC_DATA4/GPIO0_21/BOOTMODE06		BOOTMODE6
	GPMCO_AD7	R23	GPMCO_AD7/PRO_PRU1_GPO15/PRO_PRU1_GPI15/MCASPI2_AXR11/PRO_PRU0_GPO7/PRO_PRU0_GPI7/TRC_DATA5/GPIO0_22/BOOTMODE07		BOOTMODE7
	GPMCO_AD8	R24	GPMCO_AD8/PRO_PRU1_GPO16/PRO_PRU1_GPI16/MCASPI2_AXR12/PRO_PRU0_GPO8/PRO_PRU0_GPI8/TRC_DATA6/GPIO0_23/BOOTMODE08		BOOTMODE8
	GPMCO_AD9	R25	GPMCO_AD9/PRO_PRU1_GPO17/PRO_PRU1_GPI17/MCASPI2_AXR13/PRO_PRU0_GPO9/PRO_PRU0_GPI9/TRC_DATA7/GPIO0_24/BOOTMODE09		BOOTMODE9
	GPMCO_AD10	T25	GPMCO_AD10/PRO_PRU1_GPO18/PRO_PRU1_GPI18/MCASPI2_AXR14/PRO_PRU0_GPO10/PRO_PRU0_GPI10/TRC_DATA8/GPIO0_25/BOOTMODE10		BOOTMODE10
	GPMCO_AD11	R21	GPMCO_AD11/PRO_PRU1_GPO19/PRO_PRU1_GPI19/MCASPI2_AXR15/PRO_PRU0_GPO11/PRO_PRU0_GPI11/TRC_DATA9/GPIO0_26/BOOTMODE11		BOOTMODE11
	GPMCO_AD12	T22	GPMCO_AD12/PRO_PRU1_GPO20/PRO_PRU1_GPI20/MCASPI2_AXR16/PRO_PRU0_GPO12/PRO_PRU0_GPI12/TRC_DATA10/GPIO0_27/BOOTMODE12		BOOTMODE12
	GPMCO_AD13	T24	GPMCO_AD13/PRO_PRU1_GPO21/PRO_PRU1_GPI21/MCASPI2_AXR17/PRO_PRU0_GPO13/PRO_PRU0_GPI13/TRC_DATA11/GPIO0_28/BOOTMODE13		BOOTMODE13
	GPMCO_AD14	U25	GPMCO_AD14/PRO_PRU1_GPO22/PRO_PRU1_GPI22/MCASPI2_AXR18/PRO_PRU0_GPO14/PRO_PRU0_GPI14/TRC_DATA12/GPIO0_29/BOOTMODE14		BOOTMODE14
	GPMCO_AD15	U24	GPMCO_AD15/PRO_PRU1_GPO23/PRO_PRU1_GPI23/MCASPI2_AXR19/PRO_PRU0_GPO15/PRO_PRU0_GPI15/TRC_DATA13/GPIO0_30/BOOTMODE15		BOOTMODE15
	GPMCO_CSN0	M21	GPMCO_CSN0/MCASPI2_AXR14/PRO_PRU0_GPO17/PRO_PRU0_GPI17/TRC_DATA15/GPIO0_41		
	GPMCO_CSN1	L21	GPMCO_CSN1/PRO_PRU1_GPO16/PRO_PRU1_GPI16/MCASPI2_AXR15/PRO_PRU0_GPO18/PRO_PRU0_GPI18/TRC_DATA16/GPIO0_42		
	GPMCO_CSN2	K22	GPMCO_CSN2/I2C2_SCL/MCASPI2_AXR4/UART4_RXD/PRO_PRU0_GPO19/PRO_PRU0_GPI19/TRC_DATA17/GPIO0_43/MCASPI2_AFSR		
	GPMCO_CSN3	K24	GPMCO_CSN3/I2C2_SDA/GPMCO_A20/UART4_TXD/MCASPI2_AXR5/PRO_PRU0_GPO20/PRO_PRU0_GPI20/TRC_DATA18/GPIO0_44/MCASPI2_ACLR		
	GPMCO_ADVn_ALE	L23	GPMCO_ADVn_ALE/MCASPI2_AXR2/PRO_PRU0_GPO9/PRO_PRU0_GPI9/TRC_DATA7/GPIO0_32		
	GPMCO_BE0n_CLE	M24	GPMCO_BE0n_CLE/MCASPI2_ACLR/PRO_PRU0_GPO12/PRO_PRU0_GPI12/TRC_DATA10/GPIO0_35		
	GPMCO_BE1n	N20	GPMCO_BE1n/MCASPI2_AXR12/PRO_PRU0_GPO13/PRO_PRU0_GPI13/TRC_DATA11/GPIO0_36		
	GPMCO_DIR	M22	GPMCO_DIR/PRO_ECAP0_IN/AFPM_OUT/MCASPI2_AXR13/PRO_PRU0_GPO16/PRO_PRU0_GPI16/TRC_DATA14/GPIO0_40/EQEP2_S		
	GPMCO_WAIT0	U23	GPMCO_WAIT0/MCASPI2_AFSX/PRO_PRU0_GPO14/PRO_PRU0_GPI14/TRC_DATA12/GPIO0_37		
	GPMCO_WAIT1	V25	GPMCO_WAIT1/VOUT0_EXTFCLKIN/GPMCO_A21/UART6_RXD/GPIO0_38/EQEP2_I		
	GPMCO_WPN	K25	GPMCO_WPN/AUDIO_EXT_REFCLK1/GPMCO_A22/UART6_TXD/PRO_PRU0_GPO15/PRO_PRU0_GPI15/TRC_DATA13/GPIO0_3		
	GPMCO_OEN_REN	L24	GPMCO_OEN_REn/MCASPI2_AXR1/PRO_PRU0_GPO10/PRO_PRU0_GPI10/TRC_DATA8/GPIO0_33		
	GPMCO_WEN	L25	GPMCO_WEN/MCASPI2_AXR0/PRO_PRU0_GPO11/PRO_PRU0_GPI11/TRC_DATA9/GPIO0_34		

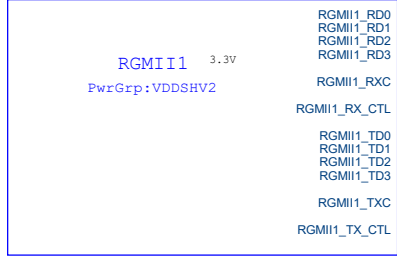
AM6232
BGA425

Bootstrap



RGMII

U1M



AM6232
BGA425

AB17	RGMII1_RD0/RMII1_RXD0/GPIO0_81		
AC17	RGMII1_RD1/RMII1_RXD1/GPIO0_82		
AB16	RGMII1_RD2/PR0_UART0_RTSn/GPIO0_83		
AA15	RGMII1_RD3/GPIO0_84		
AD17	RGMII1_RXC/RMII1_REF_CLK/PR0_UART0_CTSn/GPIO0_80		
AE17	RGMII1_RX_CTL/RMII1_RX_ER/GPIO0_79		
AE20	RGMII1_TD0/RMII1_TXD0/GPIO0_75		
AD20	RGMII1_TD1/RMII1_TXD1/GPIO0_76		
AE18	RGMII1_TD2/PR0_UART0_RXD/GPIO0_77	AE18	[13,16]
AD18	RGMII1_TD3/PR0_UART0_TXD/GPIO0_78	AD18	[13,16]
AE19	RGMII1_TXC/RMII1_CRS_DV/GPIO0_74		
AD19	RGMII1_TX_CTL/RMII1_TX_EN/GPIO0_73		

U1S

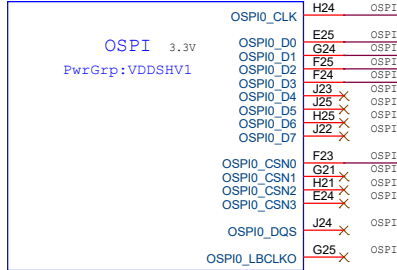


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BGA425

AE23	RGMII2_RD0/RMII2_RXD0/MCASP2_AXR2/PR0_PRU0_GPO2/PR0_PRU0_GPI2/PR0_UART0_RTSn/GPIO1_3	AE23	[15,16]
AB20	RGMII2_RD1/RMII2_RXD1/MCASP2_AFSR/PR0_PRU0_GPO3/PR0_PRU0_GPI3/MCASP2_AXR7/GPIO1_4	AB20	[15,16]
AC21	RGMII2_RD2/MCASP2_AXR0/PR0_PRU0_GPO4/PR0_PRU0_GPI4/PR0_UART0_RXD/GPIO1_5/EQEP2_A	AC21	[15,16]
AE22	RGMII2_RD3/AUDIO_EXT_REFCLK0/PR0_PRU0_GPO16/PR0_PRU0_GPI16/PR0_UART0_TXD/GPIO1_6/EQEP2_B	AE22	[15,16]
AD23	RGMII2_RXC/RMII2_REF_CLK/MCASP2_AXR1/PR0_PRU0_GPO1/PR0_PRU0_GPI1/PR0_ECAP0_SYNC_IN/GPIO1_2	AD23	[16]
AD22	RGMII2_RX_CTL/RMII2_RX_ER/MCASP2_AXR3/PR0_PRU0_GPO0/PR0_PRU0_GPI0/GPIO1_1	AD22	[15,16]
Y18	RGMII2_TD0/RMII2_TXD0/MCASP2_AXR6/PR0_PRU1_GPO2/PR0_PRU1_GPI2/GPIO0_89	Y18	[13,16]
AA18	RGMII2_TD1/RMII2_TXD1/MCASP2_ACLKR/PR0_PRU1_GPO3/PR0_PRU1_GPI3/MCASP2_AXR8/GPIO0_90	AA18	[13,16]
AD21	RGMII2_TD2/MCASP2_AFSX/PR0_PRU1_GPO4/PR0_PRU1_GPI4/PR0_ECAP0_IN_APMN_OUT/GPIO0_91/EQEP2_I	AD21	[16]
AC20	RGMII2_TD3/MCASP2_ACLKX/PR0_PRU1_GPO16/PR0_PRU1_GPI16/PR0_ECAP0_SYNC_OUT/PR0_UART0_CTSn/GPIO1_0/EQEP2_S	AC20	[16]
AE21	RGMII2_TXC/RMII2_CRS_DV/MCASP2_AXR5/PR0_PRU1_GPO1/PR0_PRU1_GPI1/GPIO0_88	AE21	[16]
AA19	RGMII2_TX_CTL/RMII2_TX_EN/MCASP2_AXR4/PR0_PRU1_GPO0/PR0_PRU1_GPI0/GPIO0_87	AA19	[13,15,16]

OSPI

U1J



AM6232
BGA425

H24	OSPI0_CLK/GPIO0_0	SD_PWR_EN	[9]
E25	OSPI0_D0/GPIO0_3	GPIO0_3	[15]
G24	OSPI0_D1/GPIO0_4	GPIO0_4	[15]
F25	OSPI0_D2/GPIO0_5	GPIO0_5	[15]
F24	OSPI0_D3/GPIO0_6	GPIO0_6	[15]
J23	OSPI0_D4/SP11_CS0/MCASP1_AXR1/UART6_RXD/GPIO0_7		
J25	OSPI0_D5/SP11_CLK/MCASP1_AXR0/UART6_TXD/GPIO0_8		
H25	OSPI0_D6/SP11_D0/MCASP1_ACLKX/UART6_RTSn/GPIO0_9		
J22	OSPI0_D7/SP11_D1/MCASP1_AFSX/UART6_CTSn/GPIO0_10		
F23	OSPI0_CSn0/GPIO0_11	EMMC_RSTn	[9]
G21	OSPI0_CSn1/GPIO0_12		
H21	OSPI0_CSn2/SP11_CS1/OSPI0_RESET_OUT1/MCASP1_AFSR/MCASP1_AXR2/UART5_RXD/GPIO0_13		
E24	OSPI0_CSn3/OSPI0_RESET_OUT0/OSPI0_ECC_FAIL/MCASP1_ACLKR/MCASP1_AXR3/UART5_TXD/GPIO0_14		
J24	OSPI0_DQS/UART5_CTSn/GPIO0_2		
G25	OSPI0_LBCLKO/UART5_RTSn/GPIO0_1		

SoC GPIO
3.3V



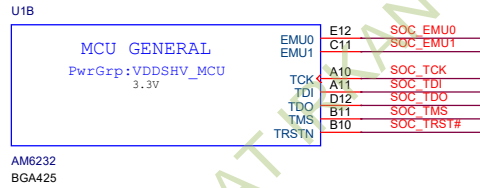
USB PORTLARI A VE TYPE-C



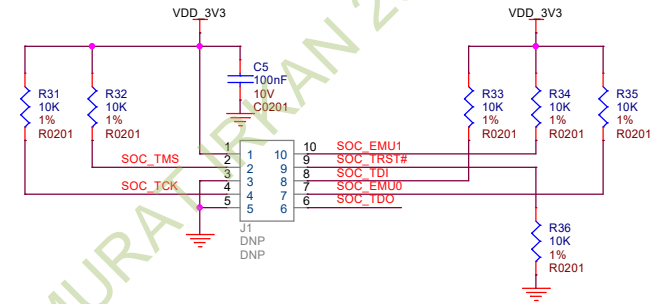
CSI



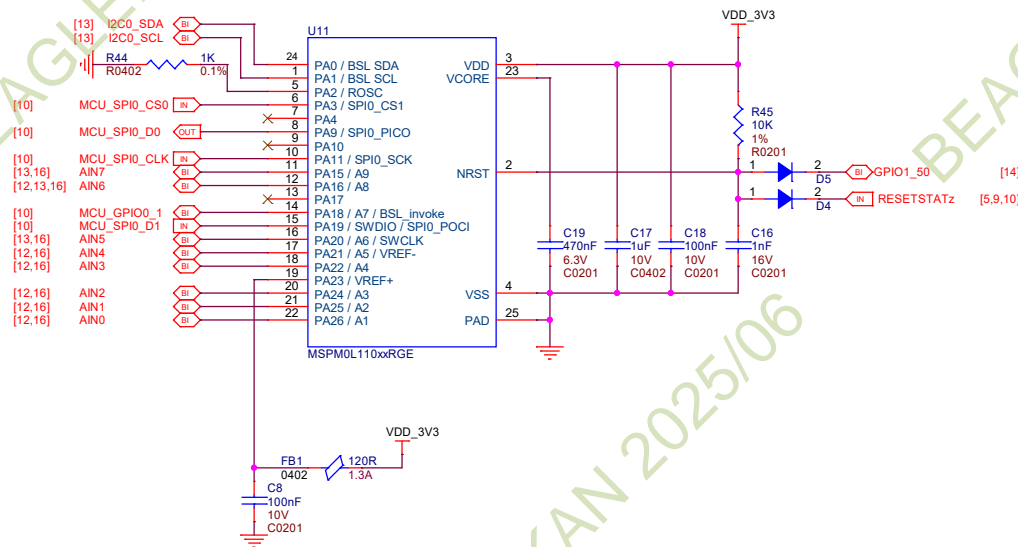
JTAG



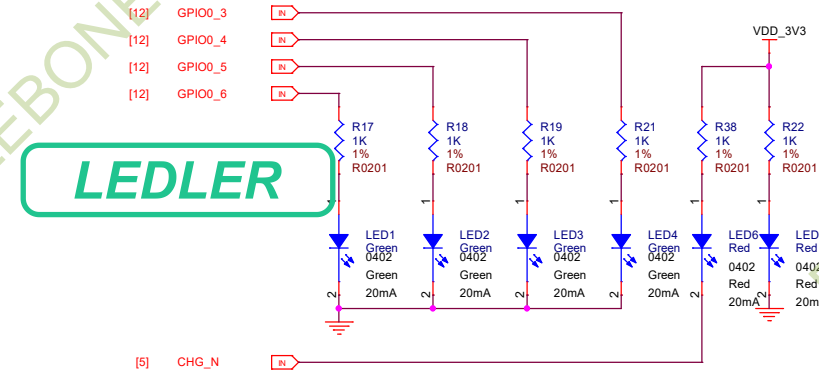
Tag-Connect



MCU

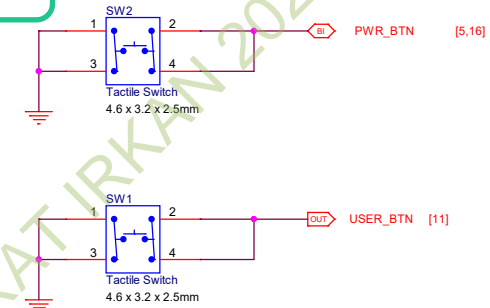


LEDs

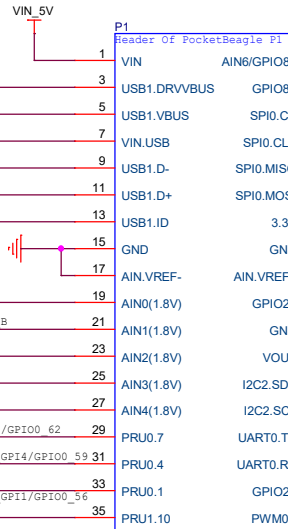
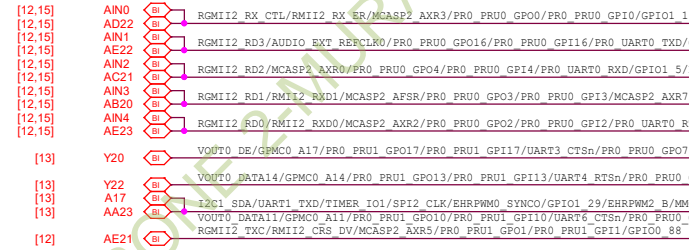


Power Button

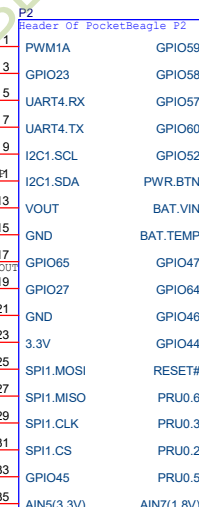
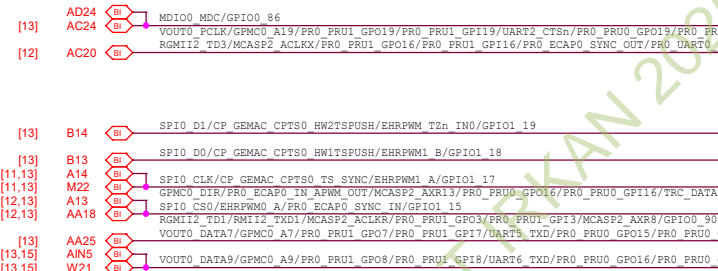
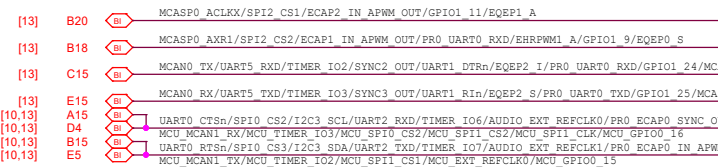
GÜÇ TUŞU



[14]	F18	BI	USB1_DRVVBUS/GPIO1 51
[14]	AB10	BI	
	USB_5V		
[14]	USB1_DM	BI	
[14]	USB1_DP	BI	
[11]	N20	BI	GPMC0_BE1n/MCASP2_AXR12/PR0_PRU0_GPIO13/PR0_PRU0_GPIO13/TRC_DATA11/GPIO0 36



2	MCASP0_AKRO/PRO_ECAP0 IN APWM OUT/AUDIO_EXT REFLCLK0/PRO_UART0_TXD/EHRPWM1_B/GPIO1_10/EQEP0_I	BI	E18	[12,13,15]
4	RGMI12_TX_CTL/RMII2_TX_EN/MCASP2_AKX4/PRO_PRUI_GPIO0/PRO_PRUI_GPIO1/GPIO0_87	BI	A19	[12,13,15]
4	MCASP0_AFSX/SP12_CS3/AUDIO_EXT REFLCLK1/GPIO1_12/EQEP1_B	BI	D20	[12,13]
6	RGMI12_TDO/RMII2_TXD0/MCASP2_AKX6/PRO_PRUI_GPIO2/PRO_PRUI_GPIO2/GPIO0_89	BI	V18	[12,13]
6	MCASP0_AFSR/SP12_CS0/UART1_RXD/EHRPWM0_A/GPIO1_13/EQEP1_S	BI	AD18	[12,13]
8	RGMI11_TD3/PRO_UART0_TXD/GPIO0_78	BI		
8	MCASP0_ACLKR/SP12_CLK/UART1_TXD/EHRPWM0_B/GPIO1_14/EQEP1_I	BI	A20	[13]
10	MCASP0_AKX3/SP12_D0/UART1_CTSn/UART6_RXD/PRO_PRUI_IEPO_EDITO_DATA IN OUT28/ECAP1 IN APWM OUT/PRO_UART0_RXD/GPIO1_7/EQEP0_A	BI	B19	[13]
12	MCASP0_SPD2/SP12_DI/UART1_RTSn/UART6_TXD/PRO_PRUI_IEPO_EDITO_DATA IN OUT29/ECAP2 IN APWM OUT/PRO_UART0_TXD/GPIO1_8/EQEP0_B	BI	B19	[12,13]
12	RGMI11_TD2/PRO_UART0_RXD/GPIO0_77	BI	AE18	[12,13]
14				
16				
18	R48 R0201 1%			
20	VOUT0_DATA5/GPMC0_A5/PRO_PRUI_GPIO5/PRO_PRUI_GPIO5/UART4_TXD/PRO_PRUI_GPIO3/PRO_PRUI_GPIO3/GPIO0_50	BI	Y24	[13]
22				
24				
26	MCU_MCAN0_TX/WKUP_TIMER_I00/MCU_SPI0_CS3/MCU_GPIO0_13	BI	D6	[10,11]
28	GPMC0_CSn3/I2C2_SDA/GPMC0_A20/UART4_TXD/MCASP1_AKX5/TRC_DATA18/GPIO0_44/MCASP1_ACLKR	BI	K24	[10,11]
28	MCU_MCAN0_RX/MCU_TIMER_I00/MCU_SPI1_CS3/MCU_GPIO0_14	BI	B3	[10,11]
30	GPMC0_CSn2/I2C2_SCL/MCASP1_AKX4/UART4_RXD/PRO_PRUI_GPIO19/PRO_PRUI_GPIO19/TRC_DATA17/GPIO0_43/MCASP1_AFSR	BI	K22	[10,11]
30	UART0_TXD/ECAP2 IN APWM OUT/SP12_DI/EHRPWM2_U/GPIO1_21	BI		
32	UART0_RXB/ECAP1 IN APWM OUT/SP12_D0/EHRPWM2_A/GPIO1_20	BI	E14	[13]
34	RGMI12_RXC/RMII2_REF_CLK/MCASP2_AKX1/PRO_PRUI_GPIO1/PRO_PRUI_GPIO1/PRO_ECAP0_SYNC_IN/GPIO1_2	BI	D14	[13]
36	VOUT0_DATA10/GPMC0_A10/PRO_PRUI_GPIO9/PRO_PRUI_GPIO9/UART6_RTSn/PRO_PRUI_GPIO0/PRO_PRUI_GPIO0/GPIO0_55	BI	AD23	[12]
36	I2C1_SCL/UART1_RXD/TIMER_I00/SP12_CS1/EHRPWM0_SYNC/GPIO1_28/EHRPWM2_A/MMC2_SDCC	BI	V20	[13]
36		BI	B17	[13]



GPIO	Pin	Signal	Function	Notes
GPIO59	2	VOUT0_DATA0/GPMC0_A0/PR0_PRUI_GP00/PR0_PRUI_GP10/UART2_RXD/PR0_PRU0_GP08/PR0_PRU0_GP18/GPIO0_45	U22	[13]
GPIO58	4	VOUT0_DATA1/GPMC0_A1/PR0_PRUI_GP01/PR0_PRUI_GP11/UART2_TXD/PR0_PRU0_GP09/PR0_PRU0_GP19/GPIO0_46	V24	[13]
GPIO57	6	VOUT0_DATA2/GPMC0_A2/PR0_PRUI_GP02/PR0_PRUI_GP12/UART3_RXD/PR0_PRU0_GP010/PR0_PRU0_GP110/GPIO0_47	W25	[13]
GPIO60	8	VOUT0_DATA3/GPMC0_A3/PR0_PRUI_GP03/PR0_PRUI_GP13/UART3_TXD/PR0_PRU0_GP011/PR0_PRU0_GP111/GPIO0_48	W24	[13]
GPIO52	10	RGMI12_TD2/MCASPT2_AFSX/PR0_PRUI_GP04/PR0_PRUI_GP14/PR0_ECAP0_IN/APWM_OUT/GPIO0_91/EQEP2_I	AD21	[12]
PWR_BTN	12		PWR_BTN	[5,15]
BAT.VIN	14		VBAT	
BAT.TEMP	16		BAT_TS	[5]
GPIO47	18	VOUT0_DATA8/GPMC0_A8/PR0_PRUI_GP016/PR0_PRUI_GP116/UART6_RXD/PR0_PRU0_GP017/PR0_PRU0_GP117/GPIO0_53	V21	[13]
GPIO64	20	MDIO0_MDIO/GPIO0_85	AB22	[13]
GPIO64	22	VOUT0_DATA4/GPMC0_A4/PR0_PRUI_GP04/PR0_PRUI_GP14/UART4_RXD/PR0_PRU0_GP012/PR0_PRU0_GP112/GPIO0_49	Y25	[13]
GPIO46	22	VOUT0_VSYNC/GPMC0_A18/PR0_PRUI_GP018/PR0_PRUI_GP118/UART2_RTSn/PR0_PRU0_GP018/PR0_PRU0_GP118/GPIO0_63	AC25	[13]
GPIO44	24	VOUT0_DATA6/GPMC0_A6/PR0_PRUI_GP06/PR0_PRUI_GP16/UART5_RXD/PR0_PRU0_GP014/PR0_PRU0_GP114/GPIO0_51	Y23	[13]
RESET#	26		MCU_PorZ	[5,10]
PRU0.6	28	VOUT0_HSYNC/GPMC0_A16/PR0_PRUI_GP015/PR0_PRUI_GP115/UART3_RTSn/PR0_PRU0_GP06/PR0_PRU0_GP16/GPIO0_61	AB24	[13]
PRU0.3	30	VOUT0_DATA13/GPMC0_A13/PR0_PRUI_GP012/PR0_PRUI_GP12/UART5_CTSn/PR0_PRU0_GP03/PR0_PRU0_GP13/GPIO0_58	AA24	[13]
PRU0.2	32	VOUT0_DATA12/GPMC0_A12/PR0_PRUI_GP011/PR0_PRUI_GP11/UART5_RTSn/PR0_PRU0_GP02/PR0_PRU0_GP12/GPIO0_57	AB25	[13]
PRU0.5	34	VOUT0_DATA15/GPMC0_A15/PR0_PRUI_GP014/PR0_PRUI_GP14/UART4_CTSn/PR0_PRU0_GP05/PR0_PRU0_GP15/GPIO0_60	AA21	[13]
AIN7(1.8V)	36	SPI0_CS1/CP_GEMAC_CPT50_TS_COMP/EHRPWM0_B/ECAP0_IN/APWM_OUT/GPIO1_16/EHRPWM_Tzn_IN5	AIN7	[13,15]
			C13	[13,15]

MCU_UART0_RXD/MCU_GPIO0_5		B5	[10]
MCU_UART0_TXD/MCU_GPIO0_6		A5	[10]
EXT_REFCLK1/SYNC1 OUT/SPI2 CS3/SYSCCLKOUT0/TIMER I04/ CLKOUT0/CP GENAC CPTS0 RFT CLK/GPIO1 30/ECAPO IN APWM OUT		A18	[13]

TBD:Where to put these signals?