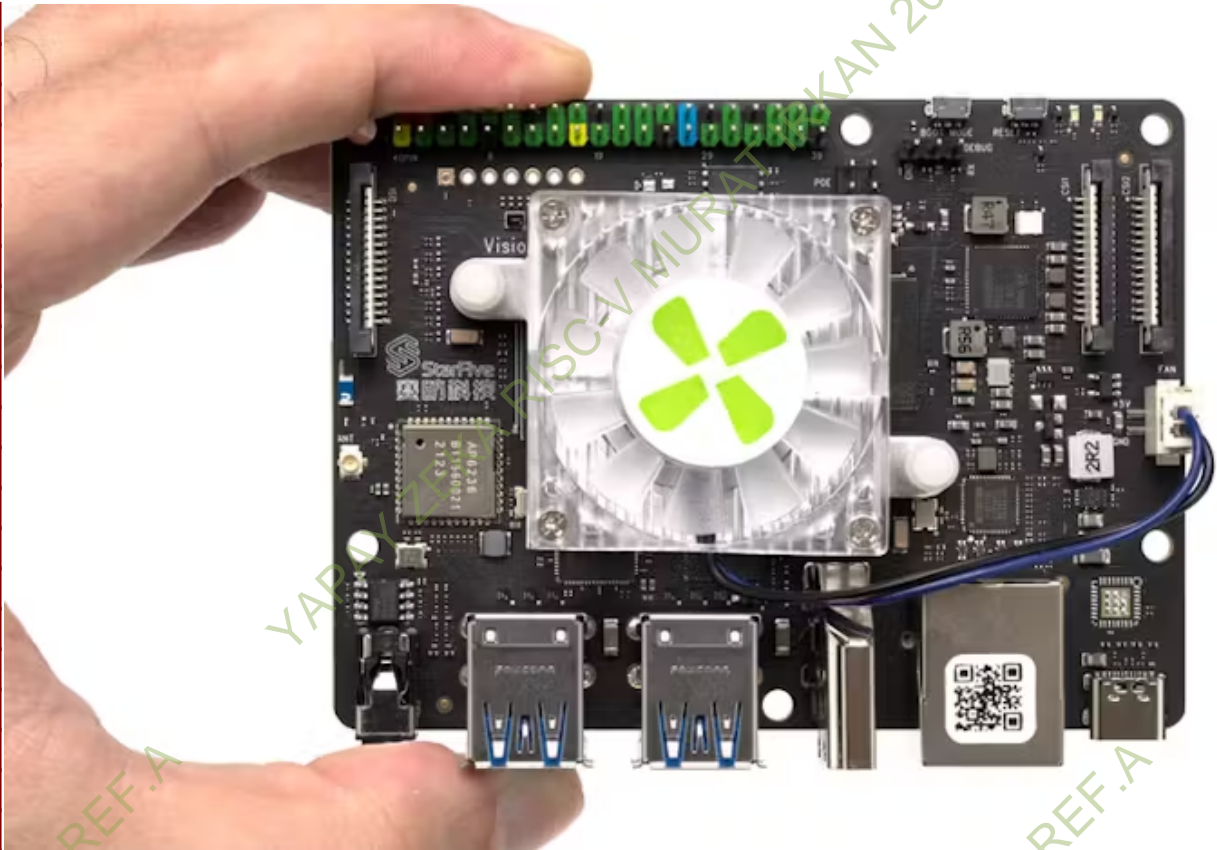
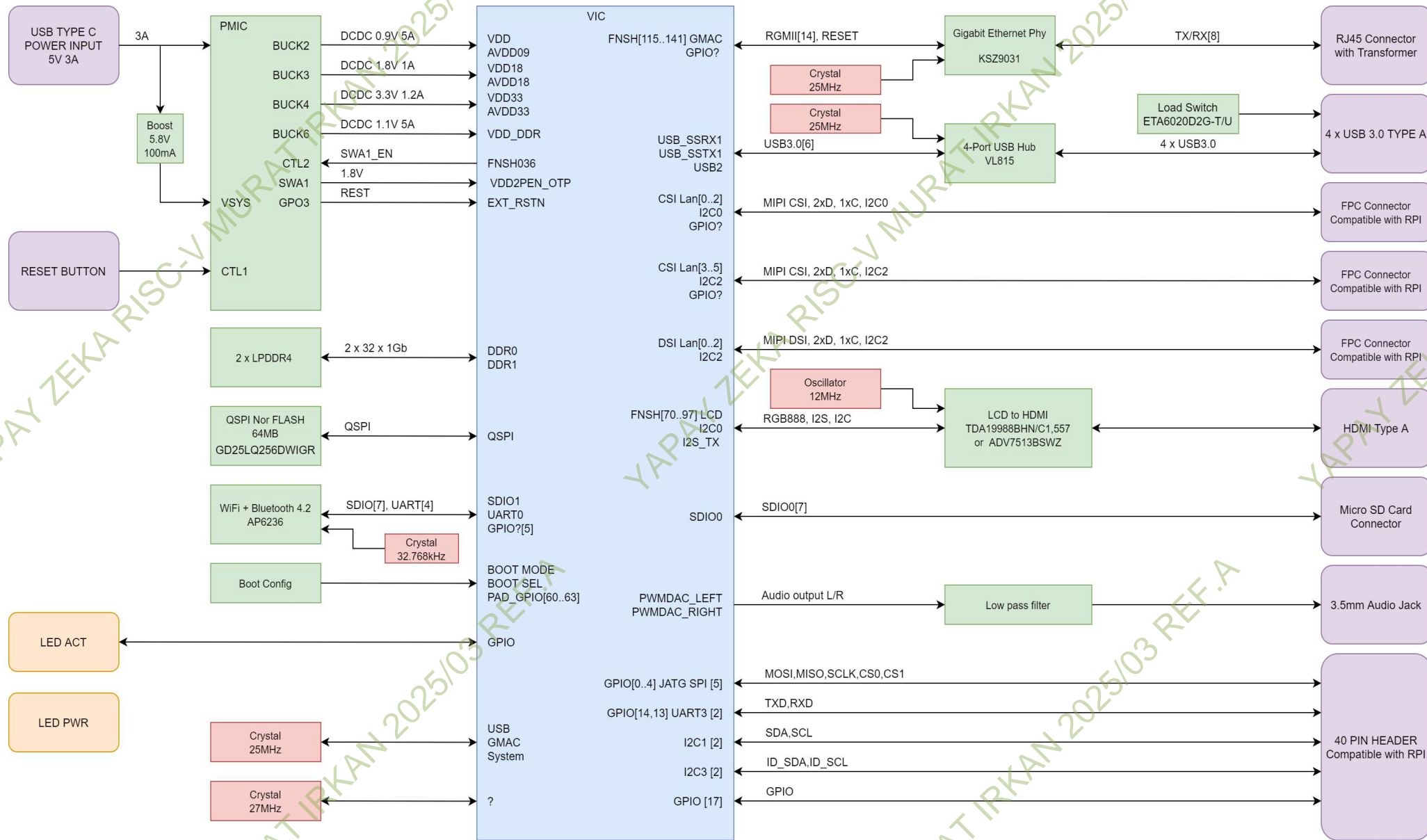


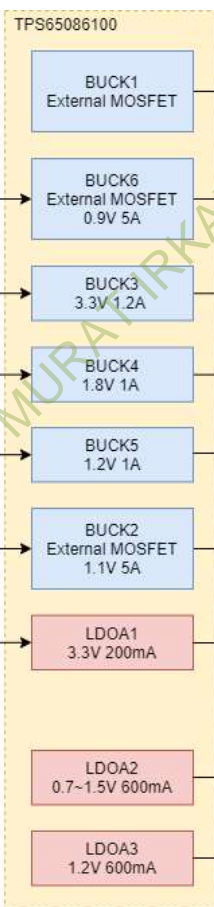
Schematic: Expansion Accessory

SHEET	SHEET NAME
01	Title/Revision History
02	System Block Diagram
03	Power Tree Diagram
04	PMIC
05	VIC Power
06	VIC DDR Ctrl
07	VIC ChipLink & CM
08	VIC LCD & GMII & GPIOs
09	VIC HighIF & Ctrl Other
10	LPDDR (A)
11	LPDDR (B)
12	Type C,uSD, QSIP Flash
13	USB 3.0 HUB
14	2 x USB TYPE A (A)
15	2 x USB TYPE A (B)
16	2 x CSI CONN
17	DSI, AUDIO JACK
18	HDMI FRAMER
19	WiFi, Bluetooth
20	10/100/1000 ETHERNET
21	RPI EXP CONN, LED, BUTTON
22	Power-DC IN

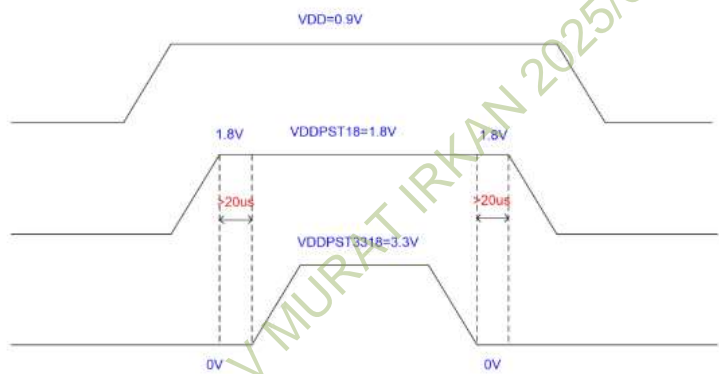




USB TYPE C
POWER INPUT
5V 3A



VIC Power-up/dwon sequence



VIC

VDD	3.36A
AVDD09_MIPIRX	2.2mA
AVDD09_USB	17.2mA
AVDD09_PLL0/1/2	20mA
VDDPLL_DDR0/1	15mA
VDD33_GMII	11.44mA
VDD33_LCD	23.7mA
VDD33_SENSOR	20.2mA
VDD33_CHIPLINK	60mA
AVDD33_USB	17.2mA
VDD18	60mA
VDD18_DDR	64mA
DVDD18_OTP	7.5mA
AVDD18_MIPI	18mA
AVDD18_USB	15.3mA
AVDD18_TS	0.1mA
VDDQ_DDR0/1	306mA

2 x LPDDR4 2xCH 4xDie 32bit

VDDQ	500mA
VDD2	2.4A
VDD1	12mA

Ethernet KSZ9031

VDDIO	41.5mA
DVDDL	221mA
AVDDL	

HDMI TDA19988

VDDDC	42mA
VDDA	35mA
VDDD	0.1mA

WiFi Bluetooth AP6236

VDD	250mA
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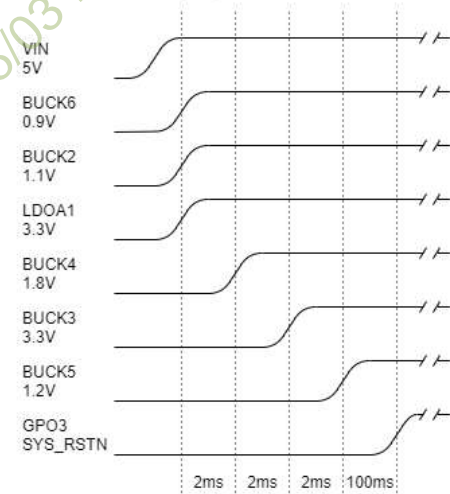
USB HUB VL815

VDD	180mA
VDD3.3V	650mA

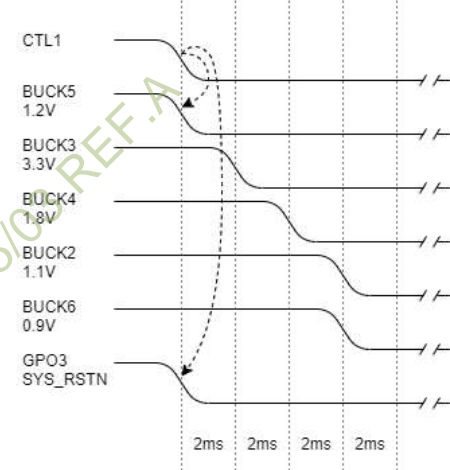
Expansions

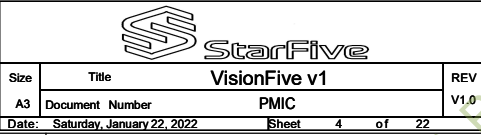
VDD5V	156mA
VDD3.3V	537mA

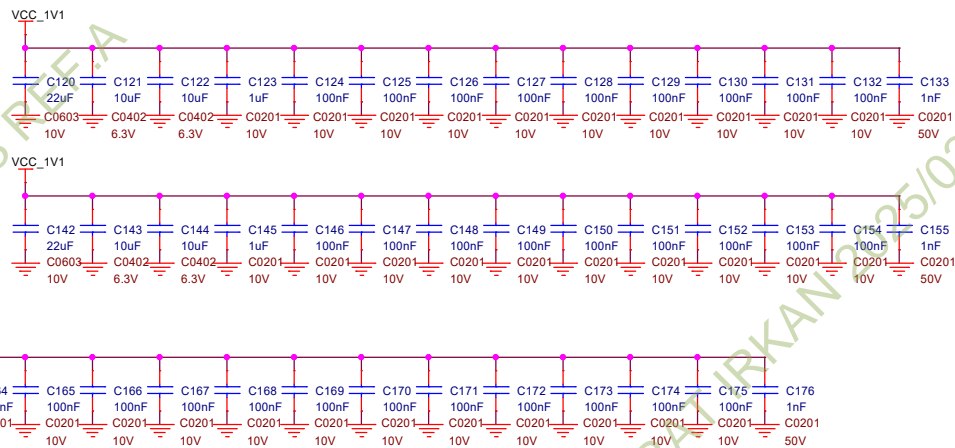
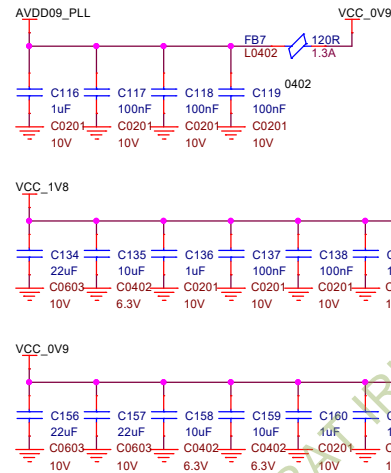
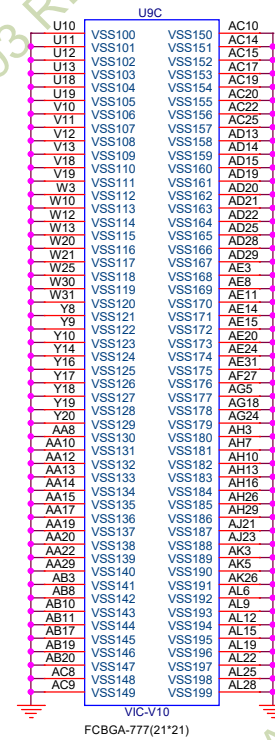
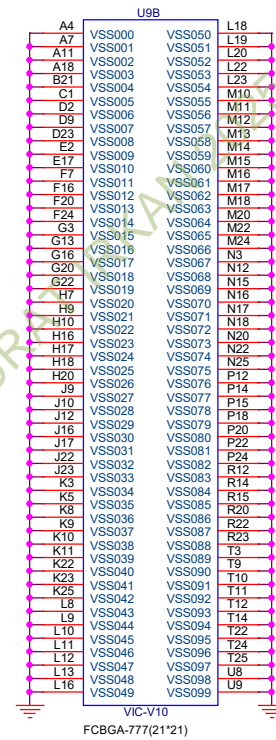
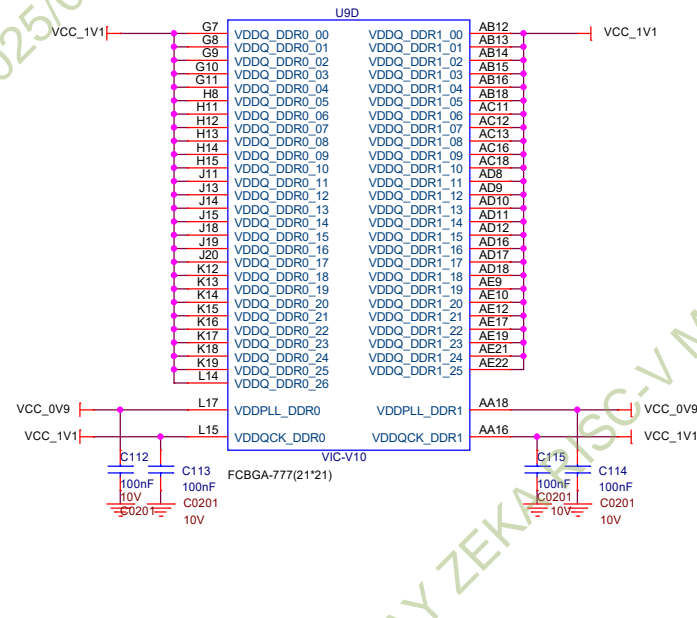
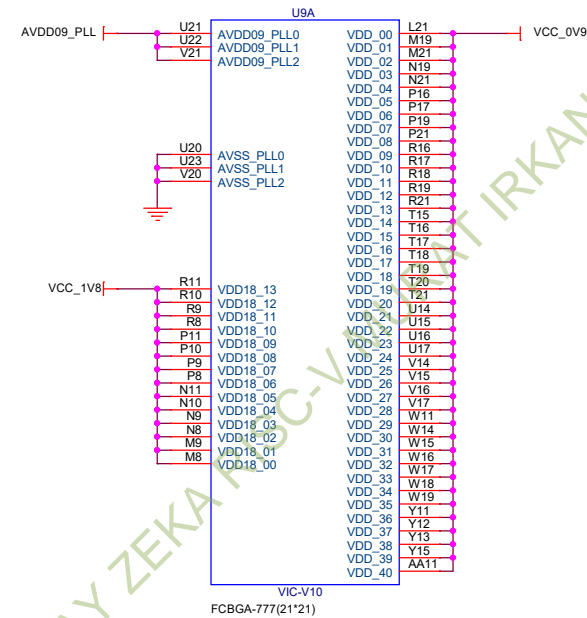
TPS65086100 Power-up sequence



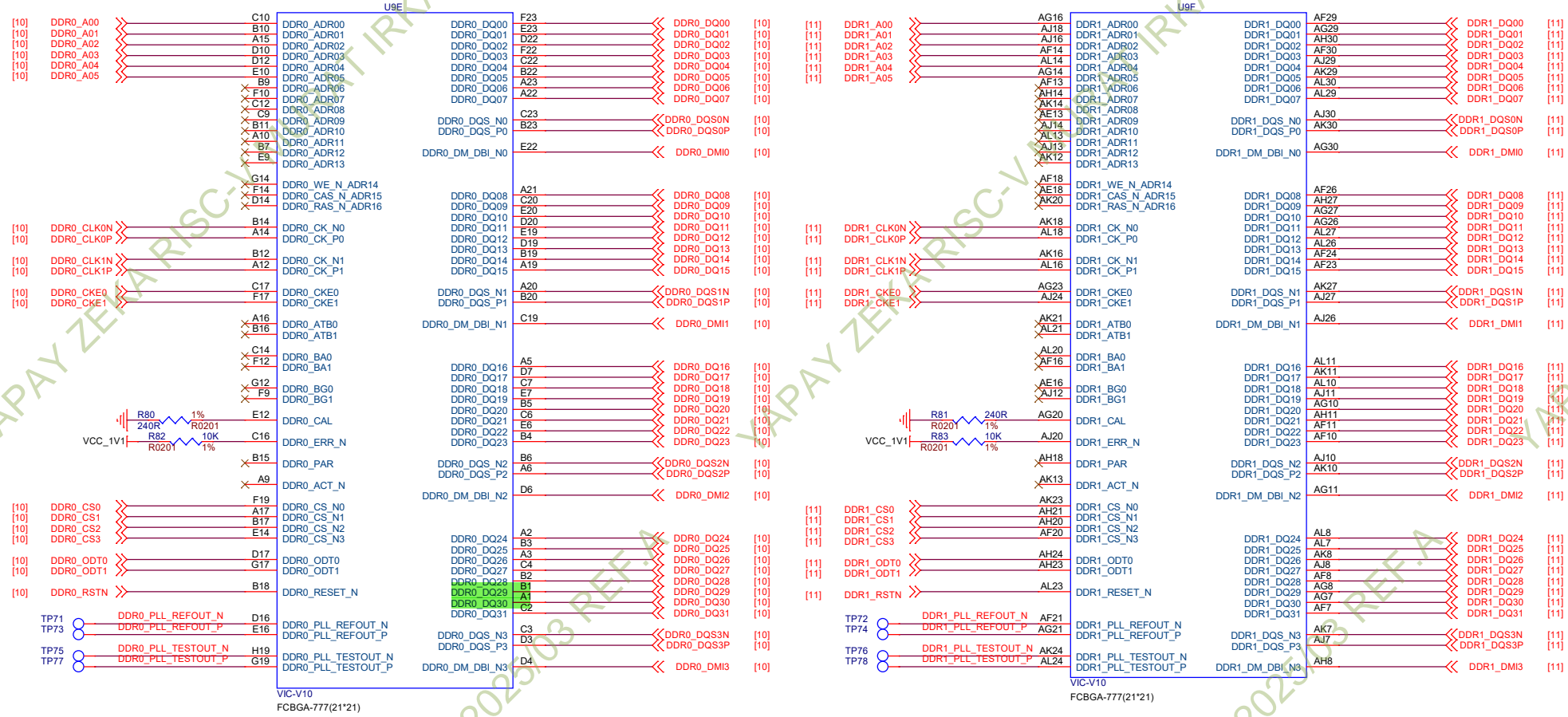
TPS65086100 Power-down sequence



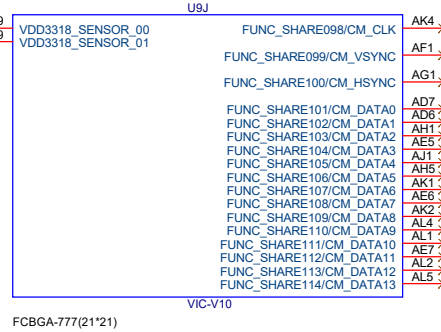
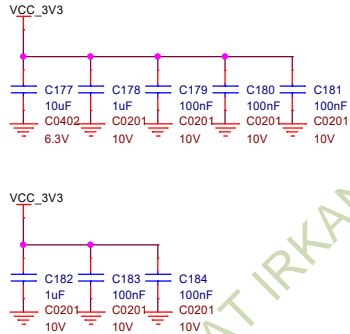
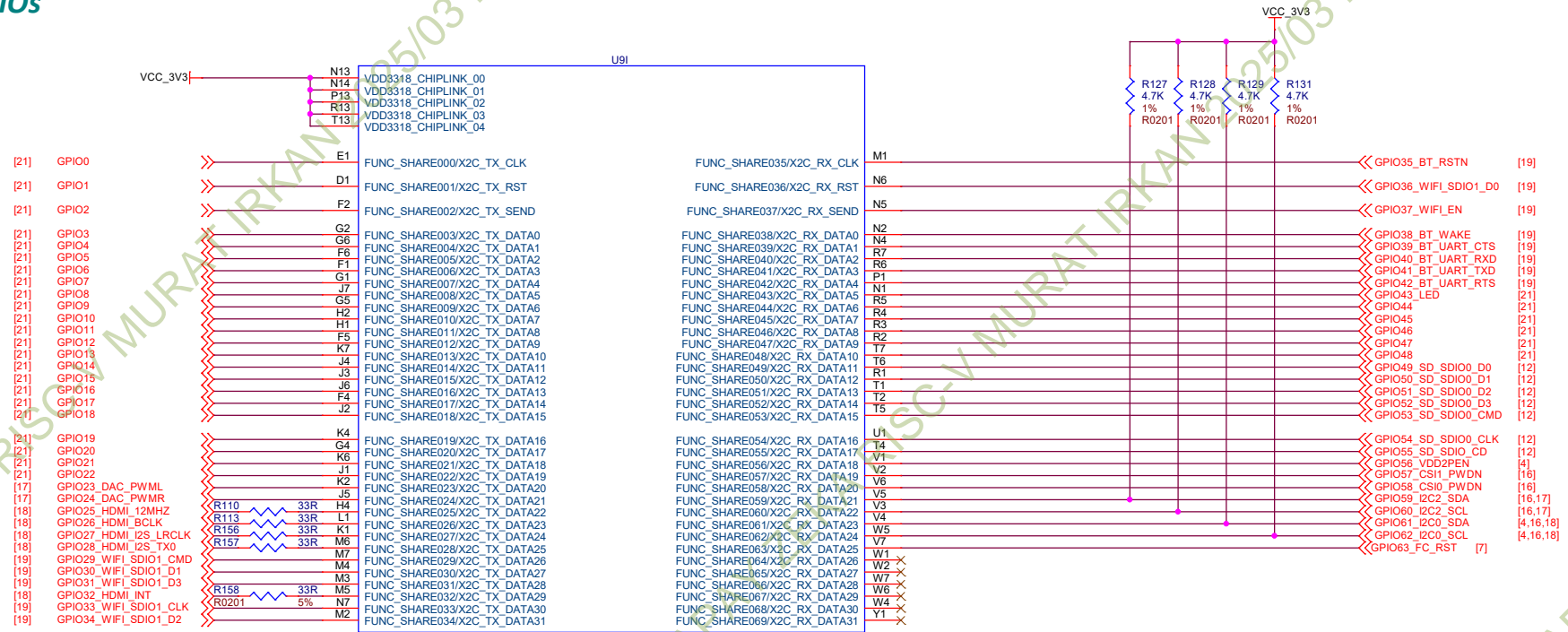


VIC Power

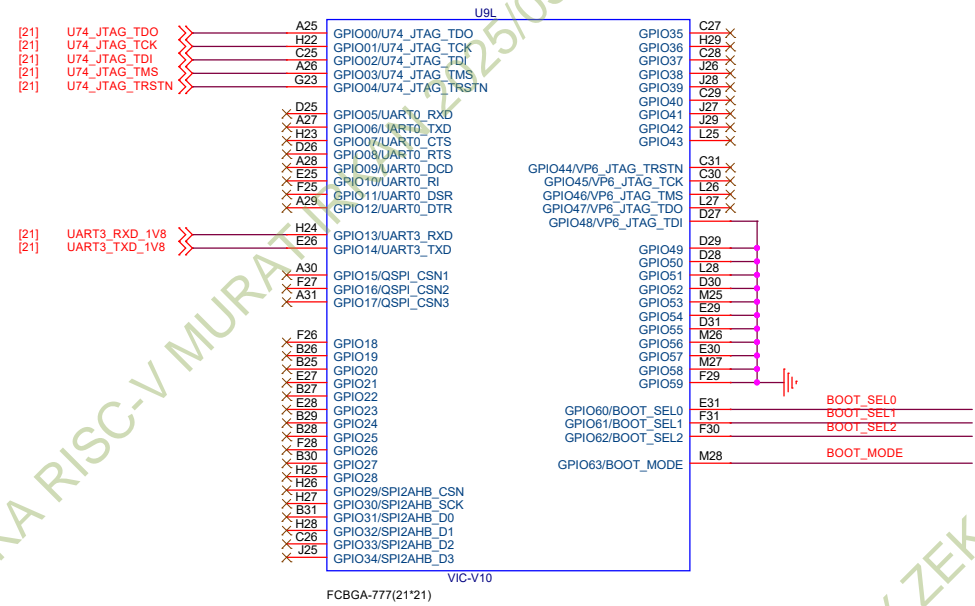
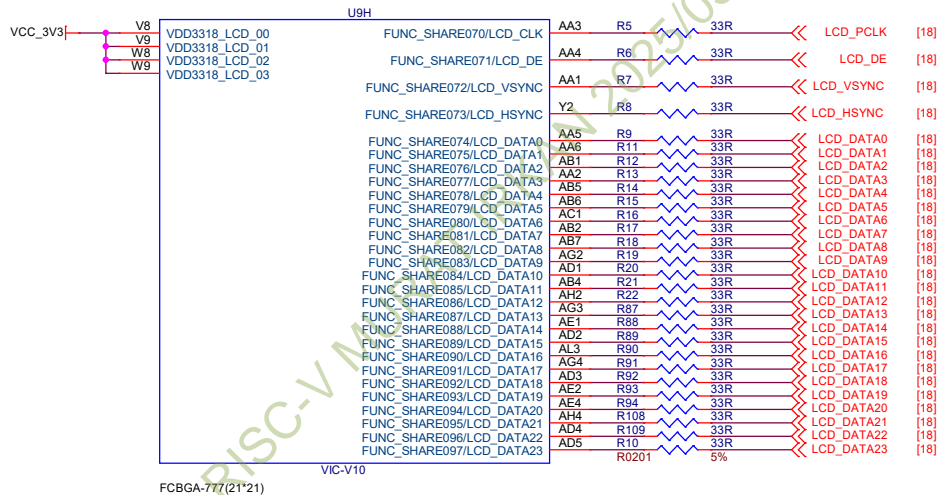
VIC DDR controller



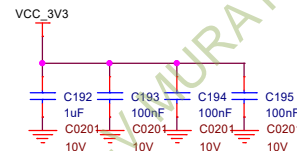
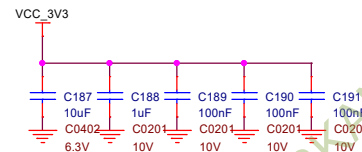
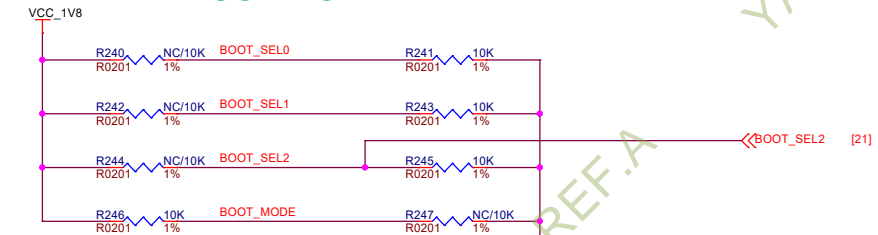
VIC 3.3V GPIOs



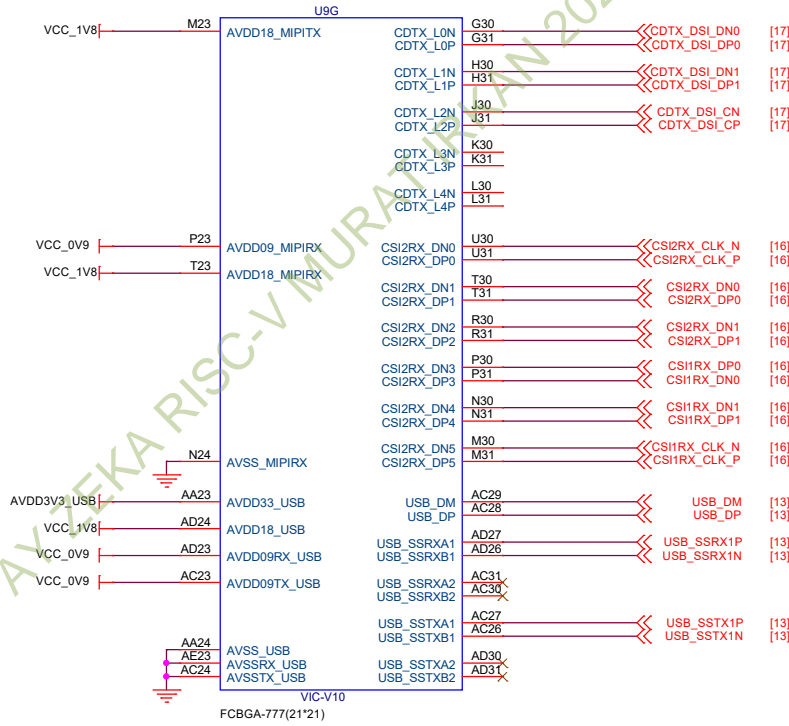
VIC LCD&RGMII



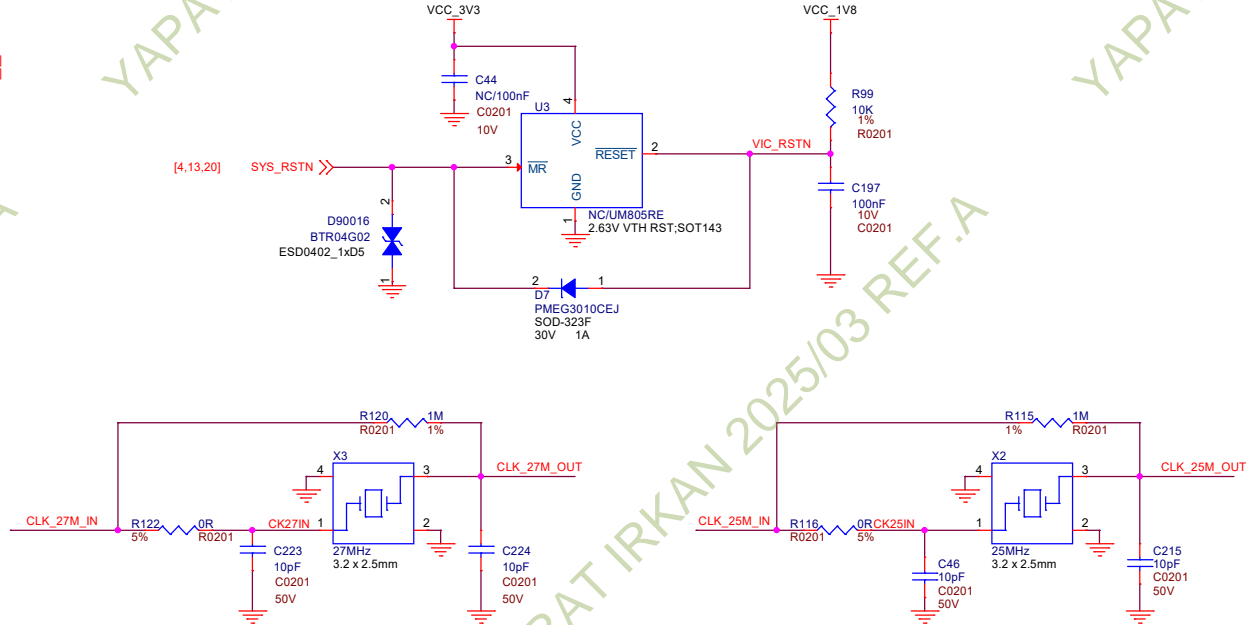
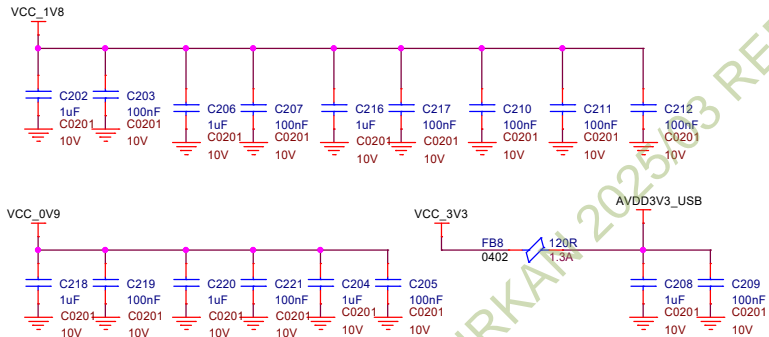
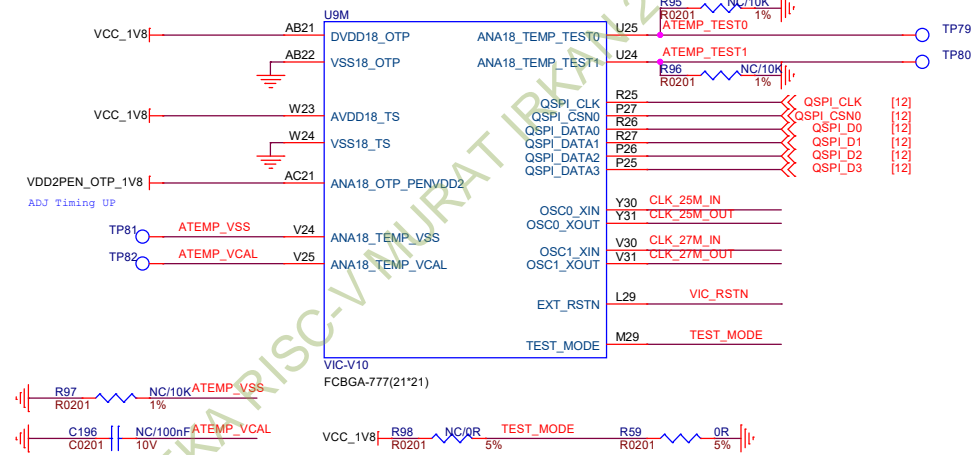
BOOT MODE



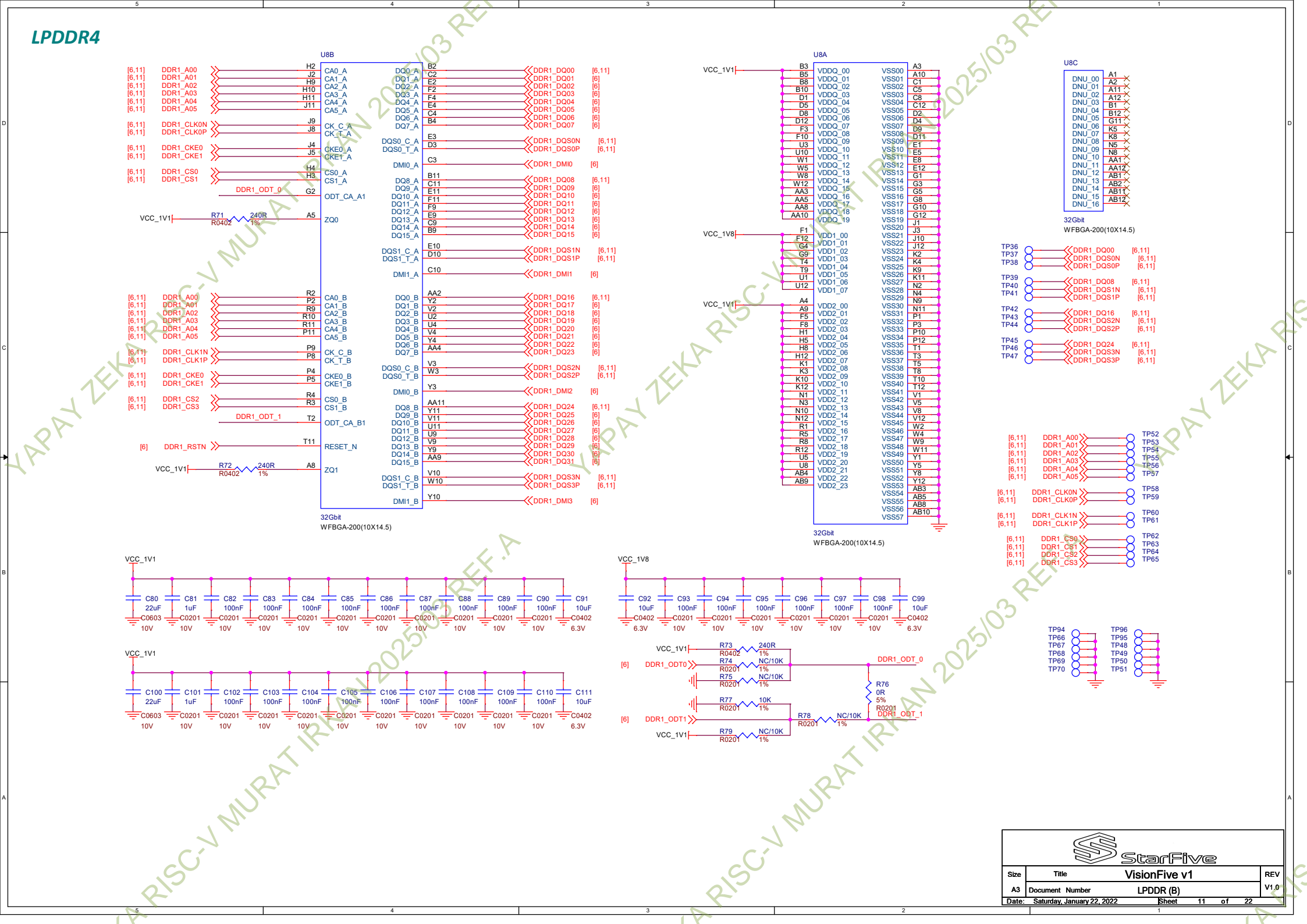
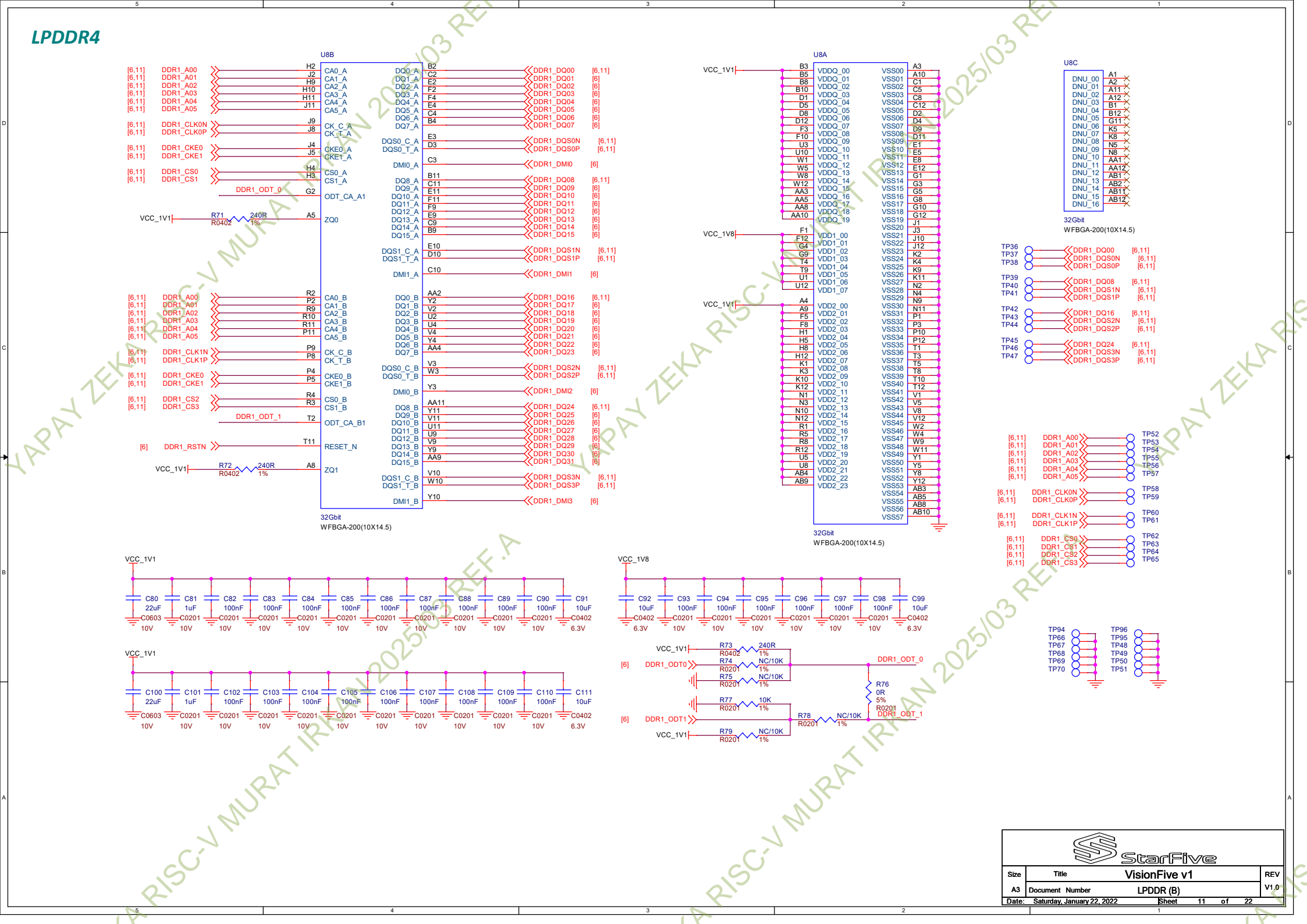
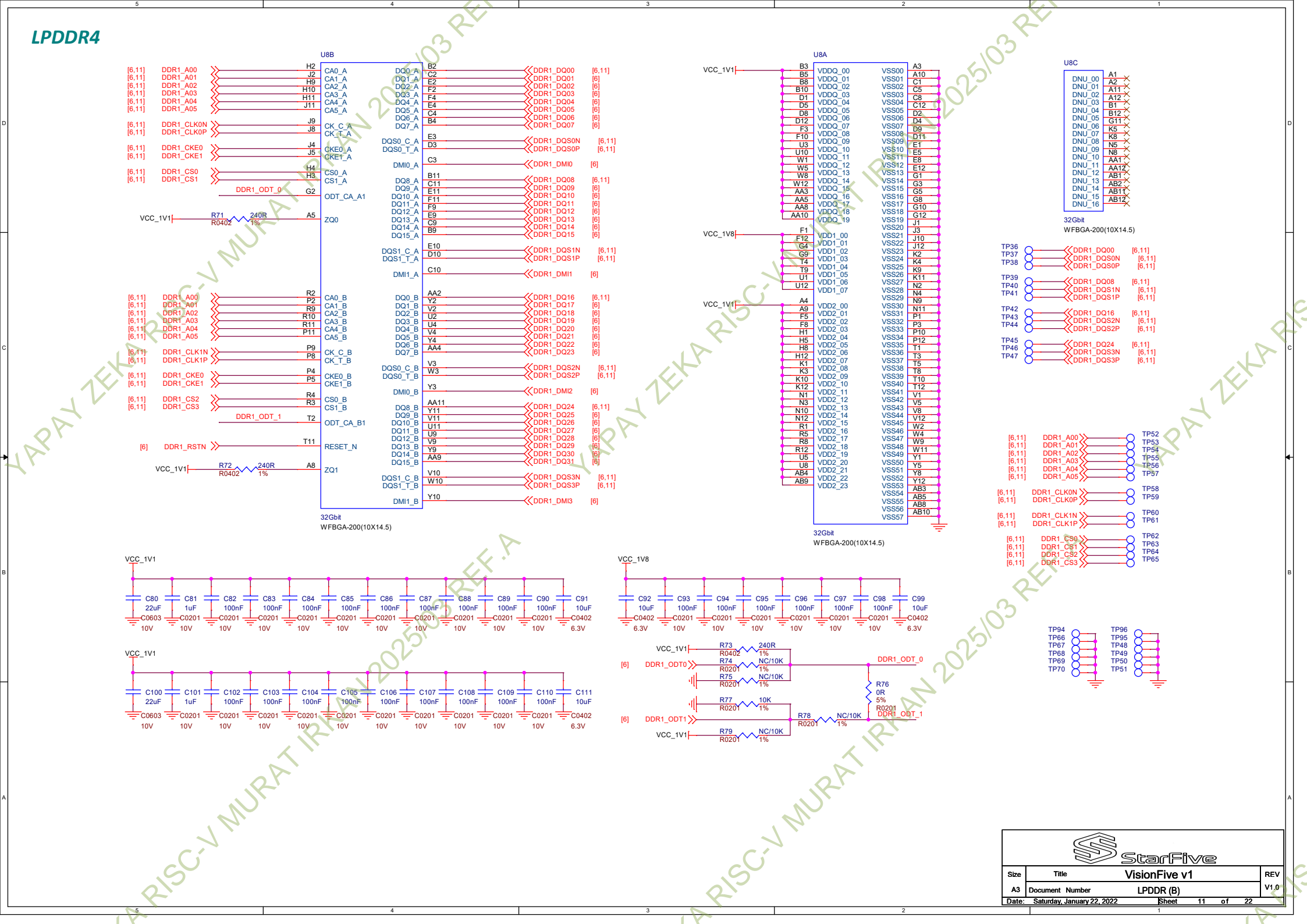
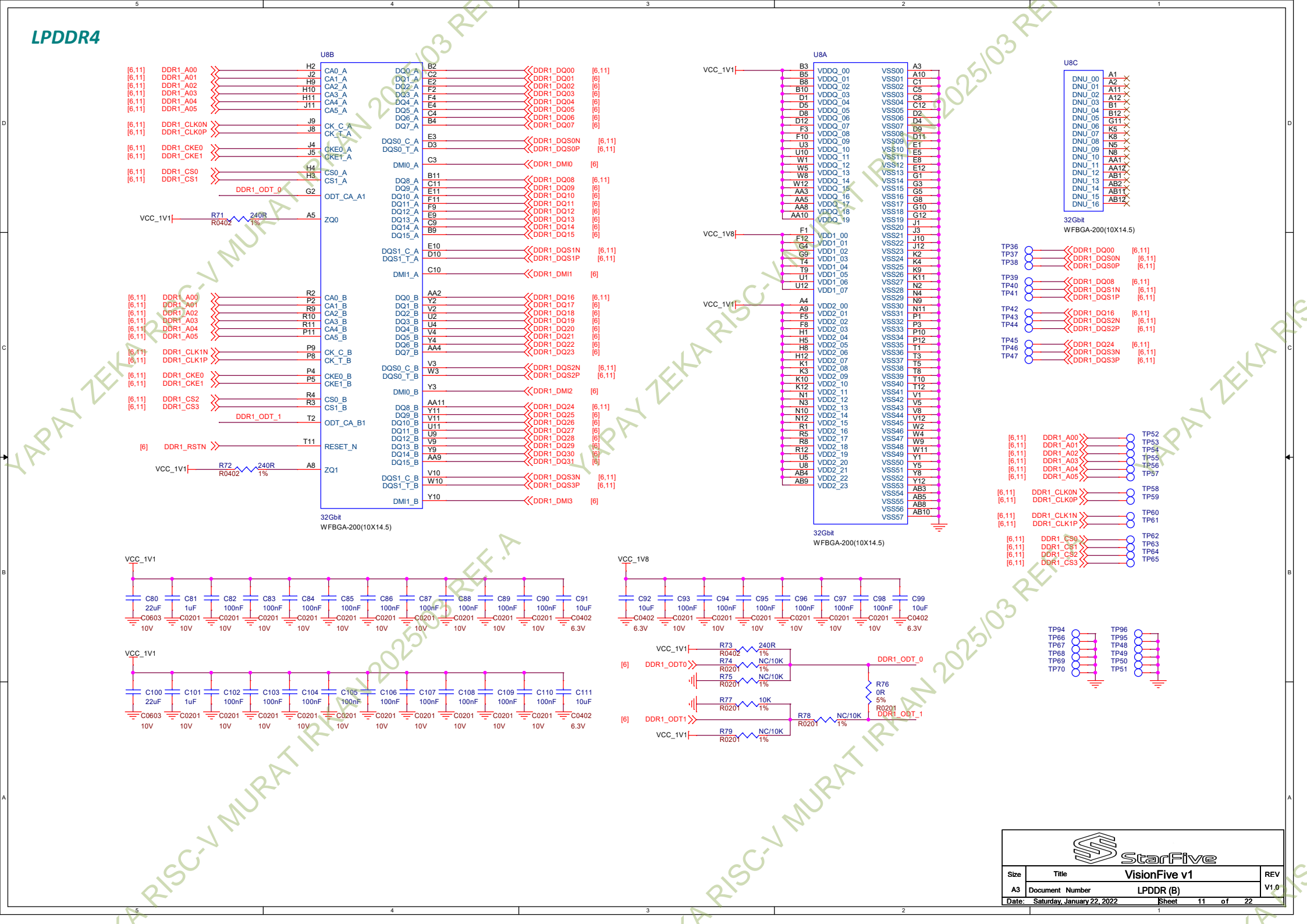
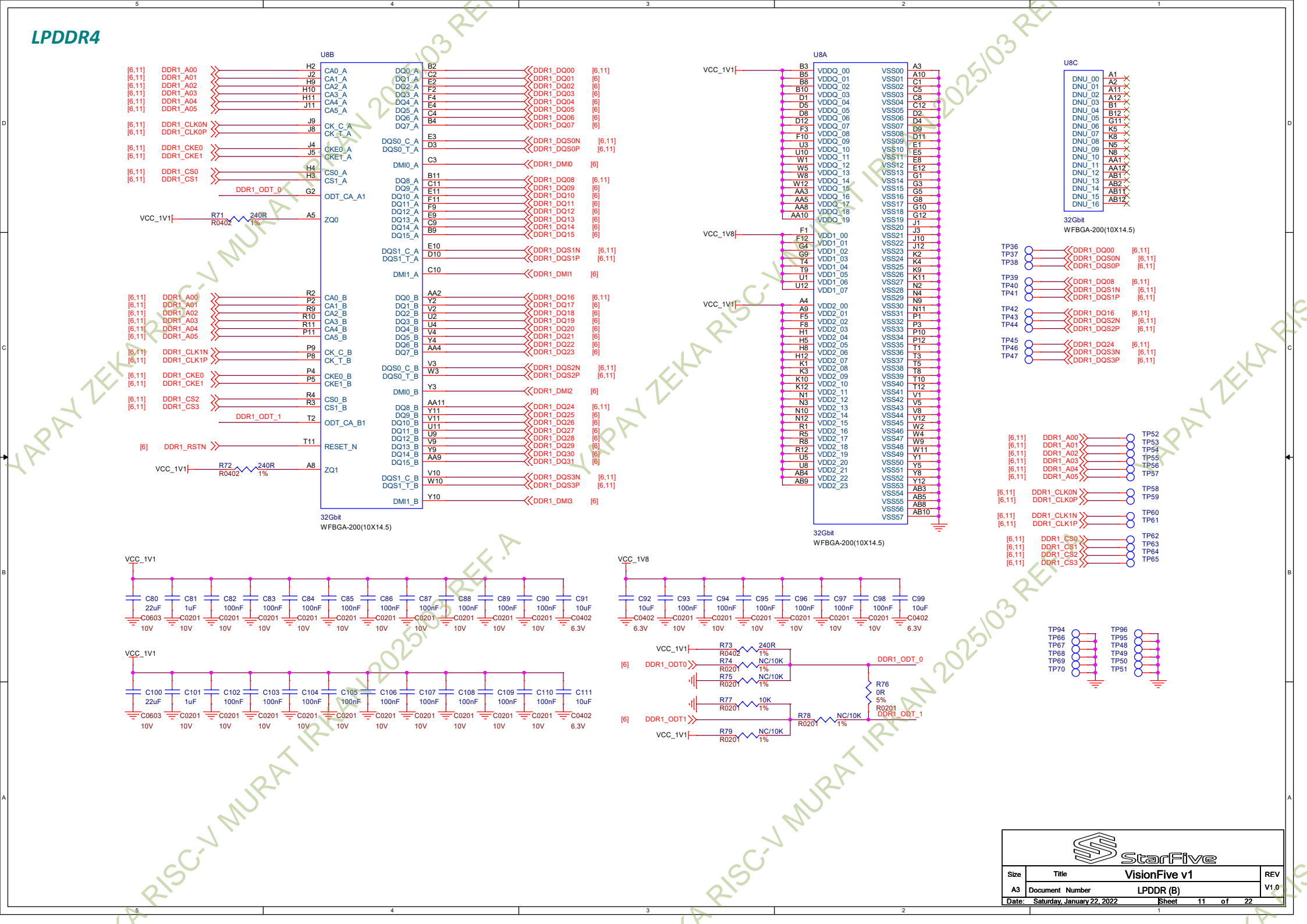
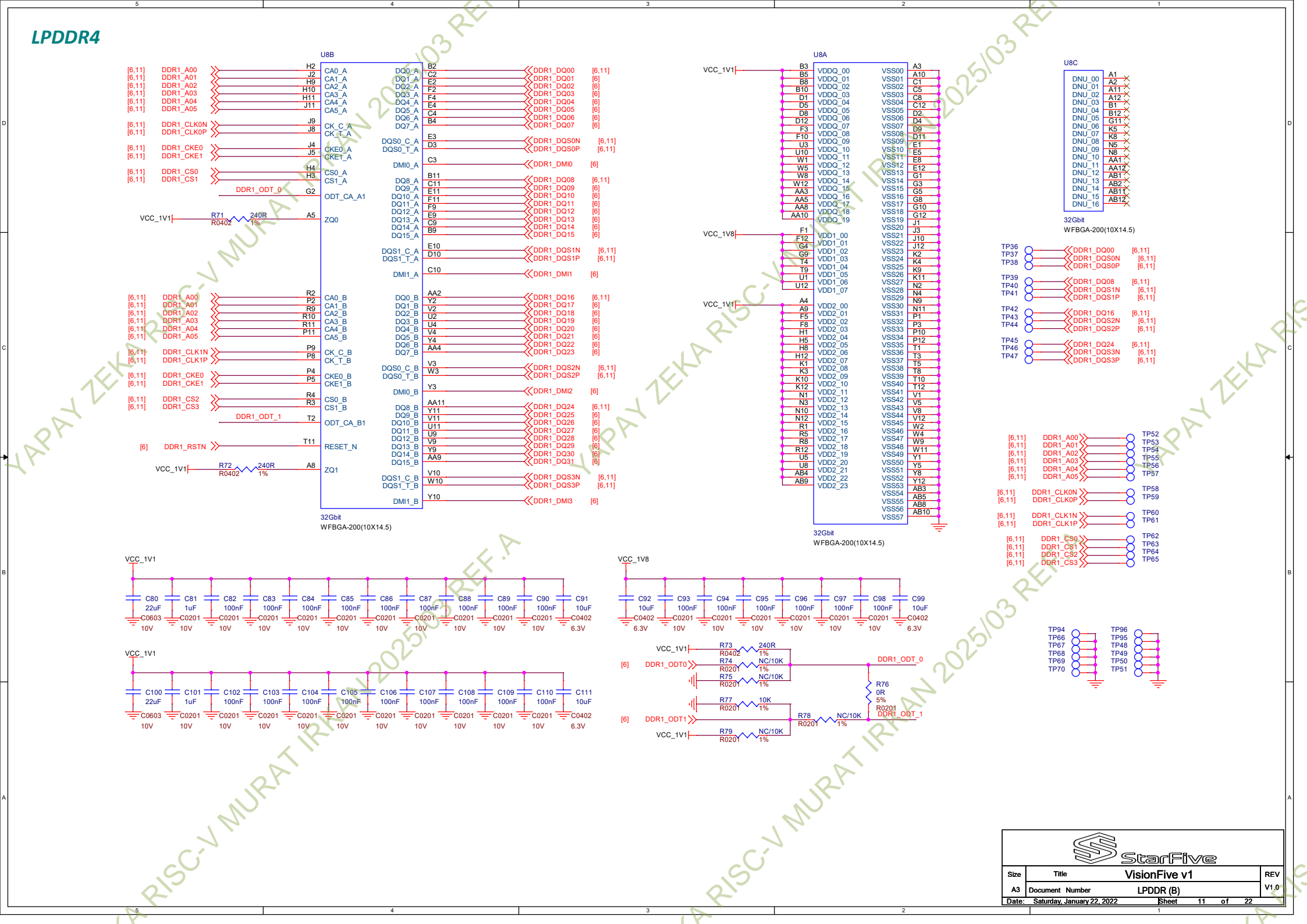
VIC MIPI&USB



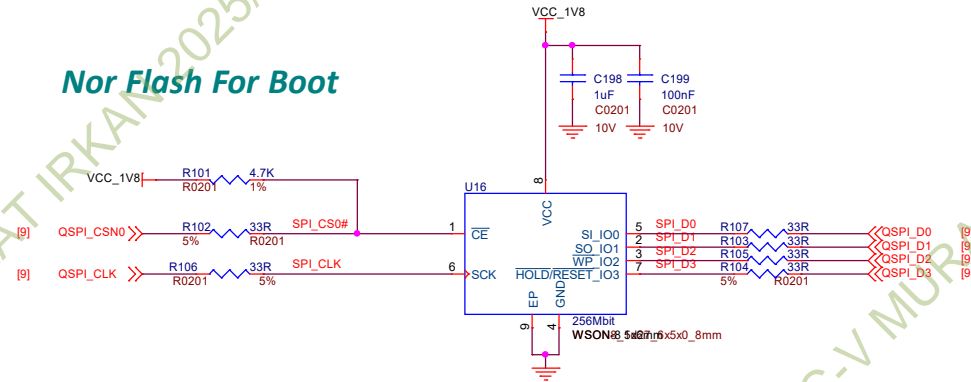
VIC Clock&



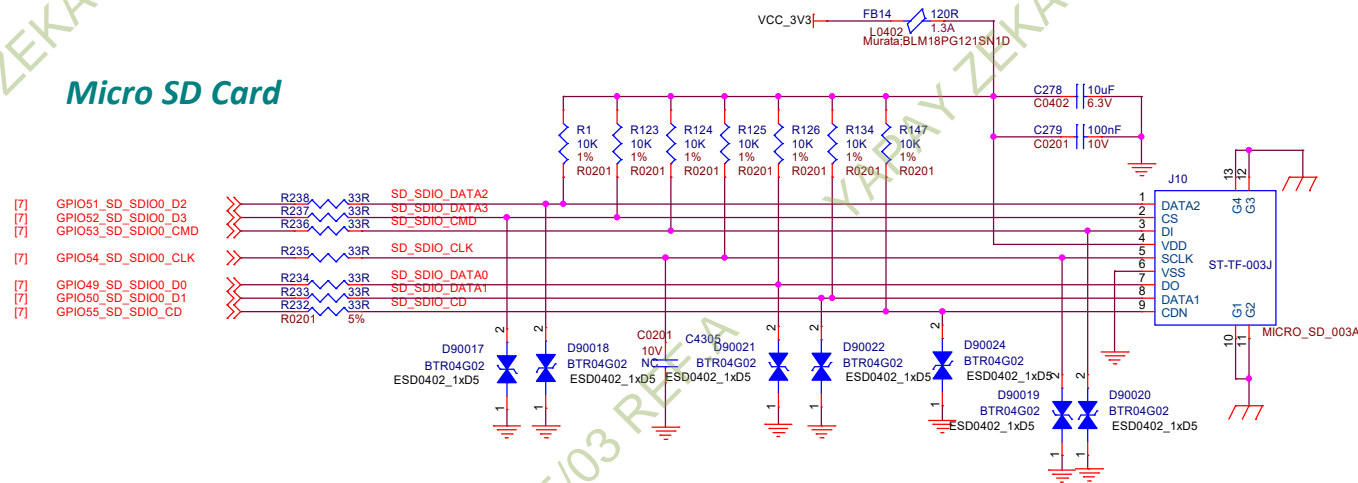
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A3	Document Number	VIC HighIF & Ctrl Other
Date:	Saturday, January 22, 2022	Sheet 9 of 22

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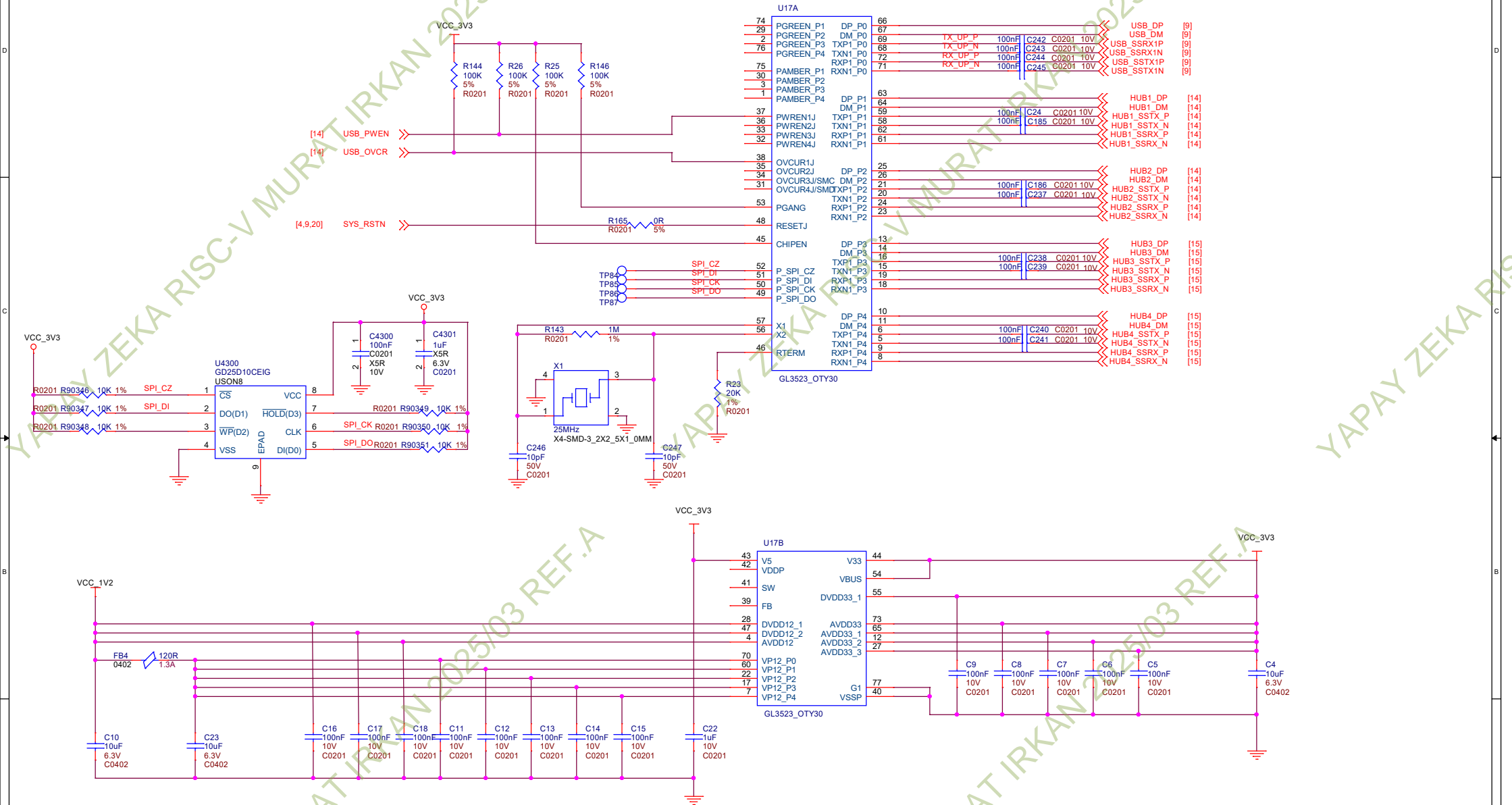
Nor Flash For Boot

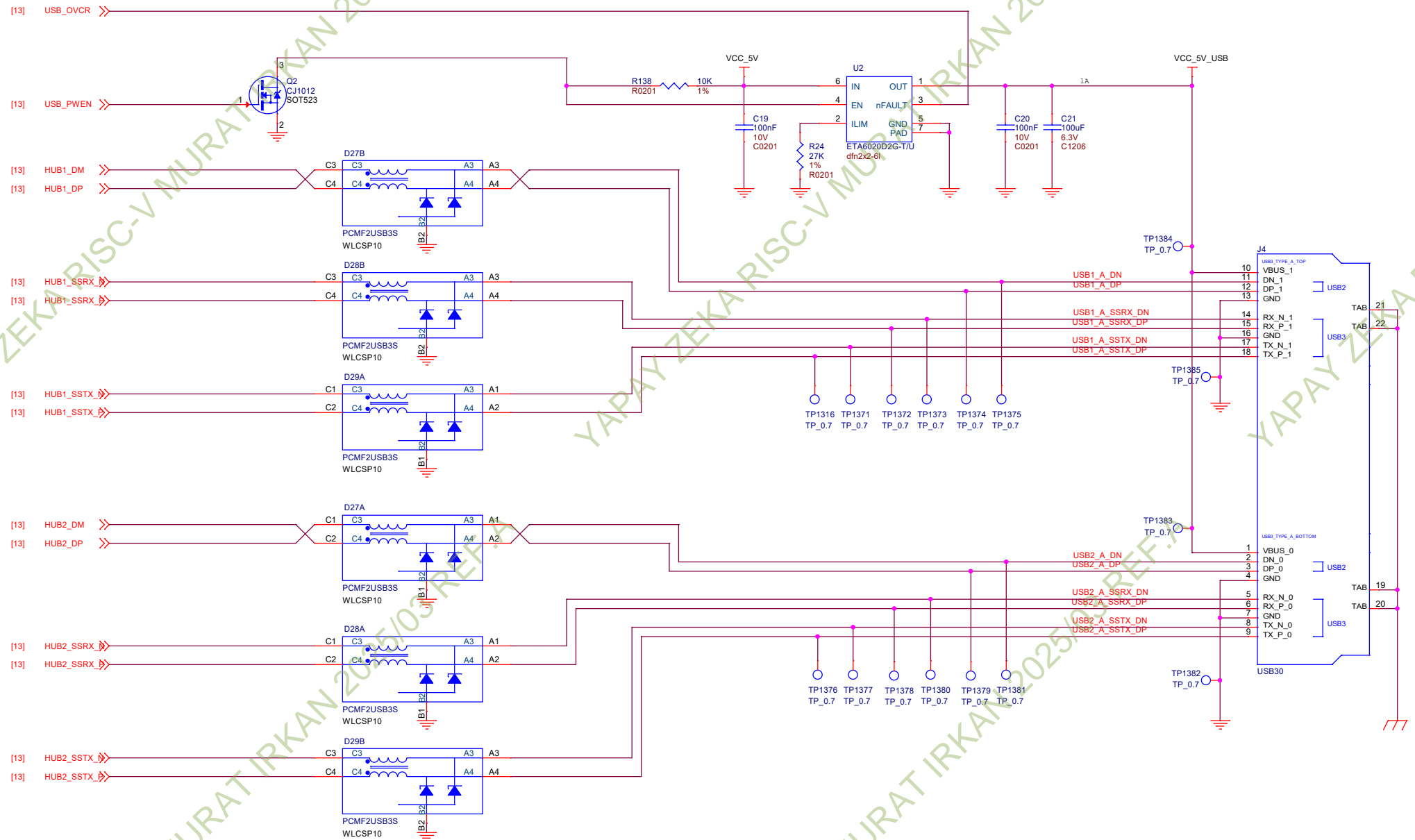


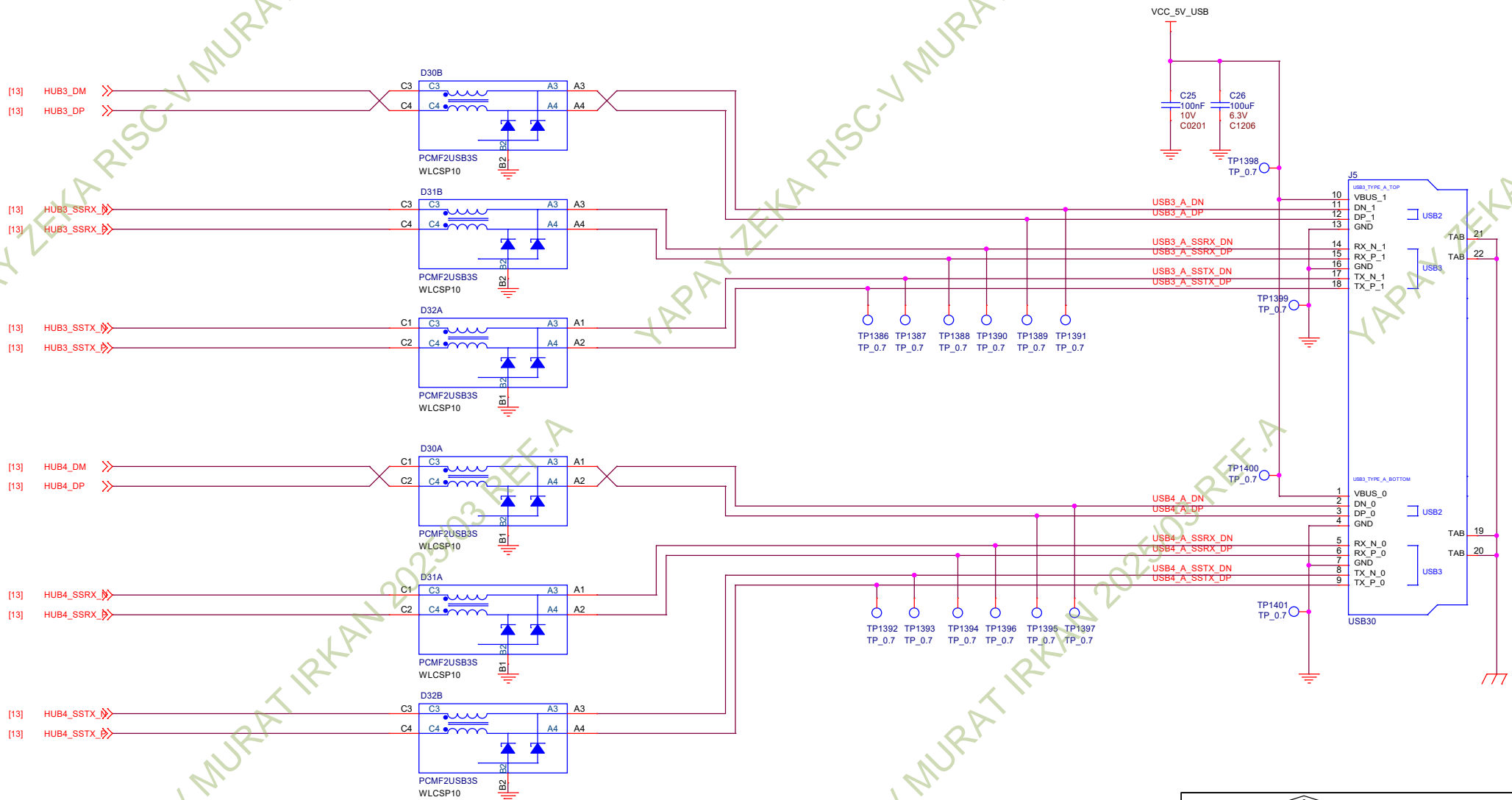
Micro SD Card



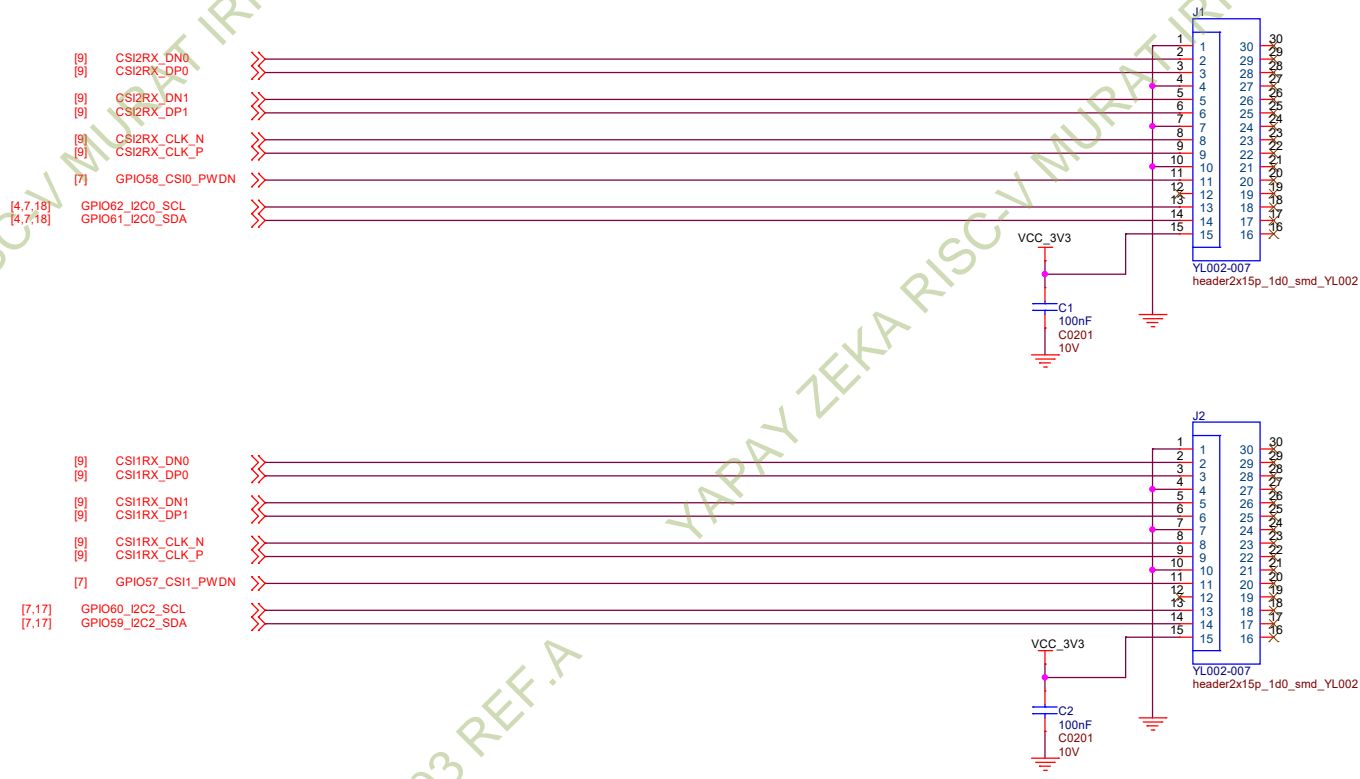
USB 3.0 HUB
TBD







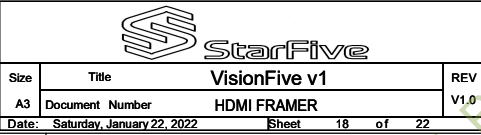
CAMERA CONNECTOR

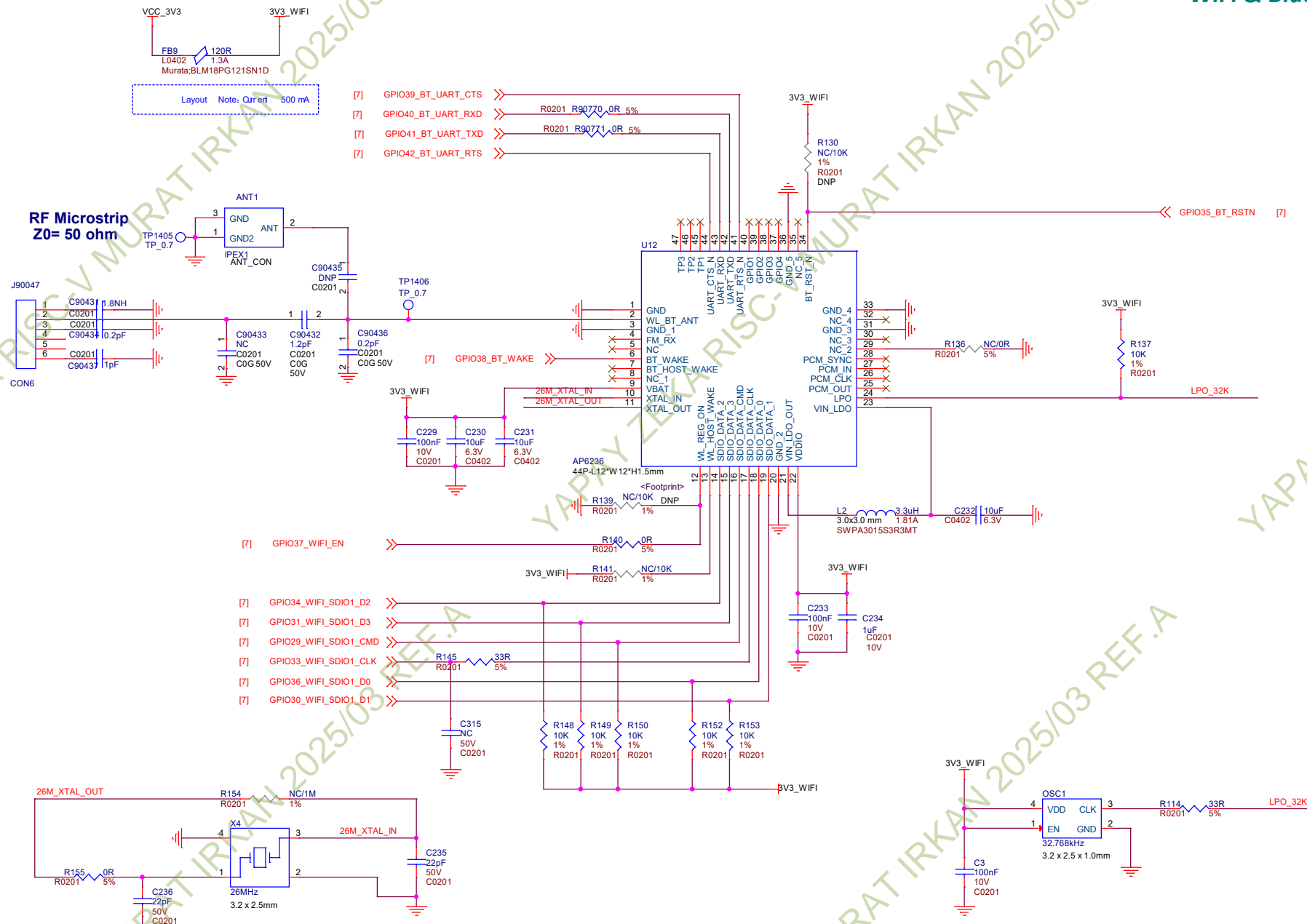


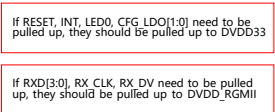
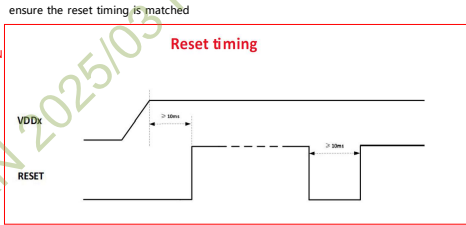
[9]	CDTX_DSI_DN1
[9]	CDTX_DSI_DP1
[9]	CDTX_DSI_CN
[9]	CDTX_DSI_CP
[9]	CDTX_DSI_DN0
[9]	CDTX_DSI_DP0
[7,16]	GPIO60_I2C2_SCL
[7,16]	GPIO59_I2C2_SDA



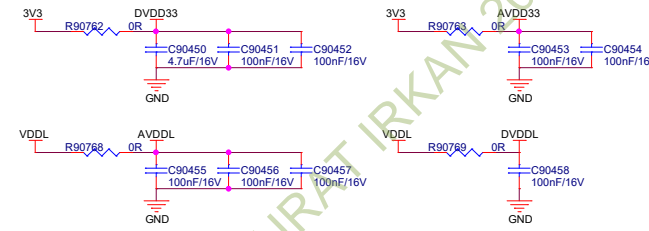
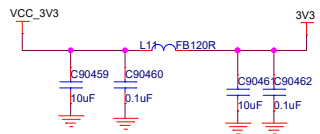
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A3	Document Number	DSI, AUDIO JACK	V1.0
Date:	Saturday, January 22, 2022	Sheet	17 of 22



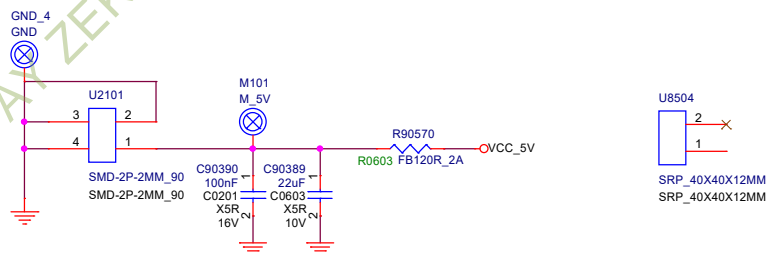
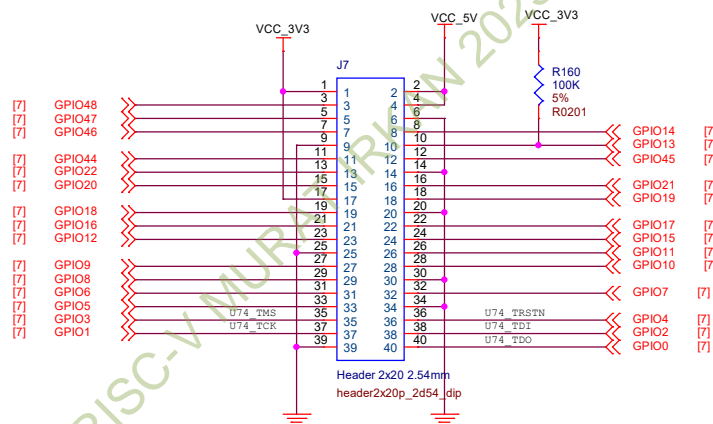




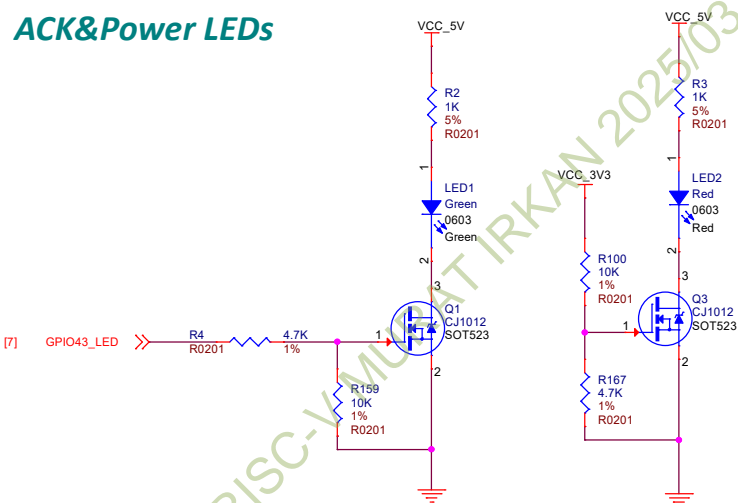
CFG_LDO[1:0]	RGMII Voltage Selection
2'b00	External 3.3V
2'b01	Internal 2.5V
2'b10	Internal 1.8V
2'b11	Not Available



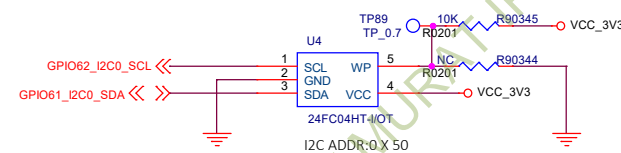
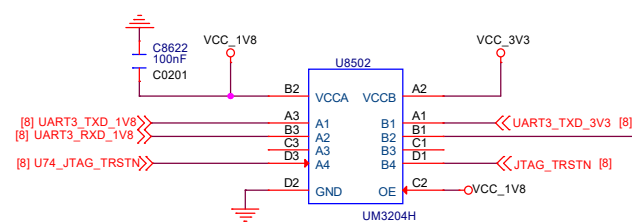
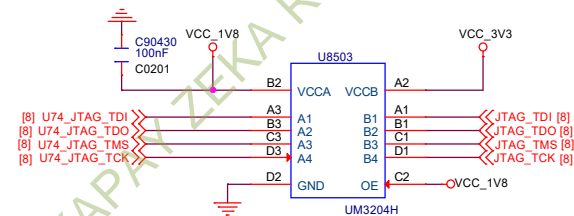
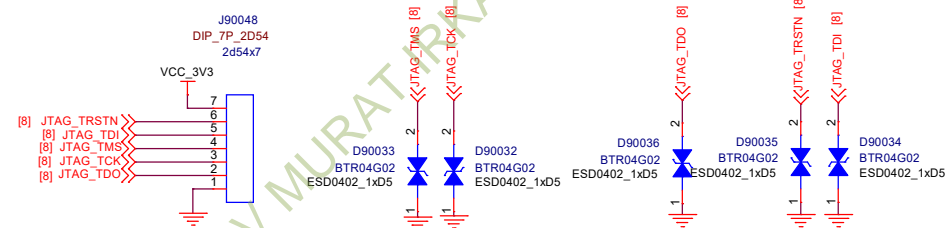
PI connector



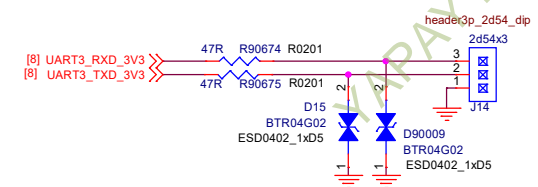
ACK&Power LEDs



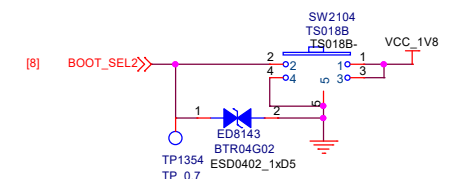
U74 JTAG 3V3 for Tag-Connect cable



DEBUG_3V3



Boot from UART



USB Type-C Power

