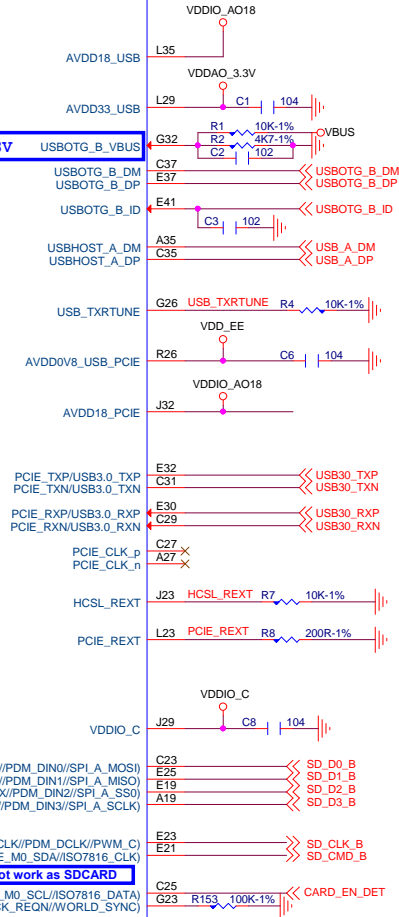


02 CPU01

U1E

LIB VER: 2018-01-05

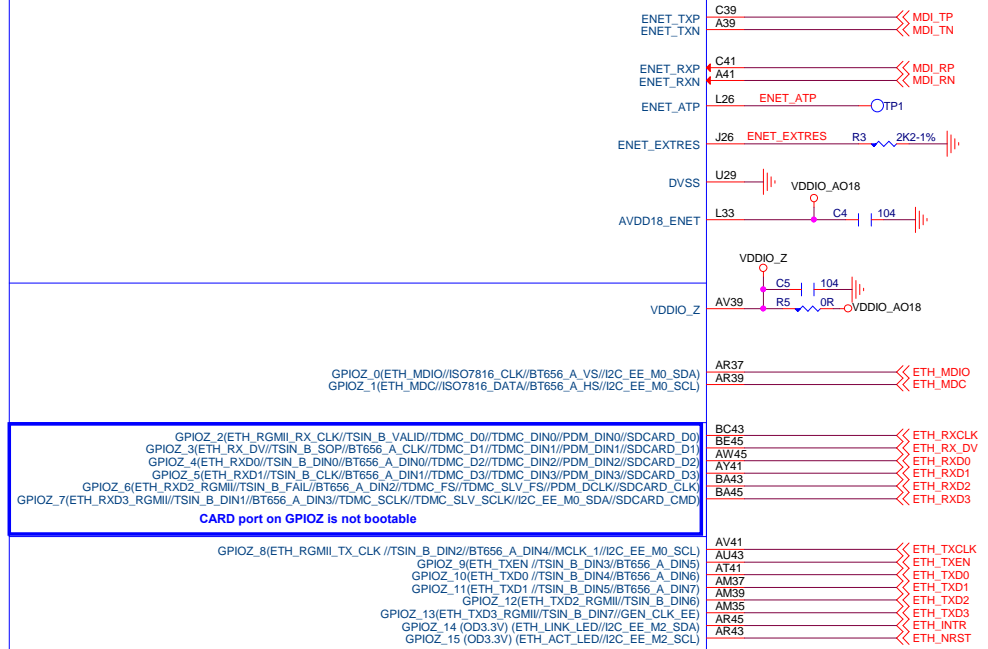
Tolerance 1.8V



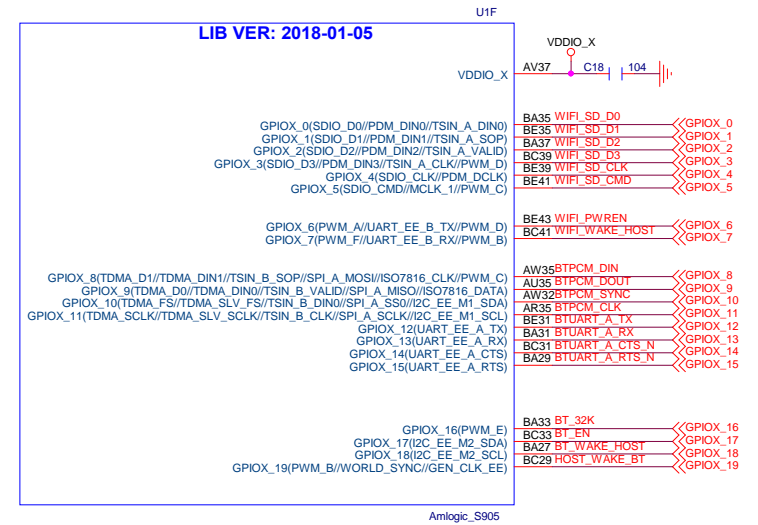
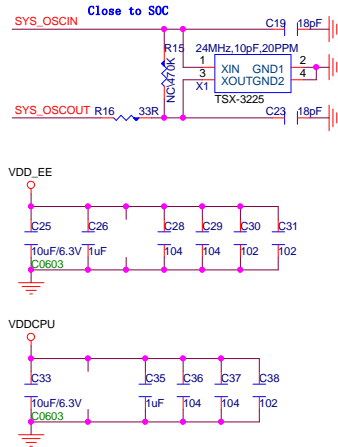
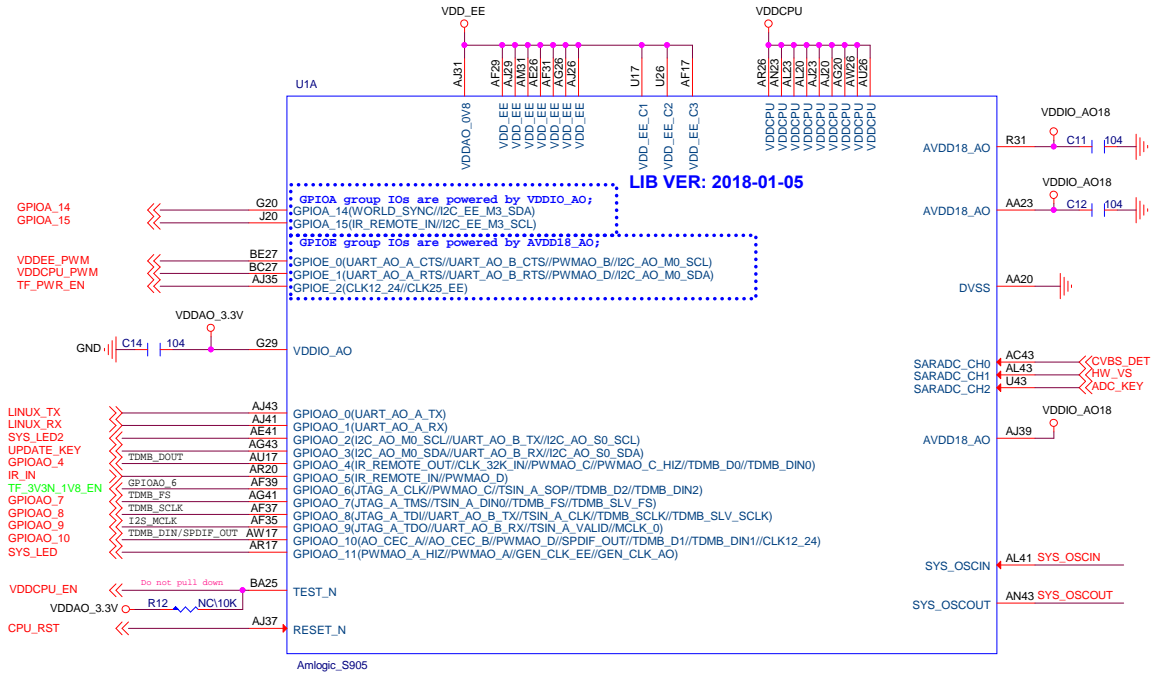
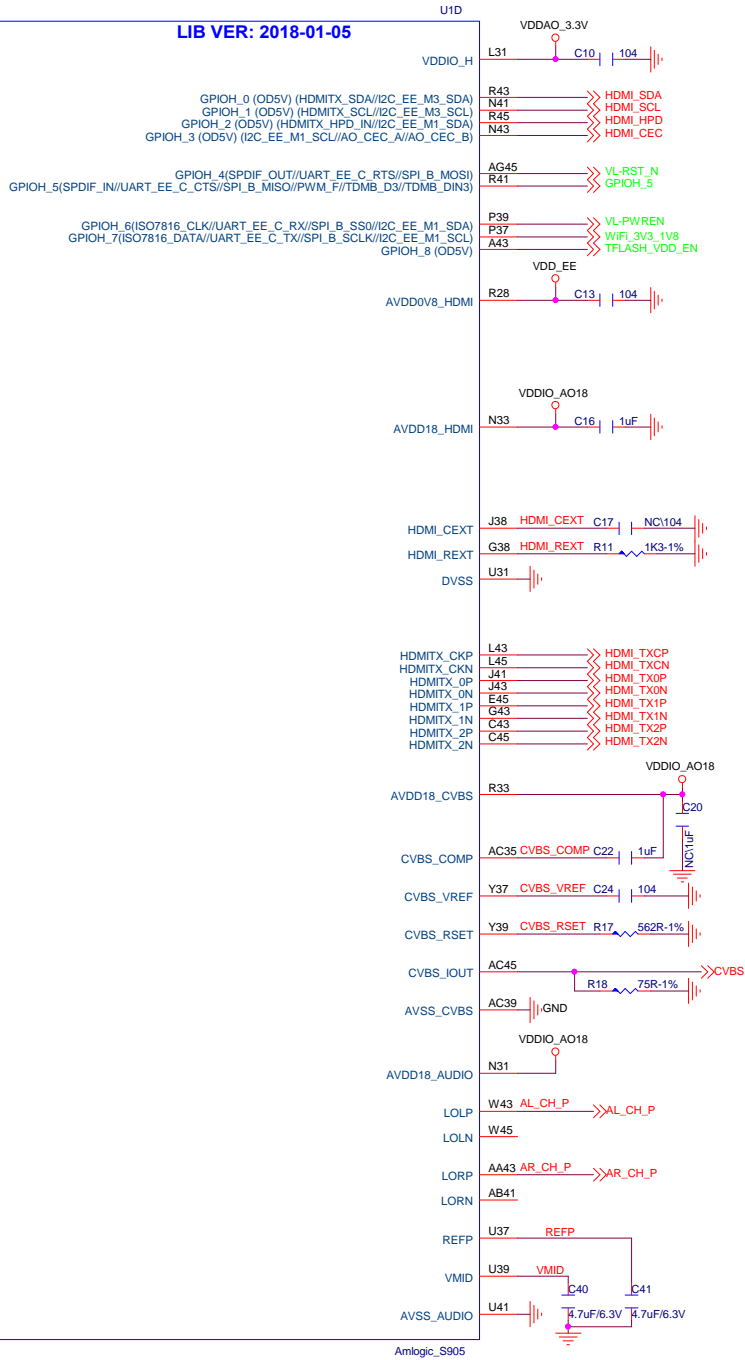
Amlogic_S905

U1G

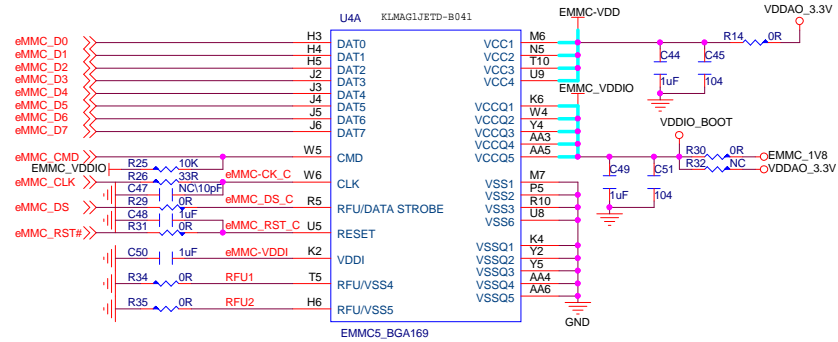
LIB VER: 2018-01-05



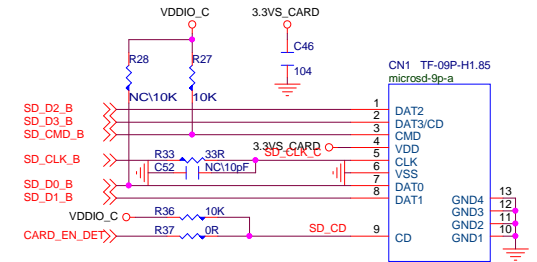
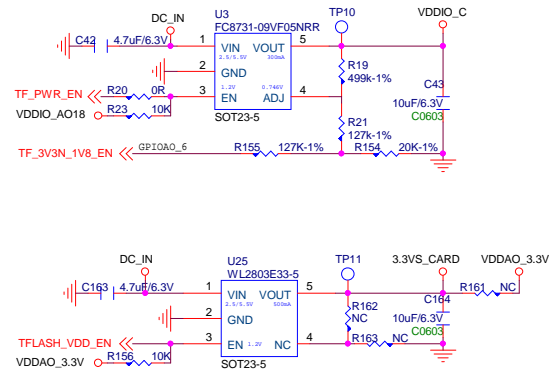
03 CPU02



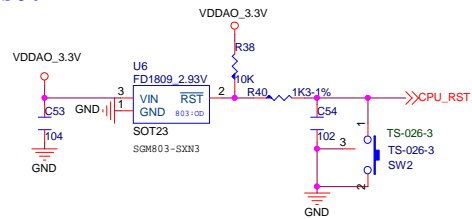
eMMC



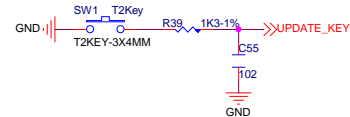
MicroSD



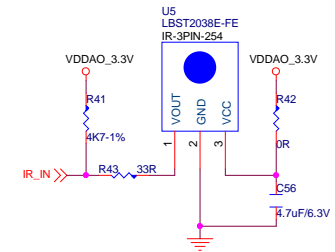
CPU reset



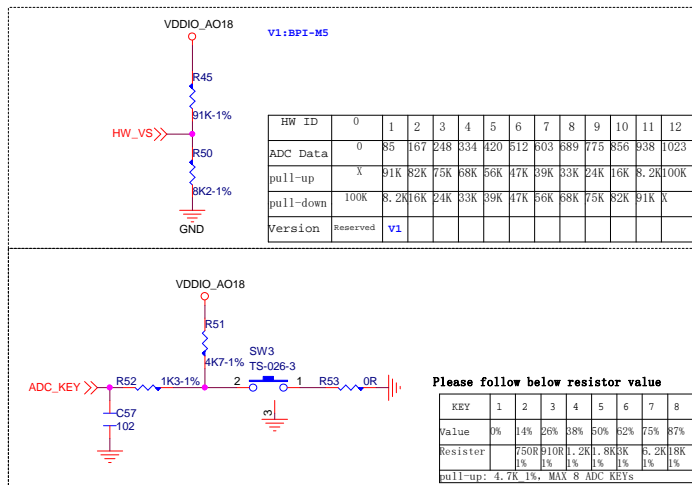
Update button



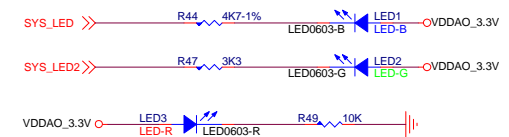
IR-IN



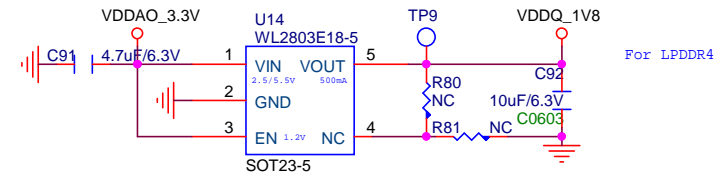
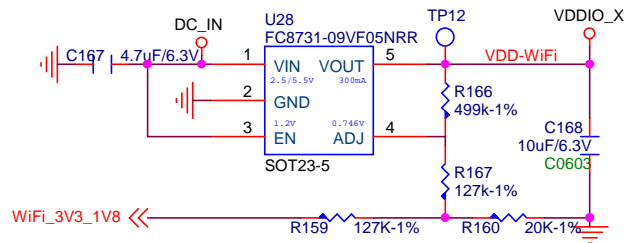
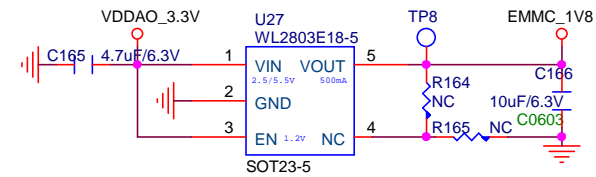
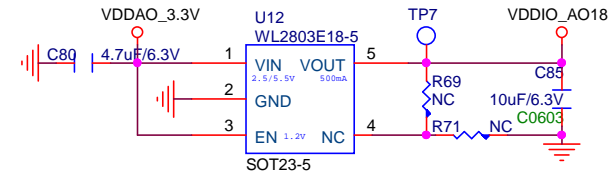
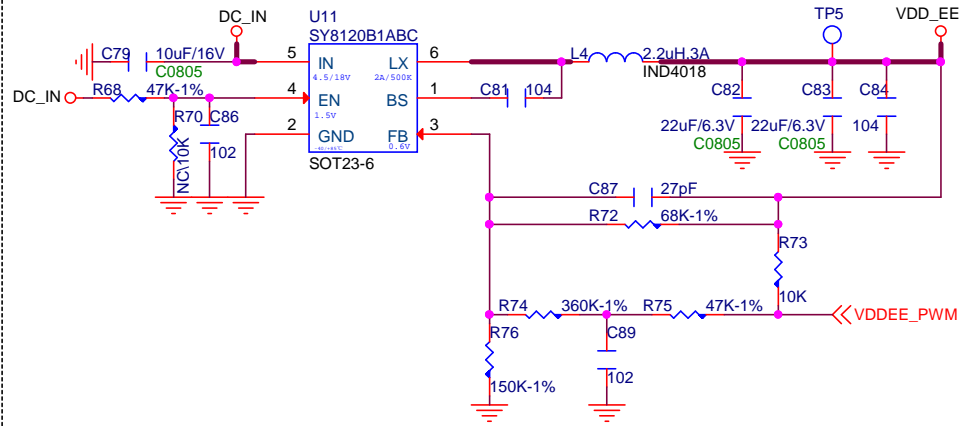
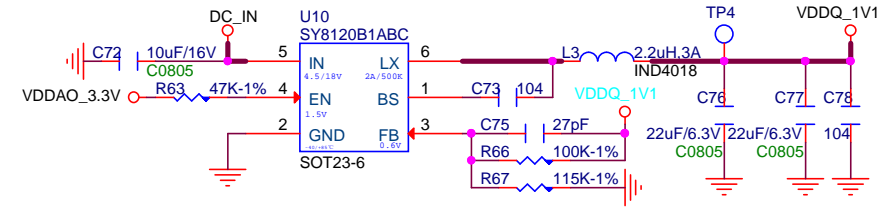
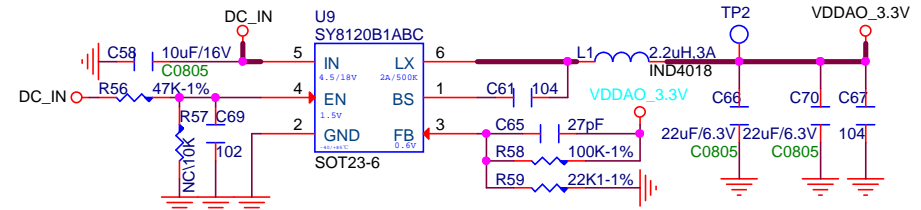
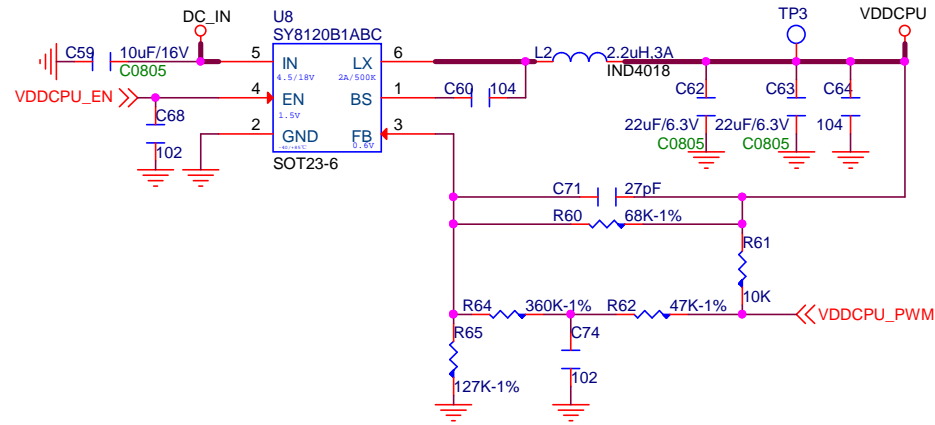
linux debug



System LED



05 Power



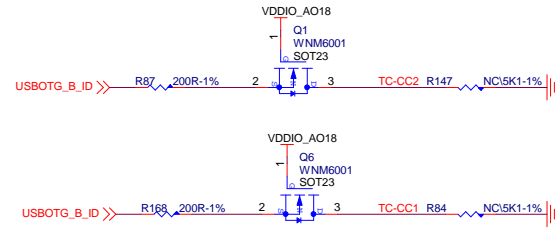
06-Ethernet

PCB layout for the Ethernet section, featuring the RTL8211F(D)I chip (U15) and various peripheral components.

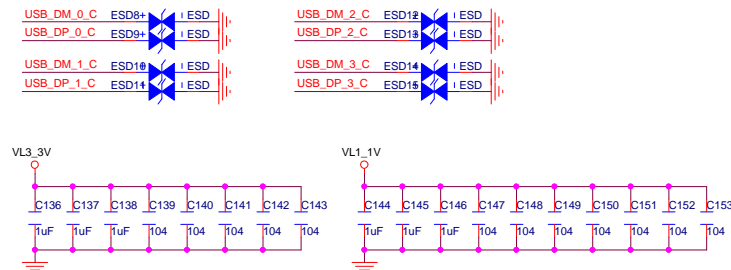
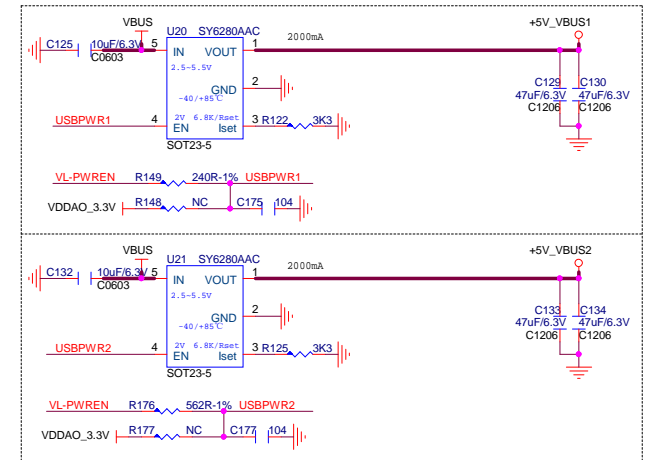
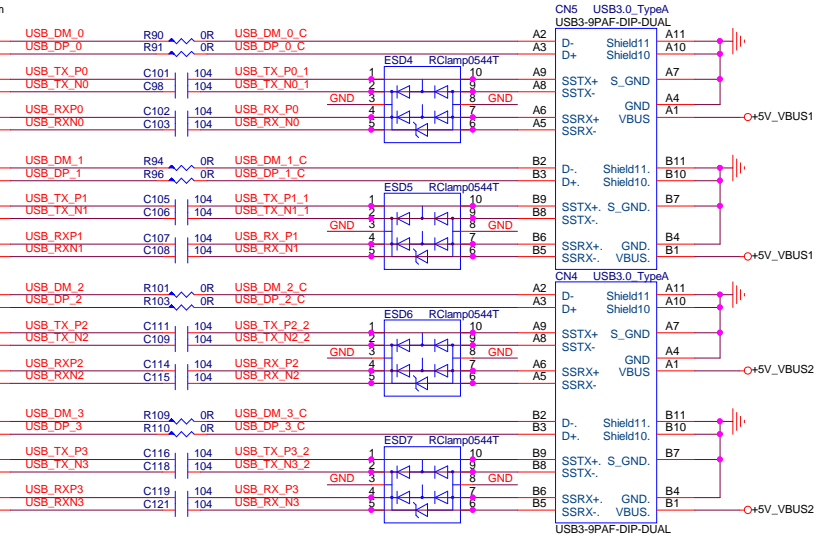
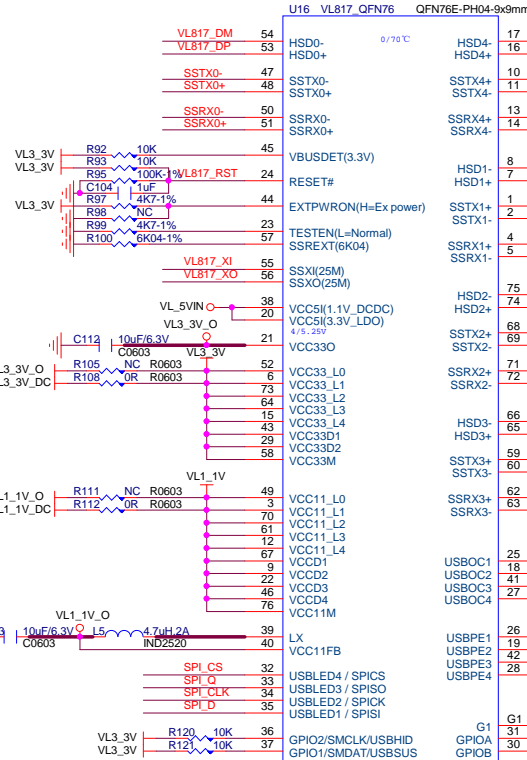
Chip Pinout (RTL8211F(D)I):

- MDI0p: 1, MDI0n: 2, MDI1p: 4, MDI1n: 5, MDI2p: 6, MDI2n: 7, MDI3p: 9, MDI3n: 10
- ETH_MDIO: RGMII_MDIO_RG2, RGMII_MDIO_MDC
- ETH_RX_CLK: RG3, RG6, RG11, RG16
- ETH_TX_CLK: CG1, CG2, CG3, CG4
- ETH_RXD0: 0_RXD0, ETH_RXD1: 0_RXD1, ETH_RXD2: 0_RXD2, ETH_RXD3: 0_RXD3
- ETH_TXD0: 0_TXD0, ETH_TXD1: 0_TXD1, ETH_TXD2: 0_TXD2, ETH_TXD3: 0_TXD3
- ETH_INTR: RG19, ETH_N_RST: RG20
- LED2/CFG0: 0_LED2/CFG0, LED2/CFG1: 0_LED2/CFG1, LED2/CFG2: 0_LED2/CFG2, LED2/CFG3: 0_LED2/CFG3, LED2/CFG4: 0_LED2/CFG4, LED2/CFG5: 0_LED2/CFG5, LED2/CFG6: 0_LED2/CFG6, LED2/CFG7: 0_LED2/CFG7, LED2/CFG8: 0_LED2/CFG8, LED2/CFG9: 0_LED2/CFG9, LED2/CFG10: 0_LED2/CFG10, LED2/CFG11: 0_LED2/CFG11, LED2/CFG12: 0_LED2/CFG12, LED2/CFG13: 0_LED2/CFG13, LED2/CFG14: 0_LED2/CFG14, LED2/CFG15: 0_LED2/CFG15, LED2/CFG16: 0_LED2/CFG16, LED2/CFG17: 0_LED2/CFG17, LED2/CFG18: 0_LED2/CFG18, LED2/CFG19: 0_LED2/CFG19, LED2/CFG20: 0_LED2/CFG20, LED2/CFG21: 0_LED2/CFG21, LED2/CFG22: 0_LED2/CFG22, LED2/CFG23: 0_LED2/CFG23, LED2/CFG24: 0_LED2/CFG24, LED2/CFG25: 0_LED2/CFG25, LED2/CFG26: 0_LED2/CFG26, LED2/CFG27: 0_LED2/CFG27, LED2/CFG28: 0_LED2/CFG28, LED2/CFG29: 0_LED2/CFG29, LED2/CFG30: 0_LED2/CFG30, LED2/CFG31: 0_LED2/CFG31, LED2/CFG32: 0_LED2/CFG32, LED2/CFG33: 0_LED2/CFG33, LED2/CFG34: 0_LED2/CFG34, LED2/CFG35: 0_LED2/CFG35, LED2/CFG36: 0_LED2/CFG36, LED2/CFG37: 0_LED2/CFG37, LED2/CFG38: 0_LED2/CFG38, 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TypeC



USB_A_DM >>> R88 OR VL817_DM
 USB_A_DP >>> C89 OR VL817_DP
 USB30_TXN >>> C99 | 104 SSRX0-
 USB30_TXP >>> C99 | 104 SSRX0+
 USB30_RXN >>> C100 | 104 SSTX0-
 USB30_RXP >>> C97 | 104 SSTX0+
 VL-PWREN >>>
 VL-RST_N <<< R169 OR VL817_RST

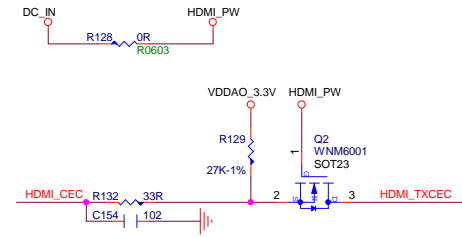
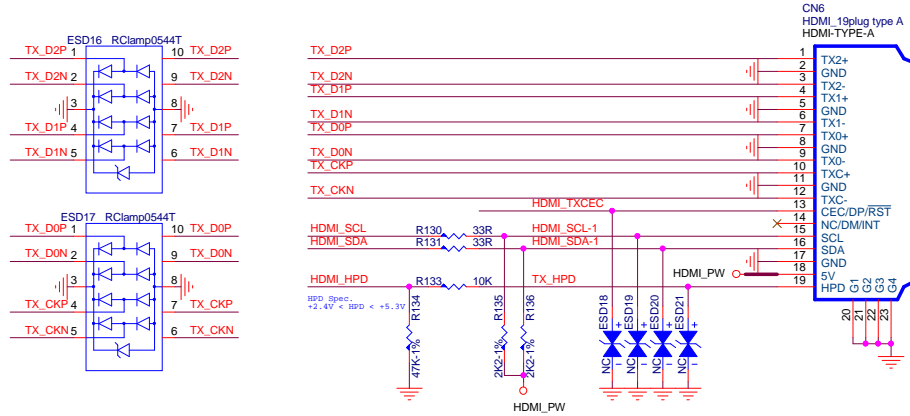


08 HDMI-GPIO

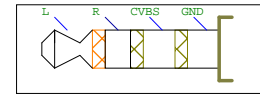
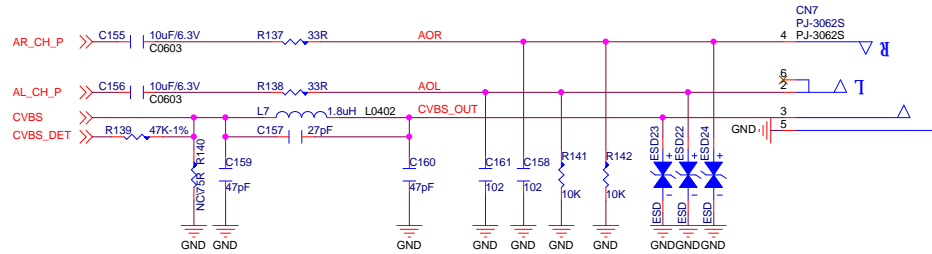
HDMI OUT

HDMI_SDA <-> HDMI_SDA
HDMI_SCL <-> HDMI_SCL
HDMI_HPD <-> HDMI_HPD
HDMI_CEC <-> HDMI_CEC

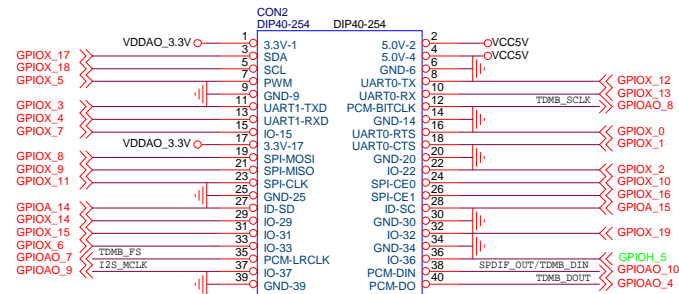
HDMI_TXCP <-> TX_CKP
HDMI_TXCN <-> TX_CKN
HDMI_TX0P <-> TX_D0P
HDMI_TX0N <-> TX_D0N
HDMI_TX1P <-> TX_D1P
HDMI_TX1N <-> TX_D1N
HDMI_TX2P <-> TX_D2P
HDMI_TX2N <-> TX_D2N



Audio DAC OUT



40 PIN GPIO



GPIOX_17 <-> R143 2K2-1% <-> VDDAO_3.3V
GPIOX_18 <-> R144 2K2-1% <-> VDDAO_3.3V
GPIOA_14 <-> R145 2K2-1% <-> VDDAO_3.3V
GPIOA_15 <-> R146 2K2-1% <-> VDDAO_3.3V

AMLOGIC_S905