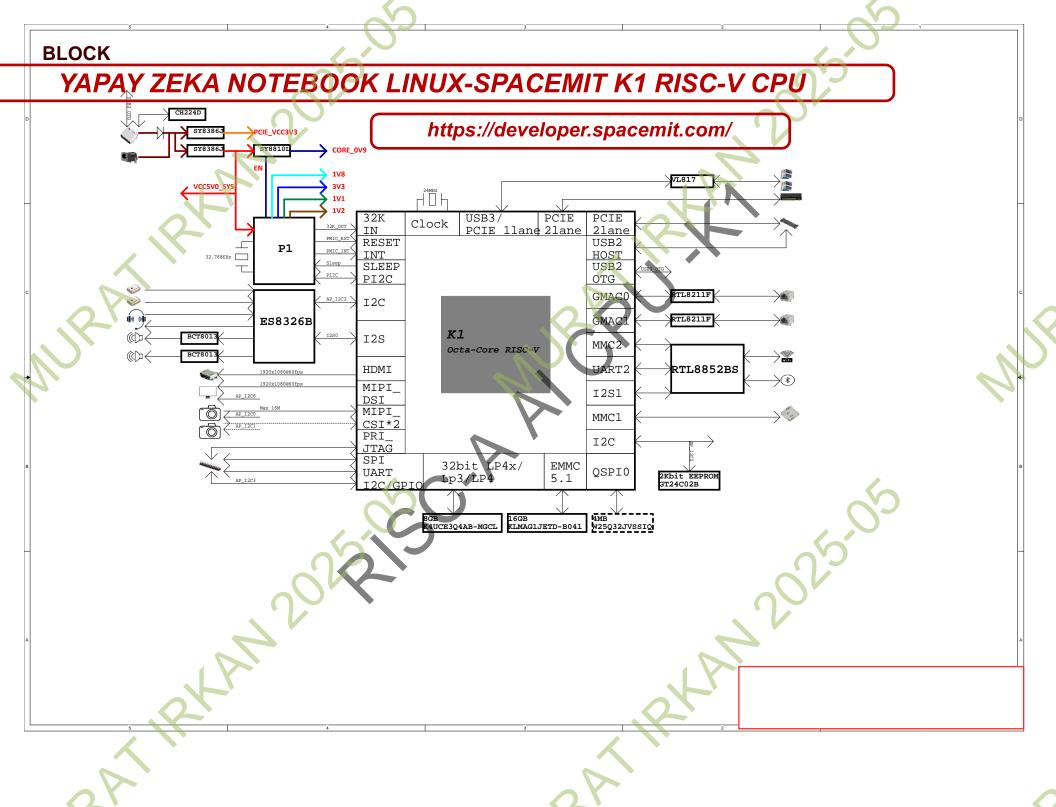
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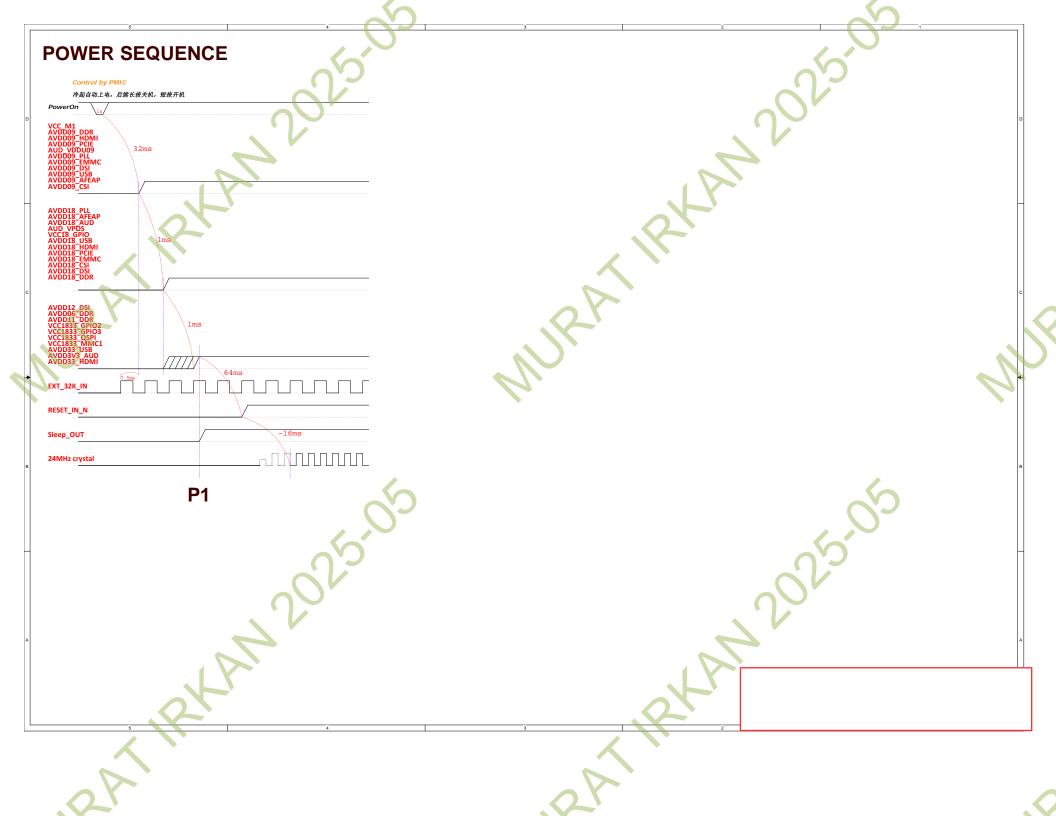
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		0/2	
	$BPI_{_}$	F3_LP4	XP200_32X1
	Revision	Date	Change Description
	V1.0	20231207	Initial version
c	V2.0	20240131	Update based on test results
MIRY	V3.0	20240401	Update based on test results

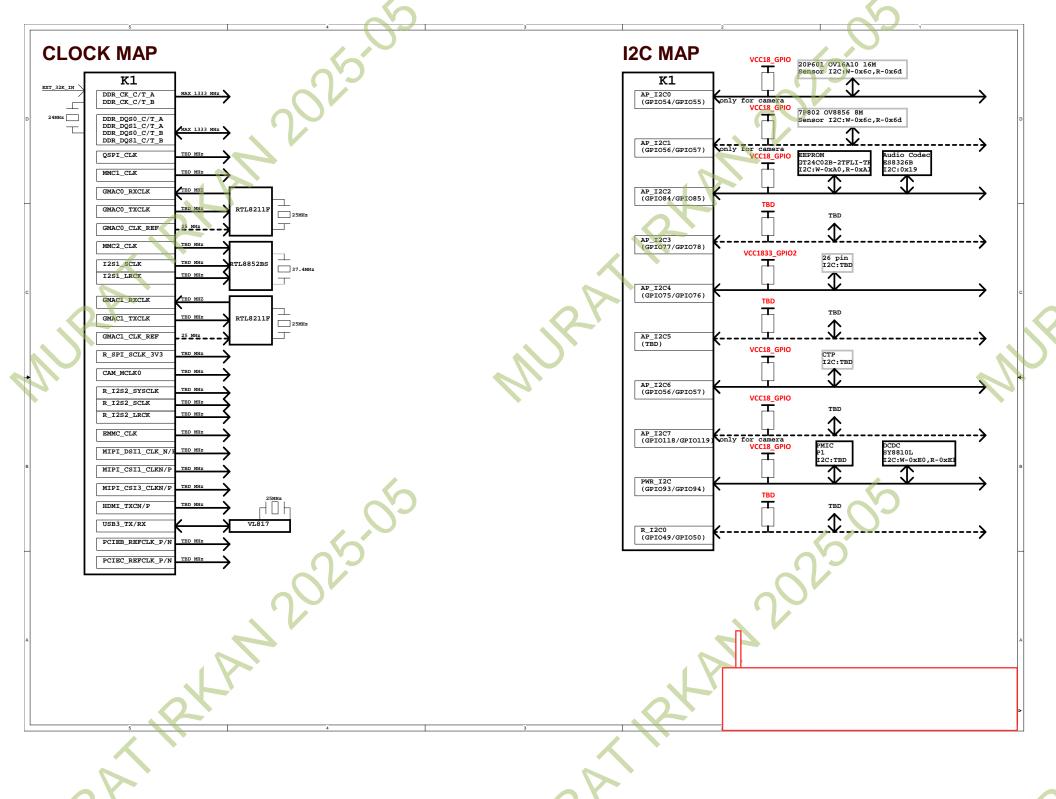
INDEX/NOTES MIJRATI IRANA 2011 01 REVISION HISTORY Page1 Page 2 02 INDEX/NOTES 03 BLOCK DIAGRAM Page3 Page4 04 POWER TREE Page5 05 POWER SEQUENCE 06 CLOCK MAP/I2C MAP Page6 07 UART MAP/OTHER MAP Page7 08 GPIO ASSIGNMENT Page8 09 CPU-eMMC-DDR-GPIO Page9 Page10 10 CPU-Perphery Pagell 11 CPU-PWR Page12 12 CPU-SYS-GPIO Page13 13 POWER-EXT DCDC Page14 14 POWER-P1 Page15 15 Memory-LPDDR4x Page16 16 Storage-eMMC Page17 17 Storage-FLASH-CARD-EEPROM Page18 18 VI-Camera Page19 19 VO-HDMI Page 20 20 VO-MIPI DSI Page 21 21 WiFi/BT-SDIO_2T2R/UART Page22 22 Ethernet-GMAC0 Page23 23 Ethernet-GMAC1 Page24 24 USB3 HUB/USB2 TypeC/DC IN Page25 25 PCIEB M.2 KEY M Page26 26 PCIEC Mini PCIE Page27 27 Audio ES8326B Page28 28 26pin GPIO/KEY/LED

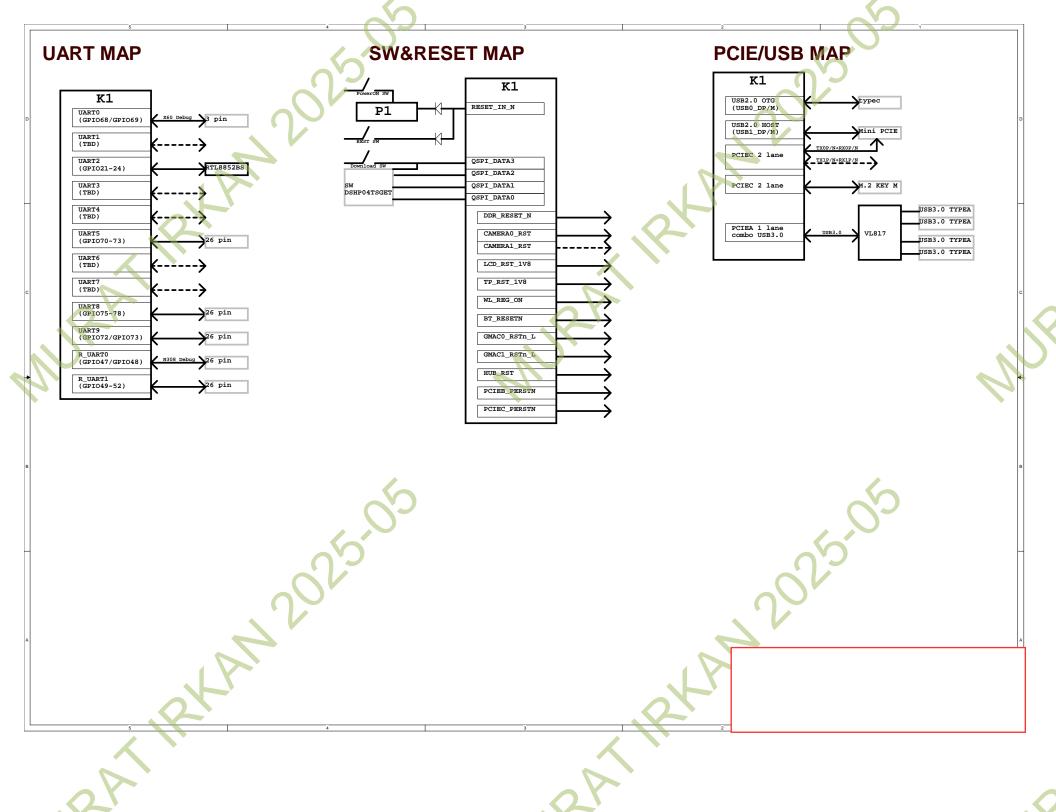


GÜÇ KONTROL ŞEMASI **POWER TREE** P1 U1401 K1 Uxxx Type-c USBVB VIN1 0.5~3.4V@3.5A BUCK1 0.9V±1% Seq:1 CH224D Uxxx CORE_0V9 AVDD09 HDMI AVDD09 EMMC AVDD09_CSI AVDD09_PCIE AVDD09_DSI VIN2 0.5~3.4V@3.5A BUCK2 DC_IN VCC18_GPIO AVDD18_HDMI AVDD18_CSI AVDD18_DSI VCC1V8 SYS 1.8V±1% Seq:2 VIN3 0.5~3.4V@2.5A BUCK3 RJ45 QSPI_VCC1833 SD_VCC33 VCC3V3_SYS VCC1833 GPIO2 VCC1833 QSPI AVDD33 USB MMC_VCC3V3 3.3V±1% Seq:3 VIN4 0.5~3.4V@2.5A BUCK4 BUCK4_3V3 VCC1833_GPIO3 AVDD3V3_AUD AVDD33_HDMI PCIE_VCC3V3 3.3V±1% SWIN SWOUT 1A LCD_VCC5V0 PCIE VCC3V3 EXT DCDC 0V6 2.1V±1% Seq:1 VIN5 0.5~3.4V@2.5A BUCK5 BUCK5_2V1 AVDD11_DDR LP4x_VDD11 1.1V±1% Seq:3 VIN6 0.5~3.4V@2.5A BUCK6 3.3V±1% Seq:3 0.5~3.4V@0.3A ALDO1 Jxx 2.8V±1% CSI AVDD28 0.5~3.4V@0.3A ALDO2 ALDO CSI AFVCC28 2.8V±1% IN 0.5~3.4V@0.3A ALDO3 2.8V±1% 0.5~3.4V@0.3A ALDO4 1.8V±2% Always 0.5~3.4V@0.2A AONLDO AVDD12_DSI 1.2V±1% Seq:3 0.5~3.4V@0.3A DLDO1 1.2V±1% CSI_DVDD12 0.5~3.4V@0.3A DLDO2 DLDO BUCK5_2V1 IN1 1.8V±1% CSI_VCCIO18 0.5~3.4V@0.3A DLDO3 Seq: 2 AVDD18_DDR LP4x VDD18 1.8V±1% 0.5~3.4V@0.5A DLDO4 1.8V±1% 0.5~3.4V@0.3A DLDO5 Seq: 2 AVDD18_AFEAP AVDD18_PLL AVDD18_AUD 1.8V±1% BUCK5_2V1 DLDO 0.5~3.4V@0.3A DLD06 1.8V±1% 0.5~3.4V@0.5A DLD07 WiFi Uxxx VIFI VCC33 LCD VCC5V0 Seq:2 NIFI_VCCIO18 HY0_VCCIO PHY1_VCCIO: VCC1V8 SYS VCC5V0_SYS USB_VCC5V0 PA_VCC5V0 HDMI_VCC5V0 FAN_VCC5V0 VCC5V0_SYS PA Uxxx VCC3V3_SYS VCC3V3_SYS Jxx Seq:2 SY8810L UXXX Seq:1

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GPIO ASSIGNMENT

GENEL KULLANIM PİN TANIMLAMA TABLOSU

PIN	Define	CFG	Function
GPIO0	GMAC0_RXDV	1	
GPI01	GMAC0_RXD0	1	1
GPIO2	GMAC0_RXD1	1	1
GPIO3	GMAC0_RXCLK	1	1
GPIO4	GMAC0_RXD2	1	1
GPIO5	GMAC0 RXD3	1	
GPIO6	GMAC0_TXD0	1	
GPIO7	GMAC0_TXD1	1	Ethernet
GPIO8	GMAC0_TXCLK	1	-GMAC0
GPIO9	GMAC0 TXD2	1	1
GPIO10	GMAC0_TXD3	1	
GPIO11	GMACO_TXEN	1	1
GPIO12	GMAC0 MDC	1	1
GPIO13	GMACO MDIO	1	1
GPIO14	GMAC0_INT_N	1	1
GPIO15	MMC2_DATA3	1	
GPIO16	MMC2 DATA2	1	1
GPIO17	MMC2 DATA1	1	1
GPIO18	MMC2 DATA0	1	1
GPIO19	MMC2_CMD	1	1
GPIO20	MMC2_CLK	1	1
GPIO21	UART2_TXD	1	WiFi/BT
GPIO22	UART2 RXD	1	1 "11 17 21
GPIO23	UART2 CTS N	1	1
GPI024	UART2 RTS N	1	1
GPIO25	I2S1_SCLK	1	1
GPI026	I2S1_LRCK	1	1
GPI027	I2S1_TXD	1	1
GPIO28	I2S1_RXD	1	1
GPI029	GMAC1_RXDV	1	
GPI030	GMAC1_RXD0	1	1
GPI031	GMAC1_RXD1	1	+
GP1031	GMAC1_RXCLK	1	+
GP1032	GMAC1_RXD2	1	+
GPI034	GMAC1_RXD3	1	+
GPIO35	GMAC1_KXD3	1	+
GPI035	GMAC1_TXD1	1	Ethernet
GP1030	GMAC1_TXCLK	1	-GMAC1
GPIO37	GMAC1_TXD2	1	+
GPI039	GMAC1_TXD3	1	+
GPIO40	GMAC1_TXEN	1	+
GPI040	GMAC1_MDC	1	+
GPIO41	GMAC1_MDIO	1	-
GPI042	GMAC1_MDIO GMAC1_INT_N	1	
GPIO43	LCD_BL_PWM_1V8	4	LCD/CTP
GPIO44	GMACO_CLK_REF	1	GMAC0
GPI045	GMACU_CLK_REF	1	GMAC1
GLIO40	GMACT_CTV_KEL	1	GIVIACT

PIN	Define	CFG	Function
GPIO110	GMAC0_RSTn_L	0	GMAC0
GPIO115	GMAC1_RSTn_L	0	GMAC1
GPIO116	WL_DIS_N	0	WiFi/BT
GPIO117	PCIEC_CLKREQN	4	PCIEC
GPI0118	I2SO_SCLK	3	
GPI0119	I2SO_LRCK	3	Audio
GPIO120	I2SO_OUT	3	Codec
GPIO121	I2SO_IN	3	
GPIO122	I2S0_SYSCLK	3	
GPI0123	HUB_PWREN	0	USB3 HUB
GPIO124	HUB_RST	0	0555_1105
GPIO125	VBUS_ON0	1	USB2
GPI0126	CODEC_IRQ	0	Audio
GPI0127	PA_SHUTDOWN	0	Codec

PIN	Define	CFG	Function
GPIO53	CAM_MCLK0	1	
GPIO54	CAM_I2CO_SCL	1	CAMERA0
GPIO55	CAM_I2CO_SDA	1	1
GPIO56	AP_I2C6_SCL	5	
GPIO57	AP_I2C6_SDA	5	LCD/CTP
GPIO58	TP_INT_1V8	0]
GPIO111	CAMERAO_RST	1	CAMERA0
GPIO112	PCIEC_WAKEN	3	PCIEC
GPIO113	CAMERAO_PDN	1	CAMERA0
GPIO114	TP_RST_1V8	0	LCD/CTP
GPIO63	BT_RESETN	0	
GPIO64	AP_WAKE_BT	0	1
GPIO65	BT_WAKE_AP	0	WiFi/BT
GPIO66	WL_WAKE_AP	0	1
GPIO67	WL_REG_ON	0	1
GPIO68	UARTO_TXD	2	X60
GPIO69	UARTO_RXD	2	Debug

PIN	Define	CFG	Function
GPIO59	PCIEB_PERSTN	4	
GPIO60	PCIEB_WAKEN	4	PCIEB
GPIO61	PCIEB_CLKREQN	4	
GPIO62	PCIEC_PERSTN	4	PCIEC
GPIO70	GPIO70	1	
GPIO71	GPIO71	1	
GPIO72	GPIO72	1	26 pin
GPIO73	GPIO73	1	
GPIO74	GPIO74	0]

红色字体GPIO表示默认、持续上拉,等效上拉电阻约60K。需要软件修改才能解除默认上拉状态

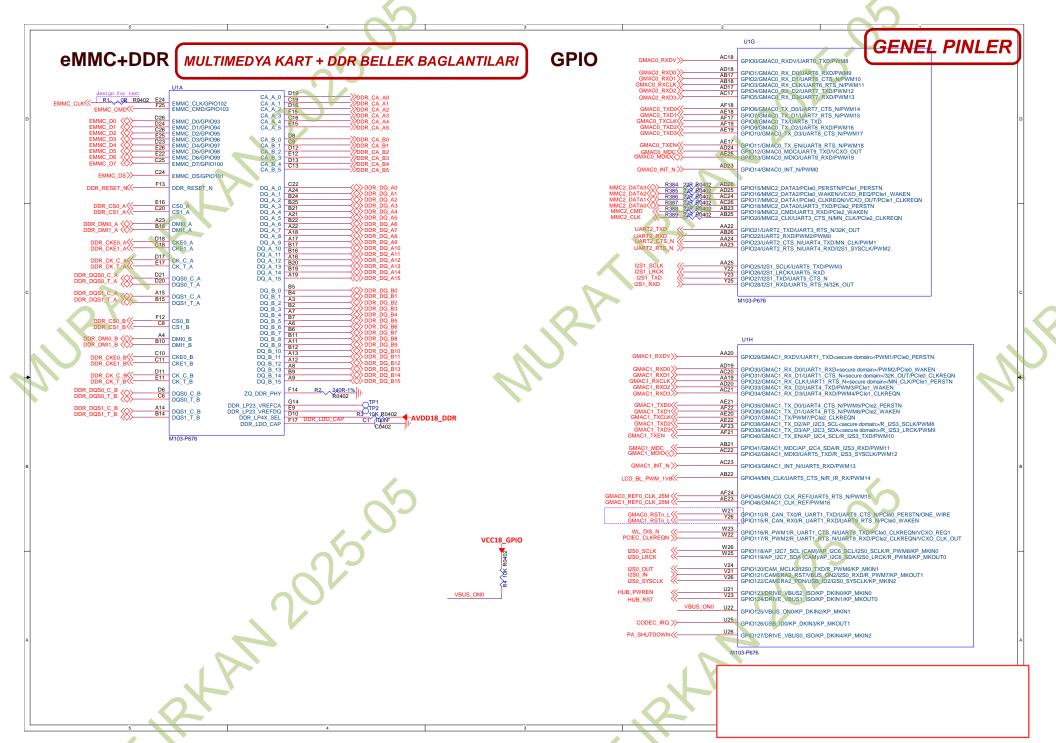
PIN	Define	CFG	Function
GPIO93	PI2C_SCL	0	
GPIO94	PI2C_SDA	0	PMIC
GPIO95	SLEEP_OUT	0	
GPIO96	GPIO96	1	LED
GPIO97	USB3_PWREN	1	USB3_HUB
GPIO81	LCD_RST_1V8	0	
GPIO82	LCD_BL_EN_1V8	0	LCD/CTP
GPIO83	LCD_PWR_EN_1V8	0	
GPIO84	AP_I2C2_SCL	4	EEPROM
GPIO85	AP_I2C2_SDA	4	Audio
GPIO86	HDMI_SCL	1	
GPIO87	HDMI_SDA	1	HDMI_OUT
GPIO88	HDMI_CEC	1	HDM1_001
GPIO89	HDMI_HPD	1	
GPIO90	GPIO90	0	
GPIO91	GPIO91	0	26 pin
GPIO92	GPI092	0	
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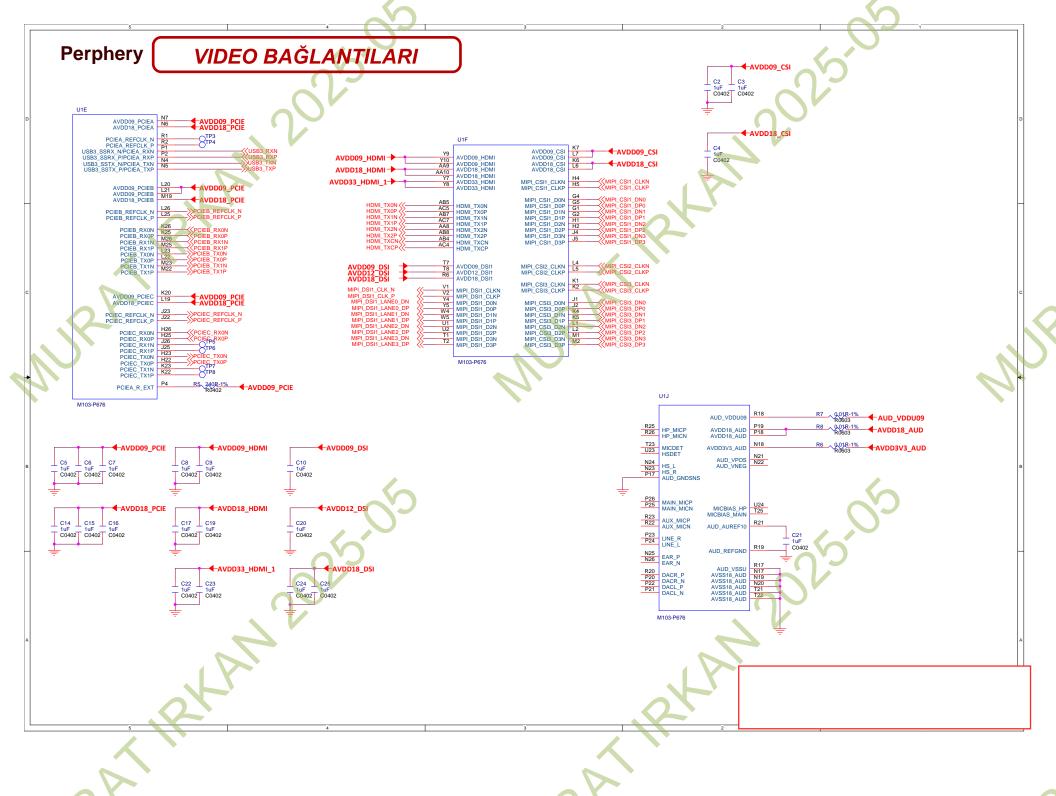
PIN	Define	CFG	Function
GPIO98	QSPI_DATA3	0	
GPIO99	QSPI_DATA2	0	1
GPI0100	QSPI_DATA1	0	SPI
GPI0101	QSPI_DATA0	0	FLASH
GPI0102	QSPI_CLK	0	
GPI0103	QSPI_CS1	0	

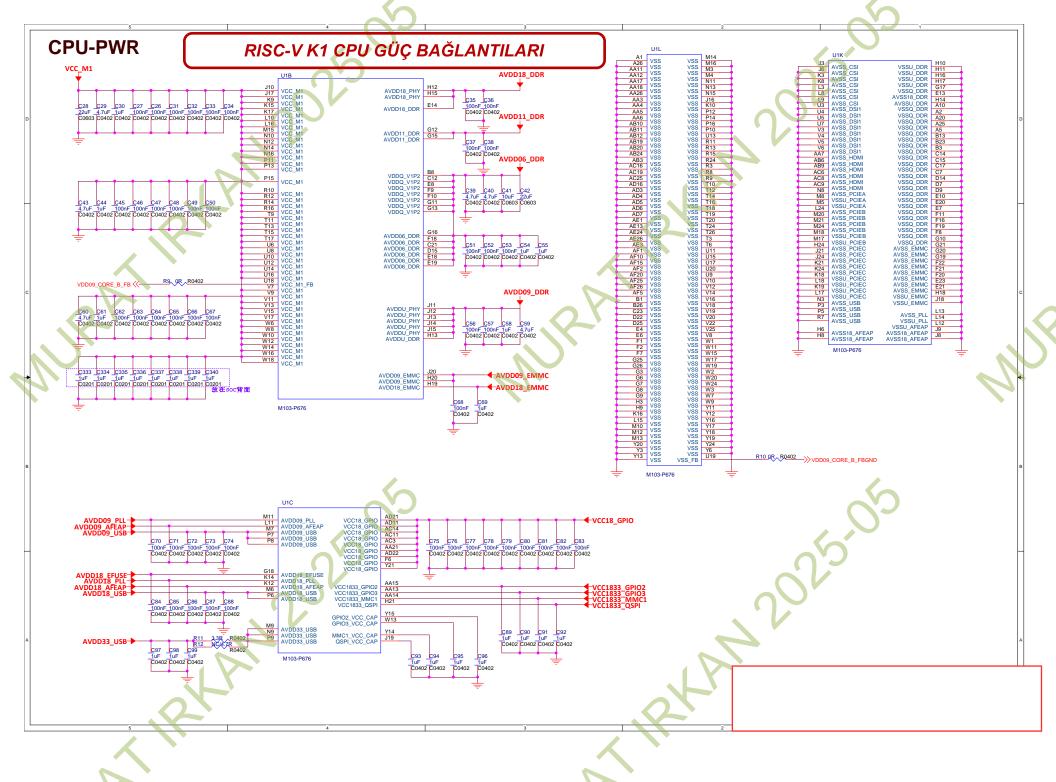
PIN	Define	CFG	Function
GPI0104	MMC1_DATA3	0	
GPI0105	MMC1_DATA2	0	
GPI0106	MMC1_DATA1	0	TF CARD
GPIO107	MMC1_DATA0	0	IF CARD
GPIO108	MMC1_CMD	0	
GPI0109	MMC1_CLK	0	

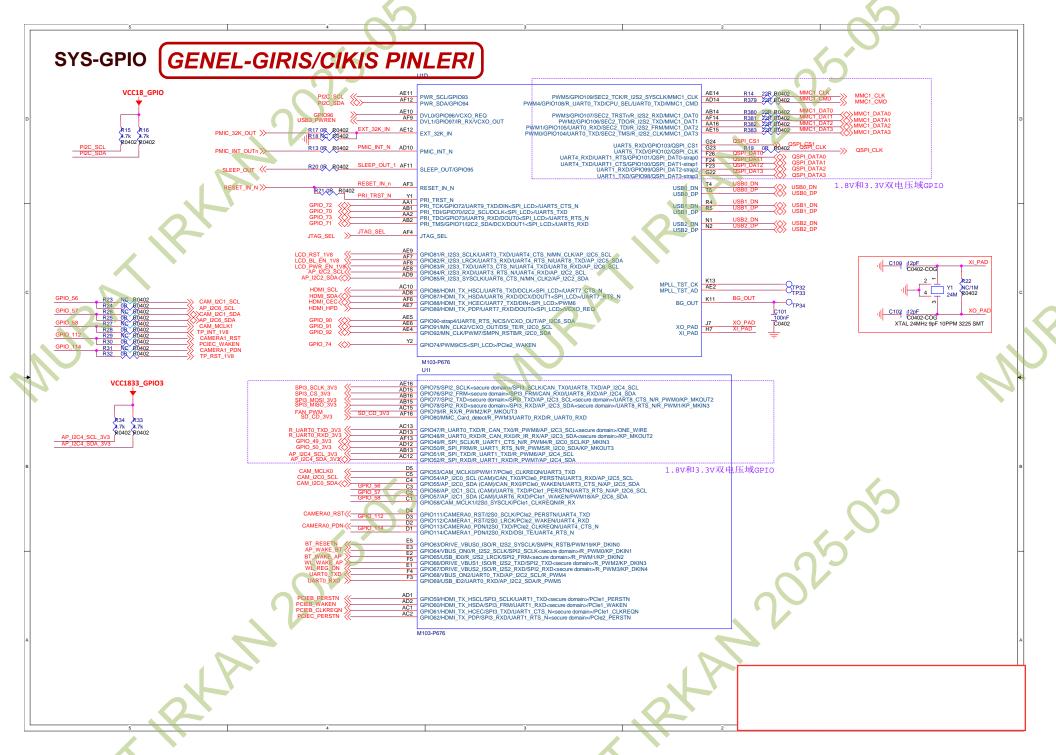
PIN	Define	CFG	Function
GPIO75	SPI3_SCLK_3V3	2	
GPIO76	SPI3_CS_3V3	2	26 pin
GPIO77	SPI3_MOSI_3V3	2	20 pin
GPIO78	SPI3_MISO_3V3	2	
GPIO79	FAN_PWM	2	FAN
GPIO80	SD_CD_3V3	1	TF CARD

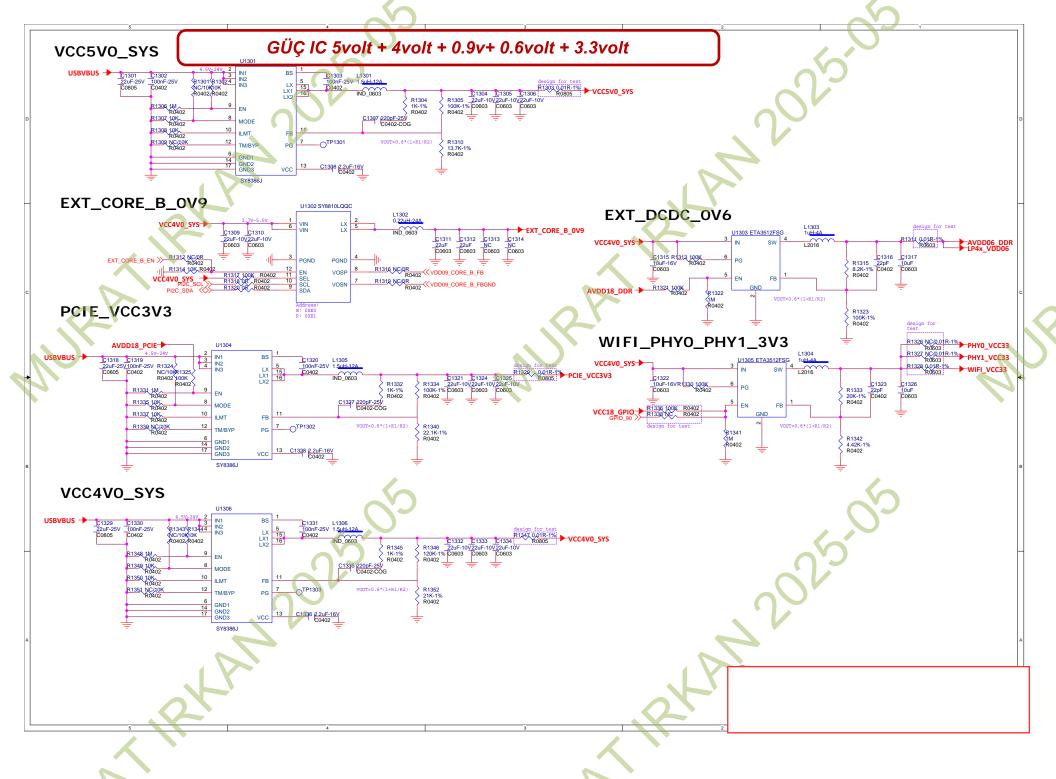
PIN	Define	CFG	Function
GPIO47	R_UARTO_TXD_3V3	1	
GPIO48	R_UARTO_RXD_3V3	1	
GPIO49	GPIO_49_3V3	0	26 pin
GPIO50	GPIO_50_3V3	0	
GPIO51	AP_I2C4_SCL_3V3	4	
CDIOES	VD T3C4 CDV 3773	1	

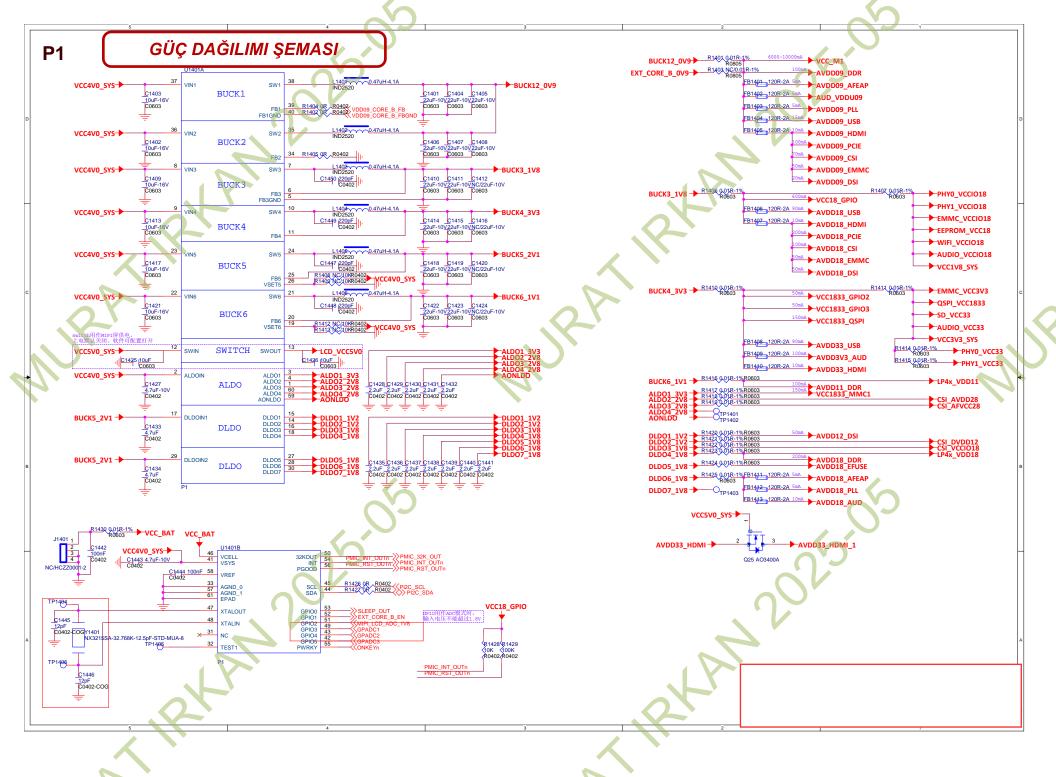


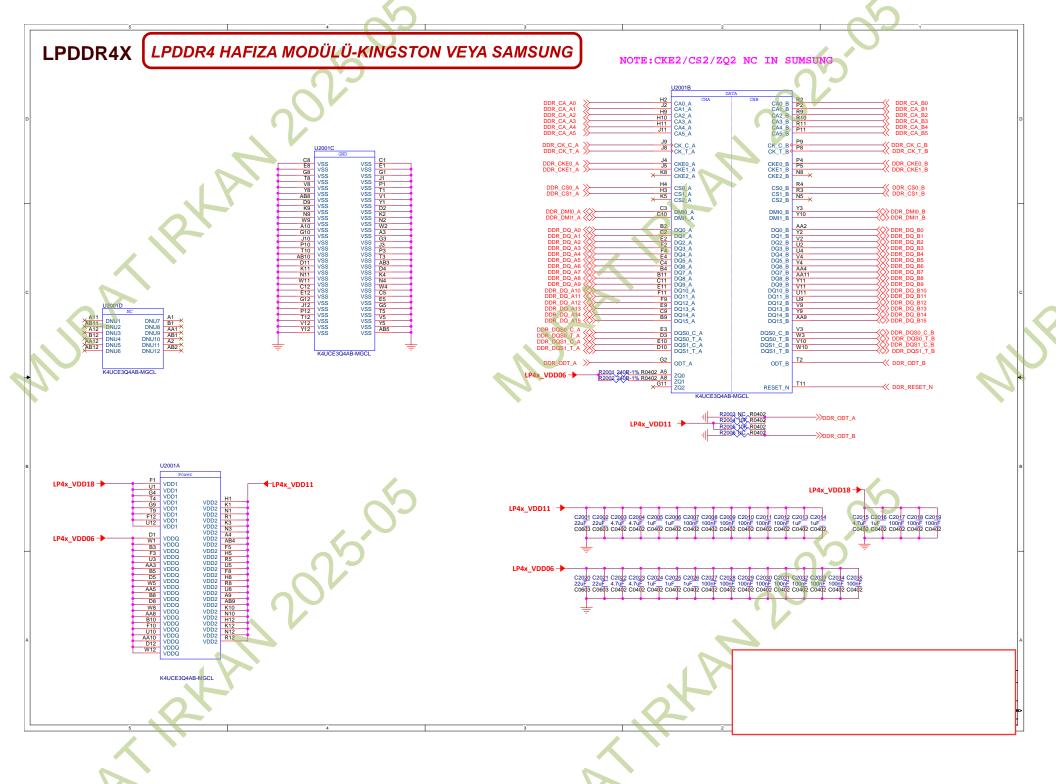


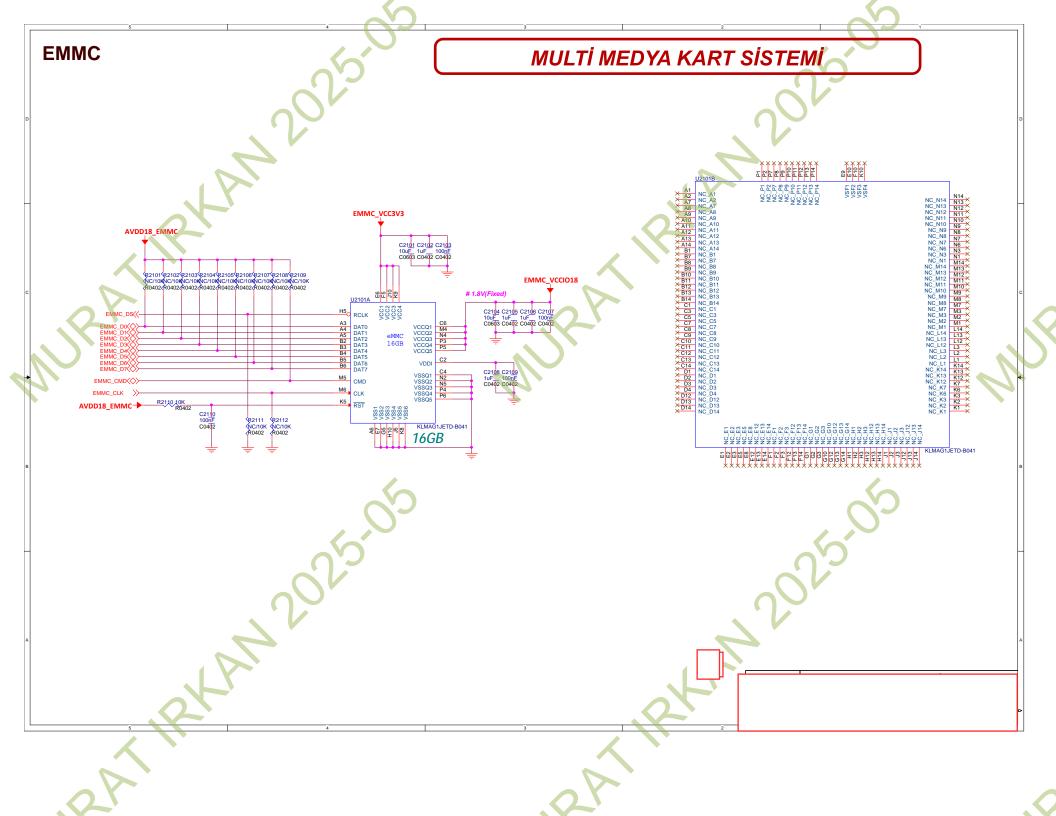


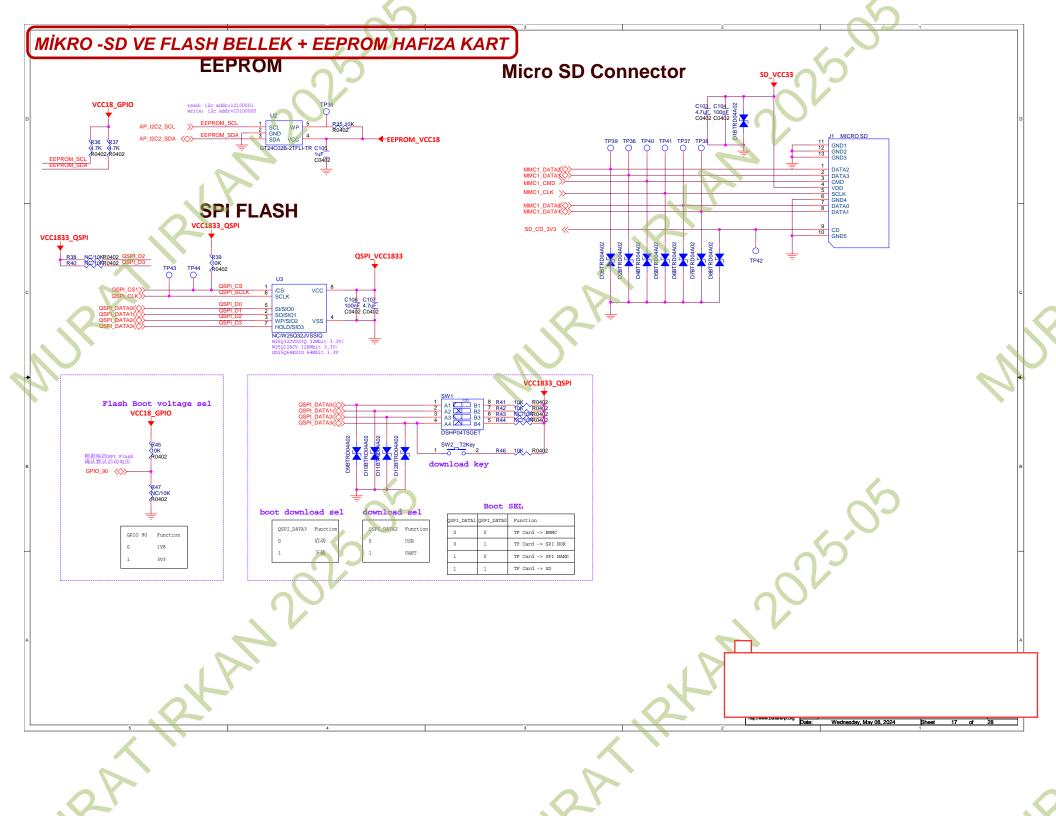


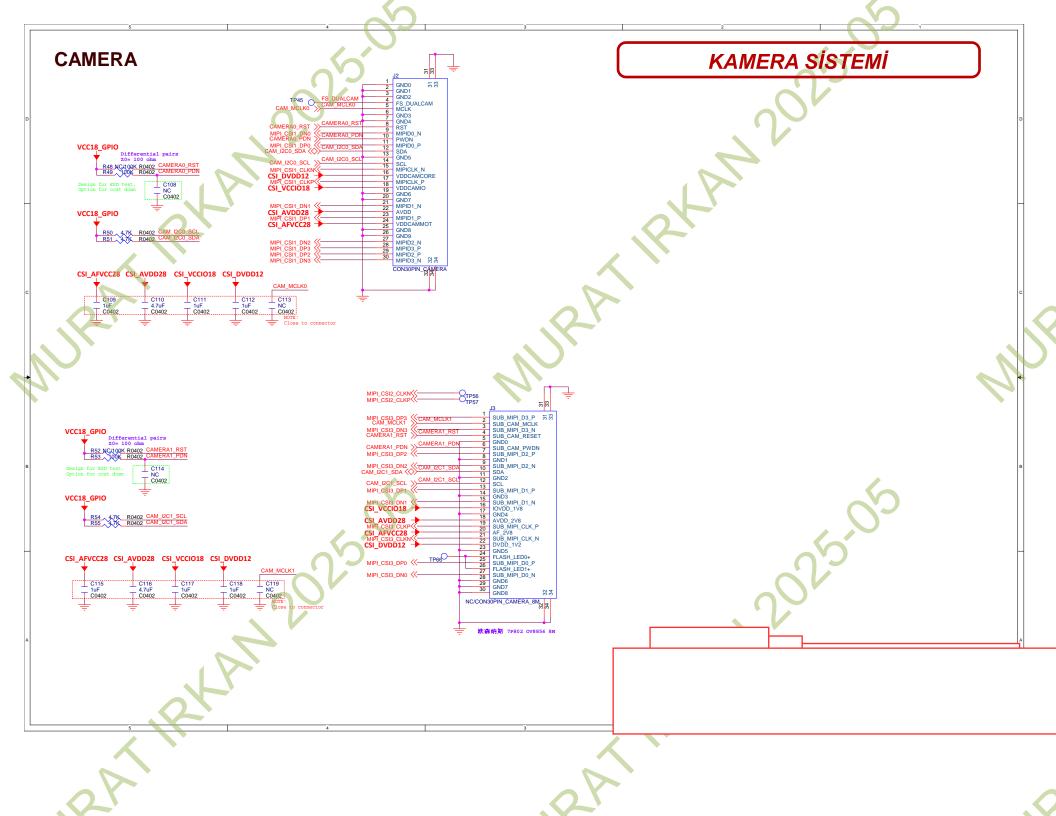


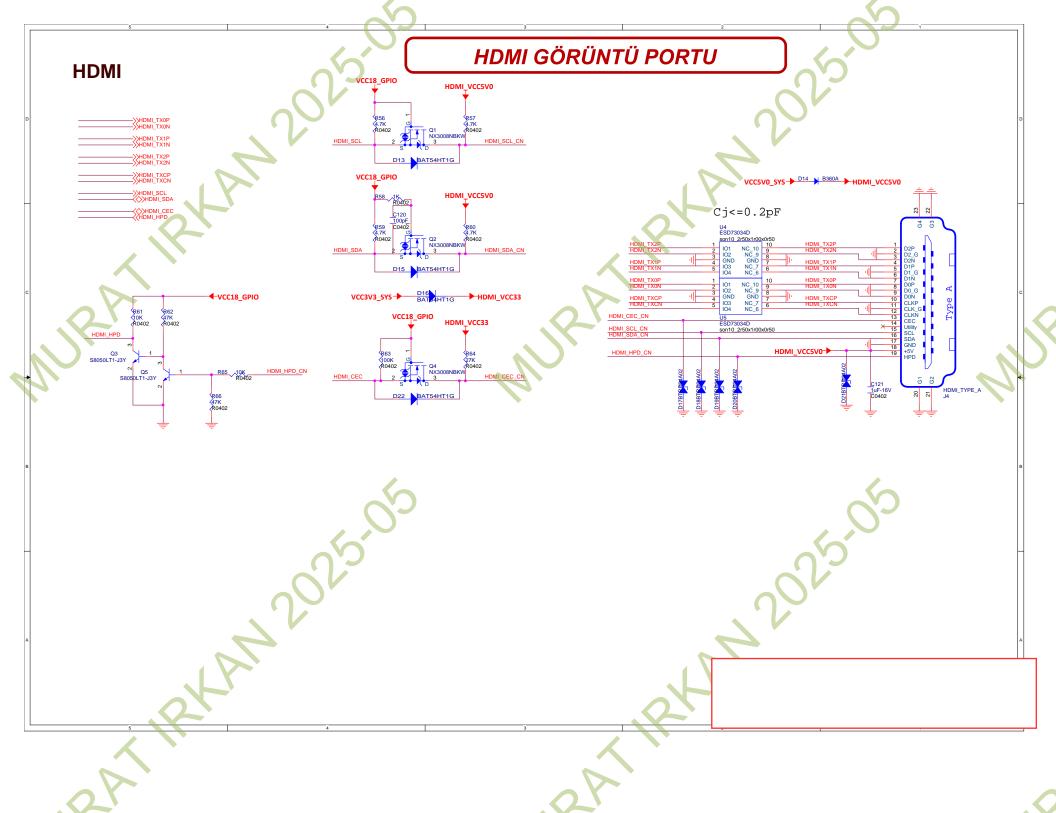


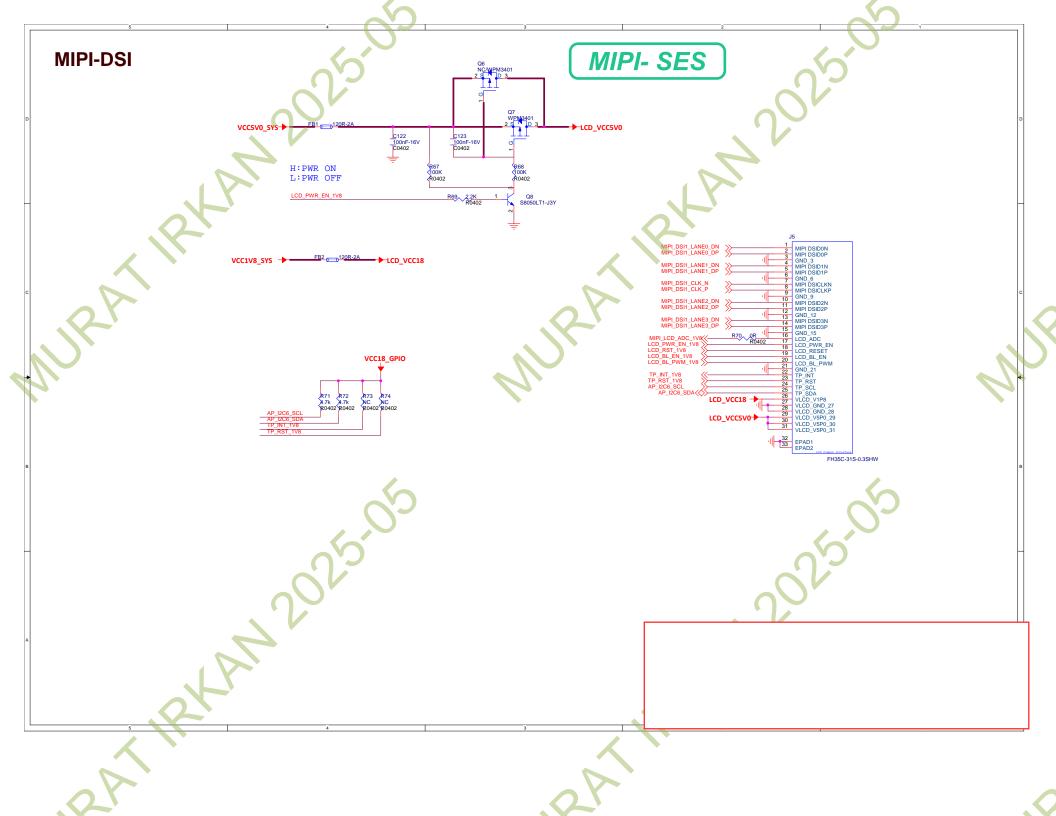


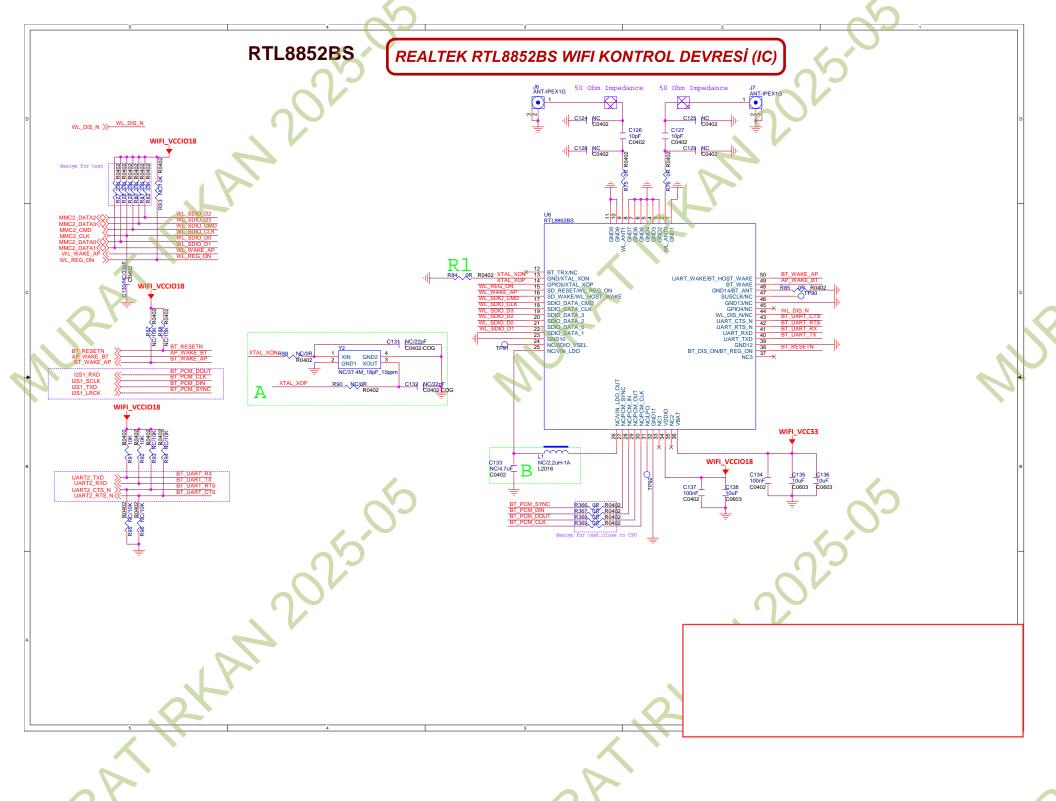


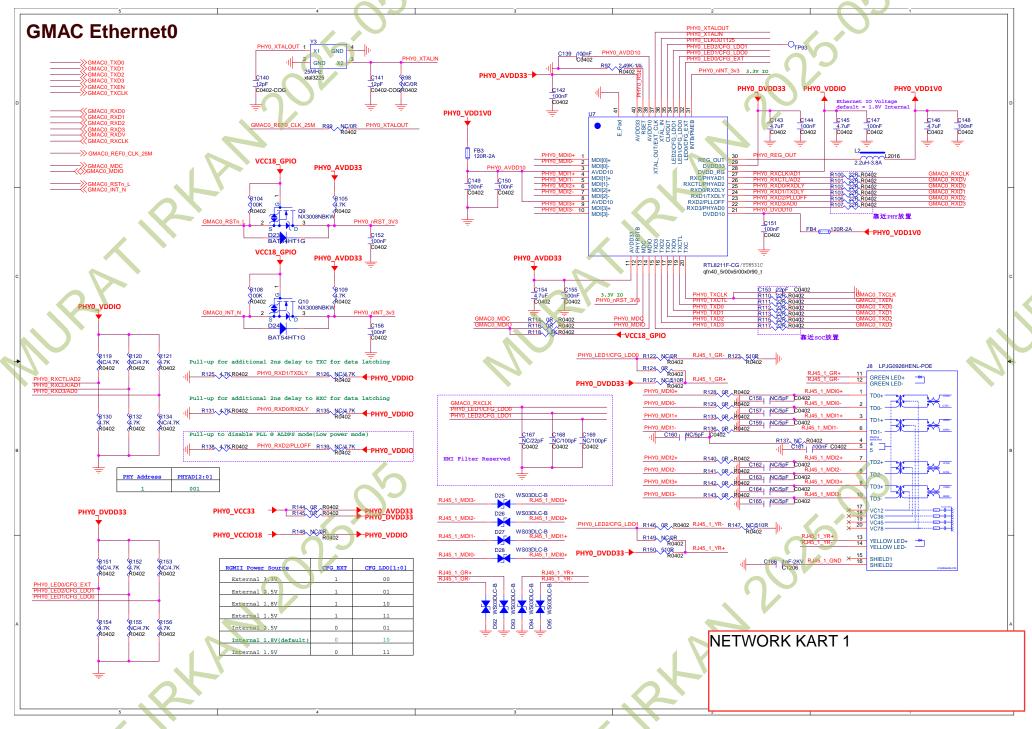




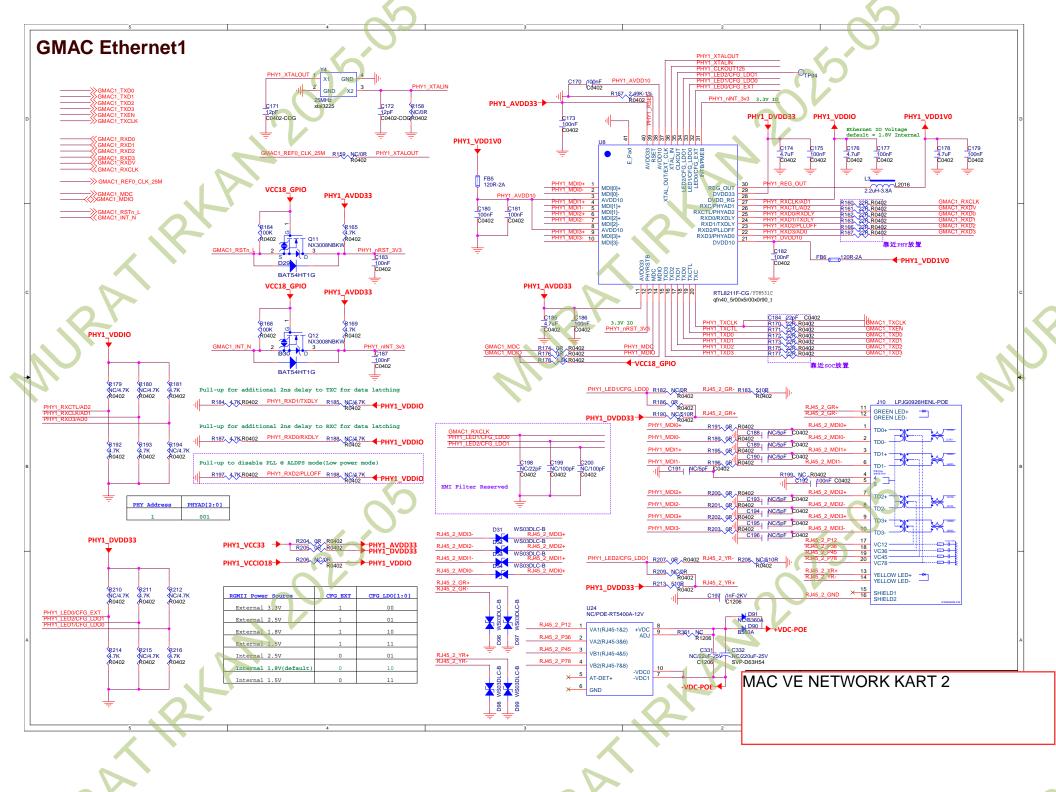


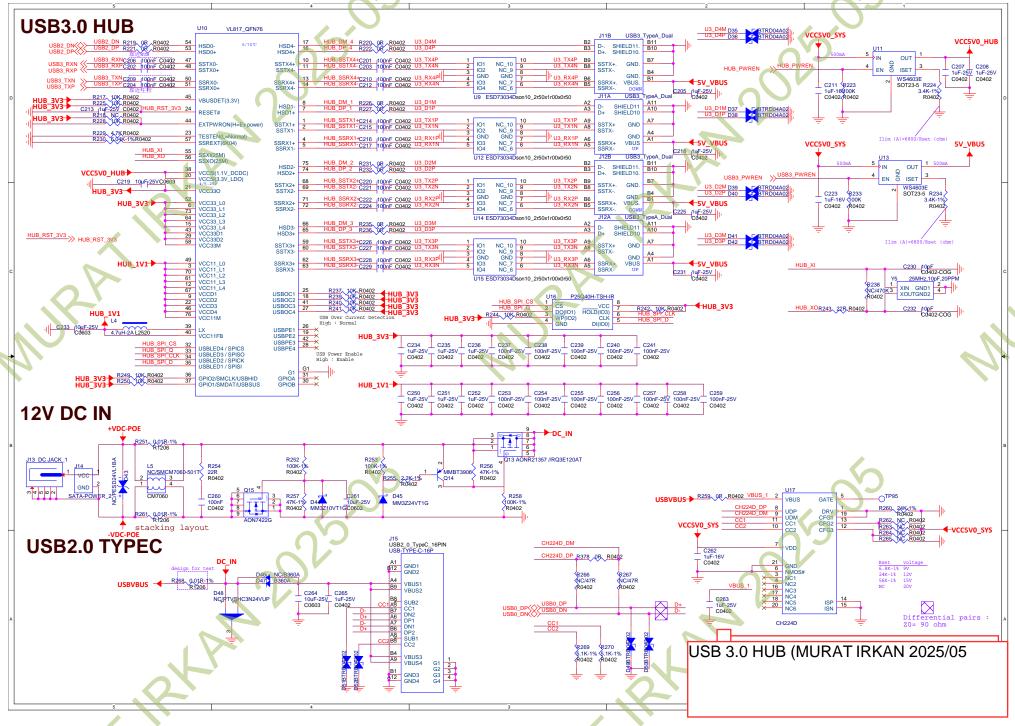




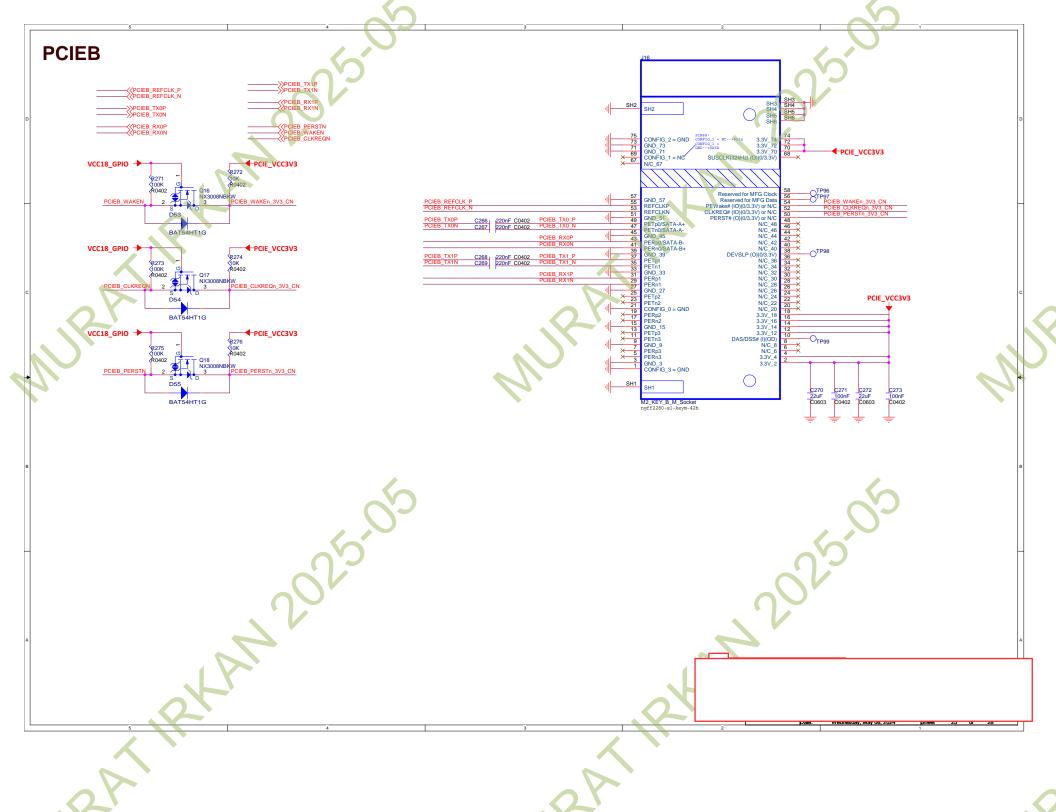


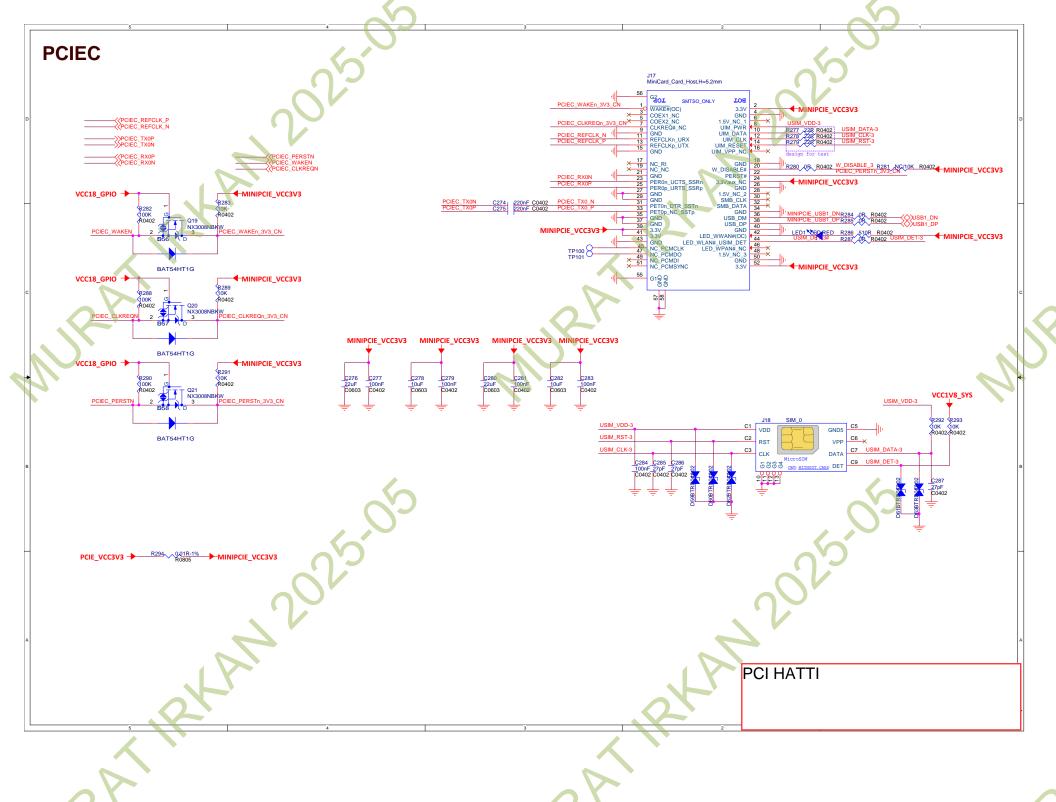
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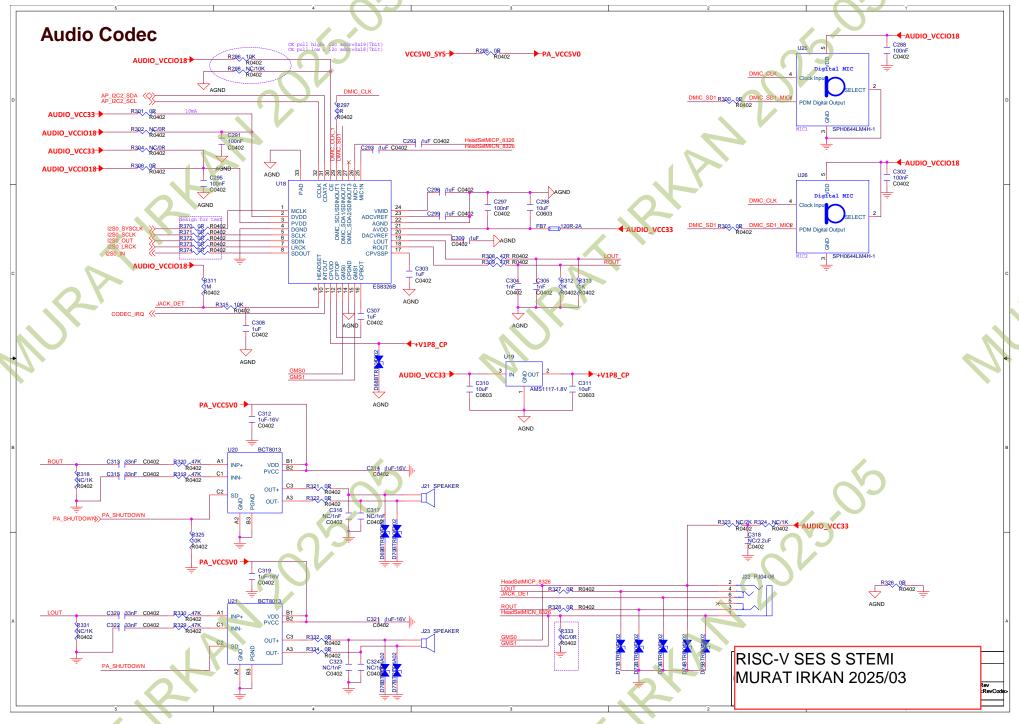




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