RK3308\_AI-VA\_MB\_RF-20180-

RAM: DDR3
ROM: eMMC/Nand/SPI Nor + TF card
Interface: ACODEC/I2S/PDM/SPDIF/LCDC/MAC

Rockchip 瑞芯微电子 Fuzhou Rockchip Electronics RK3308\_AI-VA\_MB\_REF 00.Cover Page Wednesday, July 04, 2018 1 of 51 Designed by:

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# Note

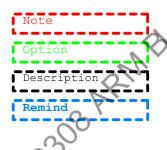
## NOTE 1:

### Component parameter description

- 1. DNP stands for component not mounted temporarily
- 2. If Value or option is DNP, which means the area is reserved without being mounted
- 3. If Flash is compatible, please notice when eMMC is used, the option is that @eMMC is mounted, @Nand is not mounted when Nand is used, the option is that @Nand is mounted, @eMMC is not mounted

### NOTE 2:

Please use our recommended components to avoid too many changes. For more informations about the second source, please refer to our AVL.



**Bill of Materials** 

**Header:** 

Item\tPart\tDescription\tPCB Footprint\tReference\tQuantitv\tOption

Combined property string:

{| ltem \t{Value}\t{Description}\t{PCB Footprint}\t{Reference}\t{Quantity}\t{Option}

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Project: RK3308\_AI-VA\_MB\_REF

File: 01.Index

Date: Wednesday, July 04, 2018 Rev: 1.1

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Revision History

Version	Date	Author	Change Note	Approved
V0.1	2018.03.07	Joseph.Wei	First edition	, Q.K.
V1.0	2018.05.11	Joseph.Wei	1 Update VDD_CORE&VDD_LOG Power supply circuits, they are seperated by different power supply 2 Update Analog MIC circurt,MIC1&MIC 2 as Loopback function, MIC3~MIC8 as MIC IN function. 3 Update VDD_LOG Power supply circuit,    Set VDD_LOG default voltage as 1.04V,modify R2208=110K%1 4 Update TF Card circuits. 5 Update RMII circuits. 6 Update WiFi circuits. 7 ADD LPDDR2&DDR2 design circuits. 8 Upadate Hardware ID circuits. Detailed change point please check published document:    《RK3308 AI-VA REF V10 20180511 SCH Modify Notes》.	NIRATI
V1.2 M	2018.07.03	Joseph.Wei	1 ADD PMIC RK816-3 design circuits. 2 FLASH Default selecet Nand Flash. 3 ADD RTL8723DS design circuits. 4 Modify the ADC of Recovery to ADC_KEY_IN1. 5 Modify divider resistance of WIFI_REG_ON . 6 Modify divider resistance of CLK_32K_OUT . 7 ADD PowerAmp XA8872 design circuits. 8 VDD_1V0 Power Supply from VDD_LOG. 9 PDM MIC:PDM_CLK Unified modify to PIN T16 of RK3308. 10 Modify Loopback circuit parameter.	

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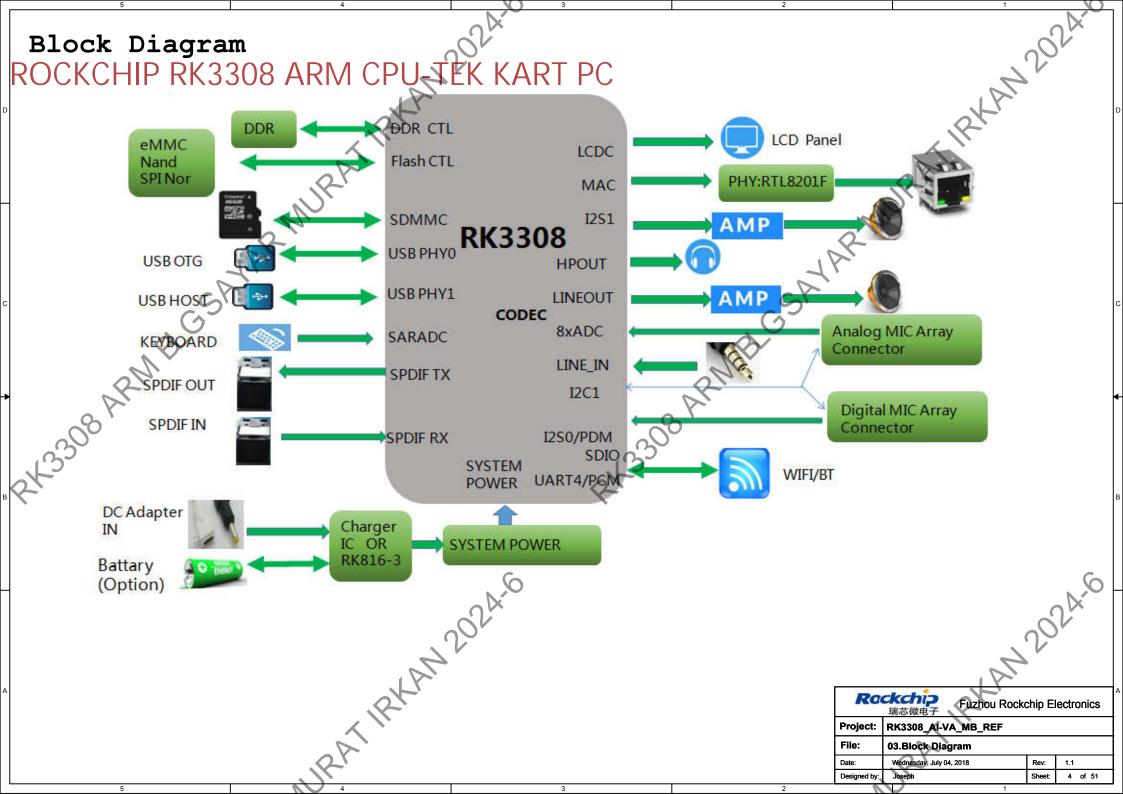
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D	5	1 RAT	RXA	1		. <b>P</b>	2	NR	ATIRXAN	2024.0
	Port	Pin name	Domain	Bus name	Pull-up voltage	Slave Device	Slave Addı (MS 7Bits)	Note	Slave Bus Capability	
С	12C0	GPIO1_D1/UART1_TX/12C0_SCL/SPI2_CSN0 GPIO1_D0/UART1_BX/12C0_SDA/SPI2_CLK	VCCIO1	I2C0_SCL I2C0_SDA	vcc_io	Reserve		SAA		C
<b>→</b>	1201	GA100_B4/I2C1_SCL GP100_B3/I2C1_SDA	VCCIO0	I2C1_SCL I2C1_SDA	VCC_IO	IS31FL3236A  ATSHA204A-SSHDA-T  XA8326  RK816-3	0x3C/0x3F 0x64 0x31/0x30 0x1A	MIC Array Connector-Anal	og	
000	S202	GPIO2_A3/UARTO_RTSN/SPIO_CSNO/I2C2_SCL GPIO2_A2/UARTO_CTSN/SPIO_CLK/I2C2_SDA	VCCIO2	I2C2_SCL I2C2_SDA	APIO2_VDD	Reserve				
B PYS	5		RXP	A202ª	3	RX23	2	File: 04.12C  Date: Wednesc  Designed by: Joseph	Fuzhou Rocko 8_AI-VA_MB_REF	Chip Electronics  Rev: 1.1 Sheet: 5 of 51

