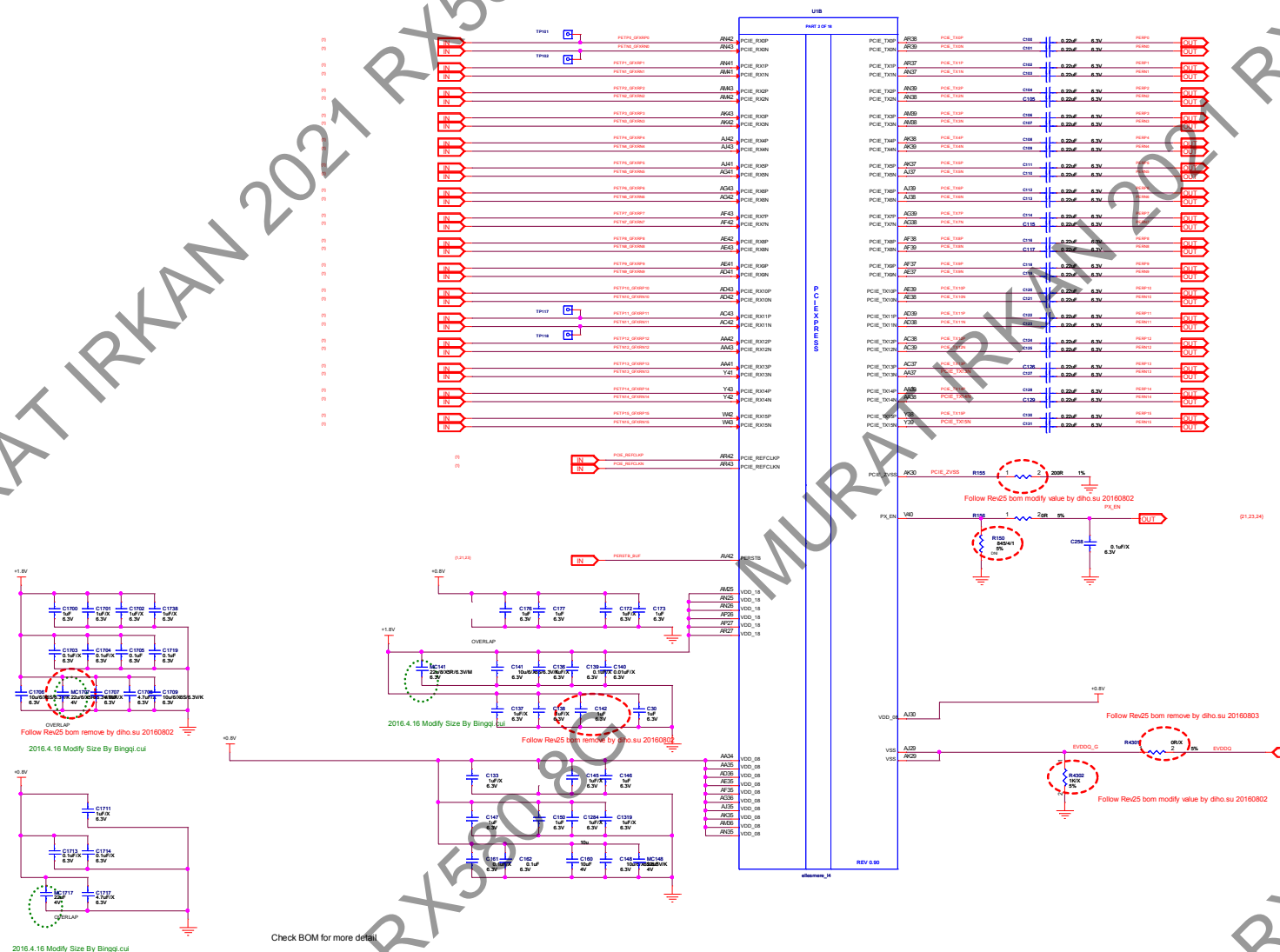


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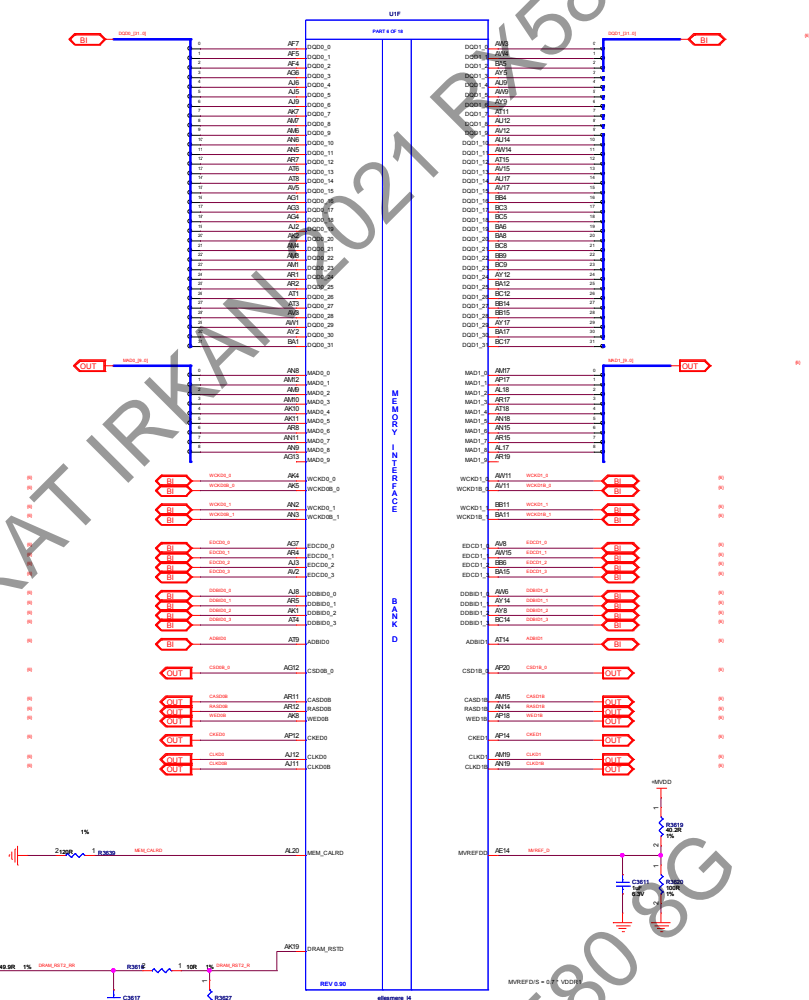
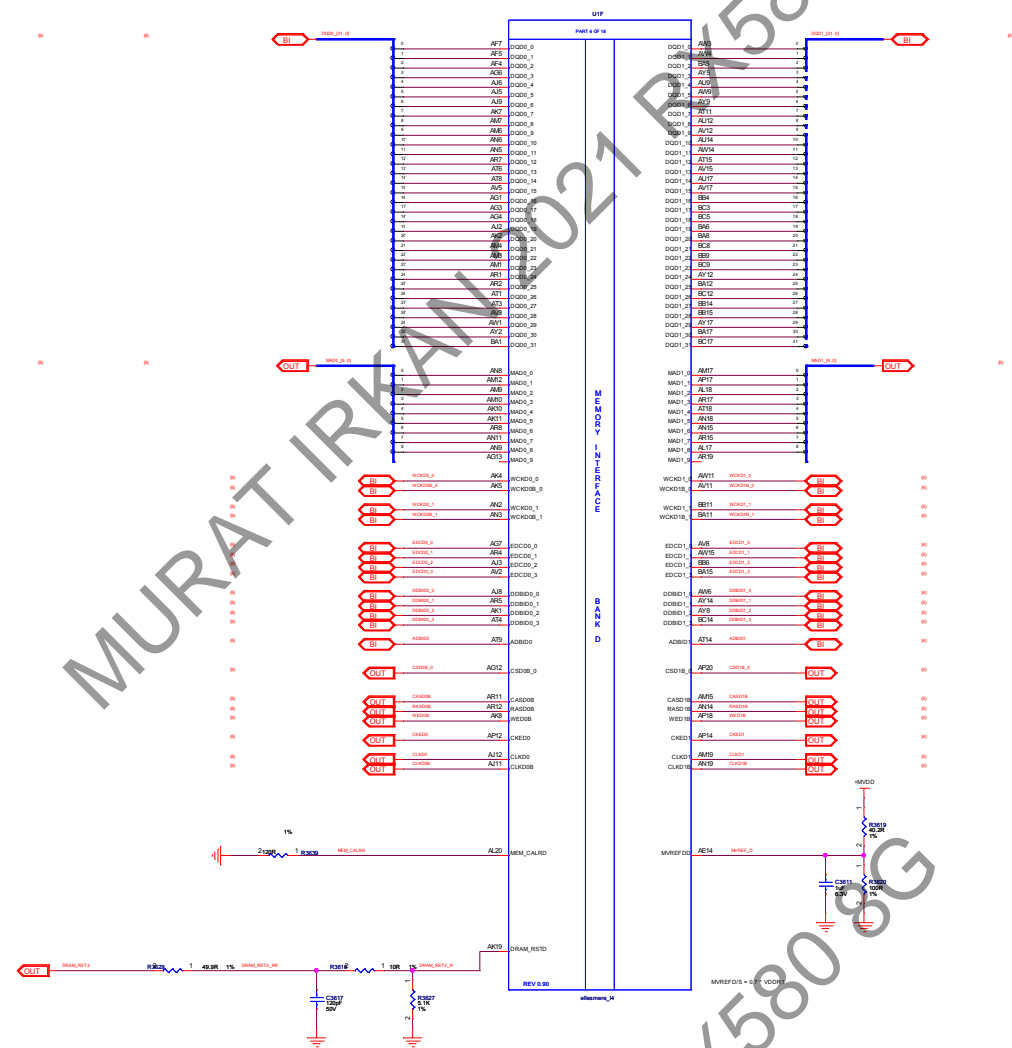


(2) ELLESMERE PCIE INTERFACE

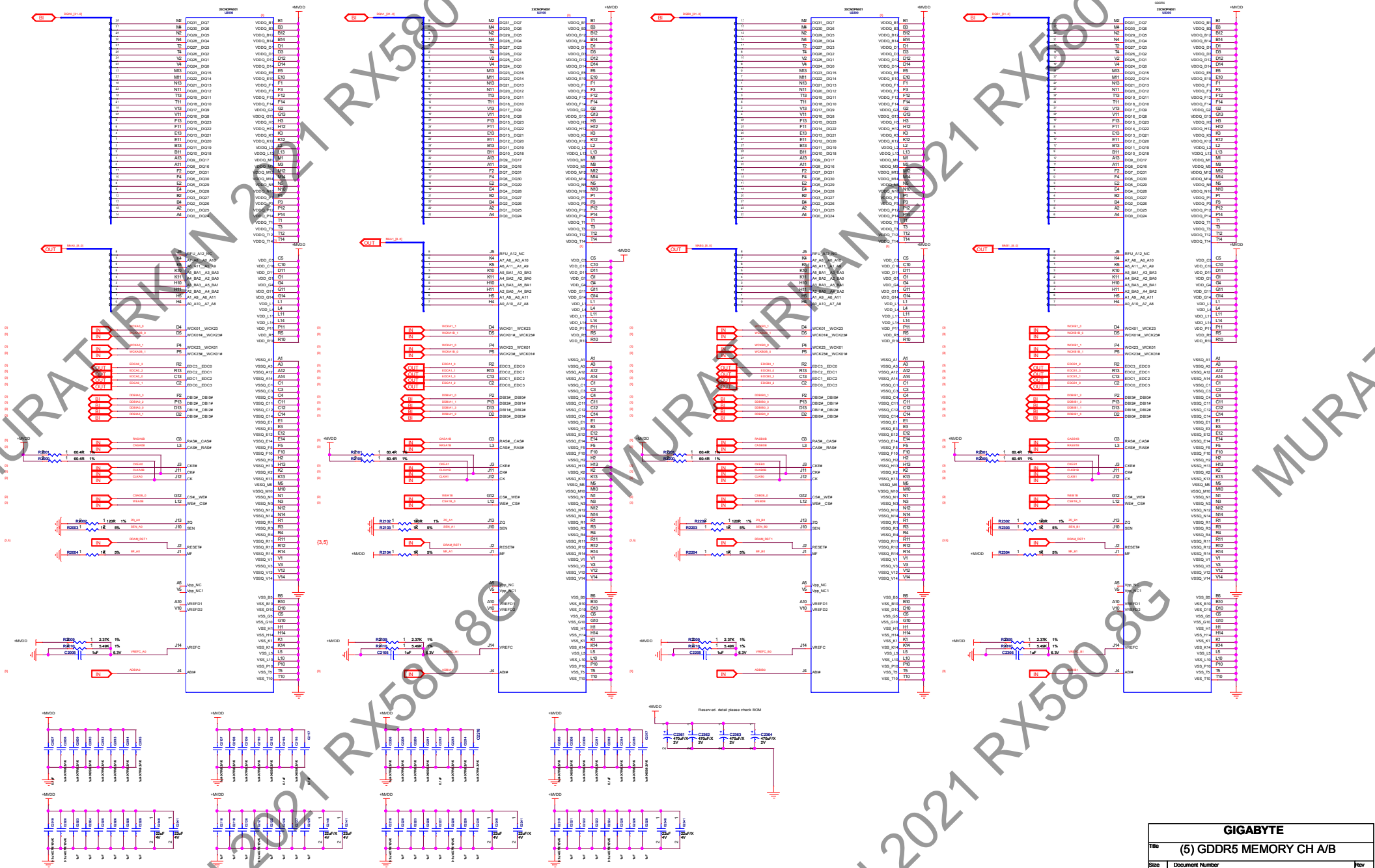


GIGABYTE			
Title ELLESMERE PCIE INTERFACE			
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(4) ELLESMERE MEM INTERFACE CH C/D



(5) GDDR5 MEMORY CH A/B

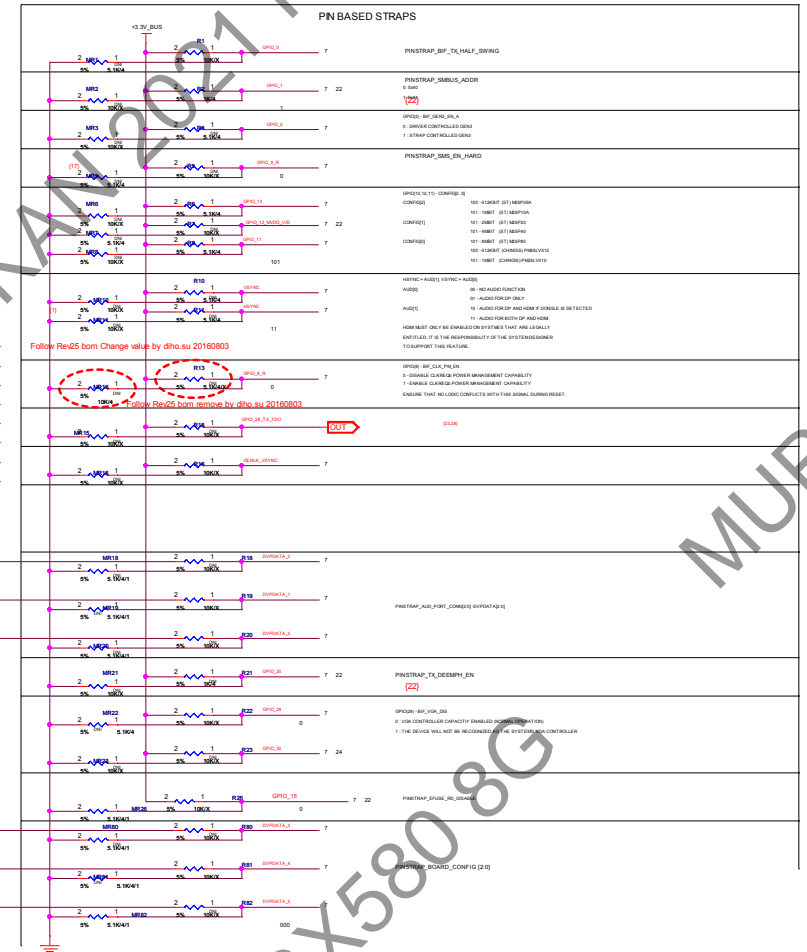
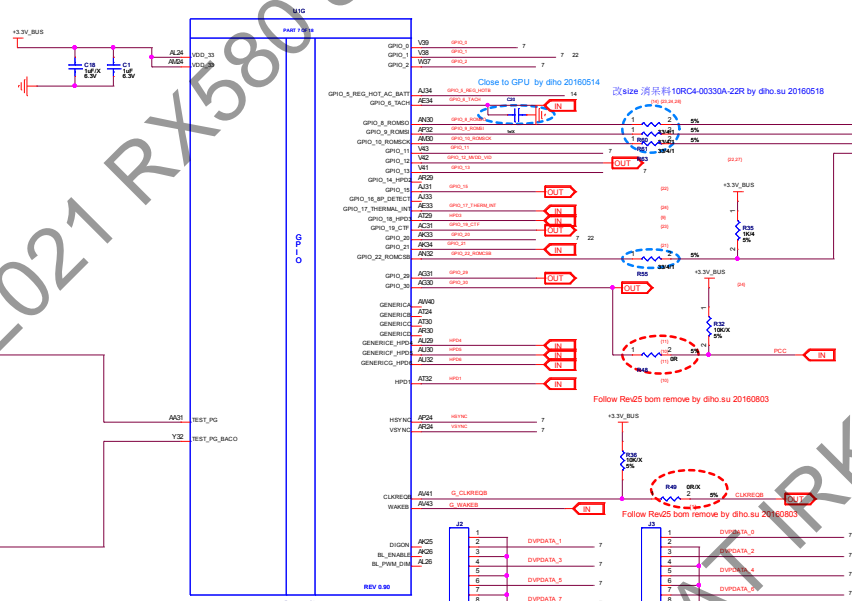
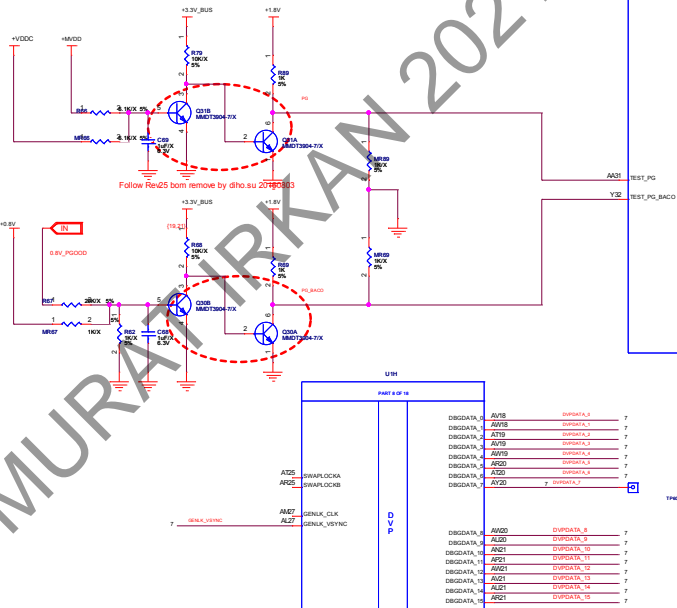


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Title (5) GDDR5 MEMORY CH A/B			
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(7) ELLESMERE GPIO STRAP CF XTAL

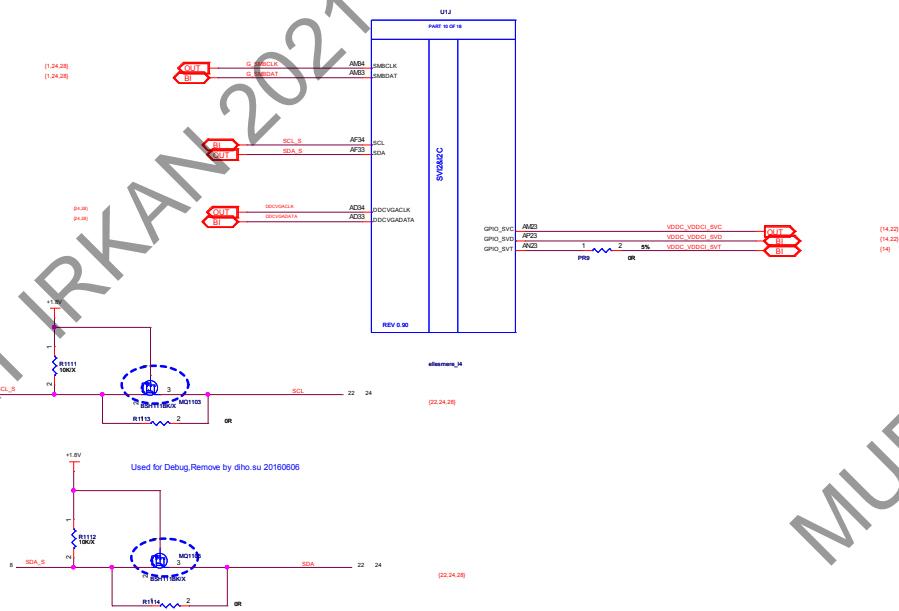
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I2C ADDRESS	FUNCTION	DEVICE
0x98	EXT TEMP SENSOR	LM60083

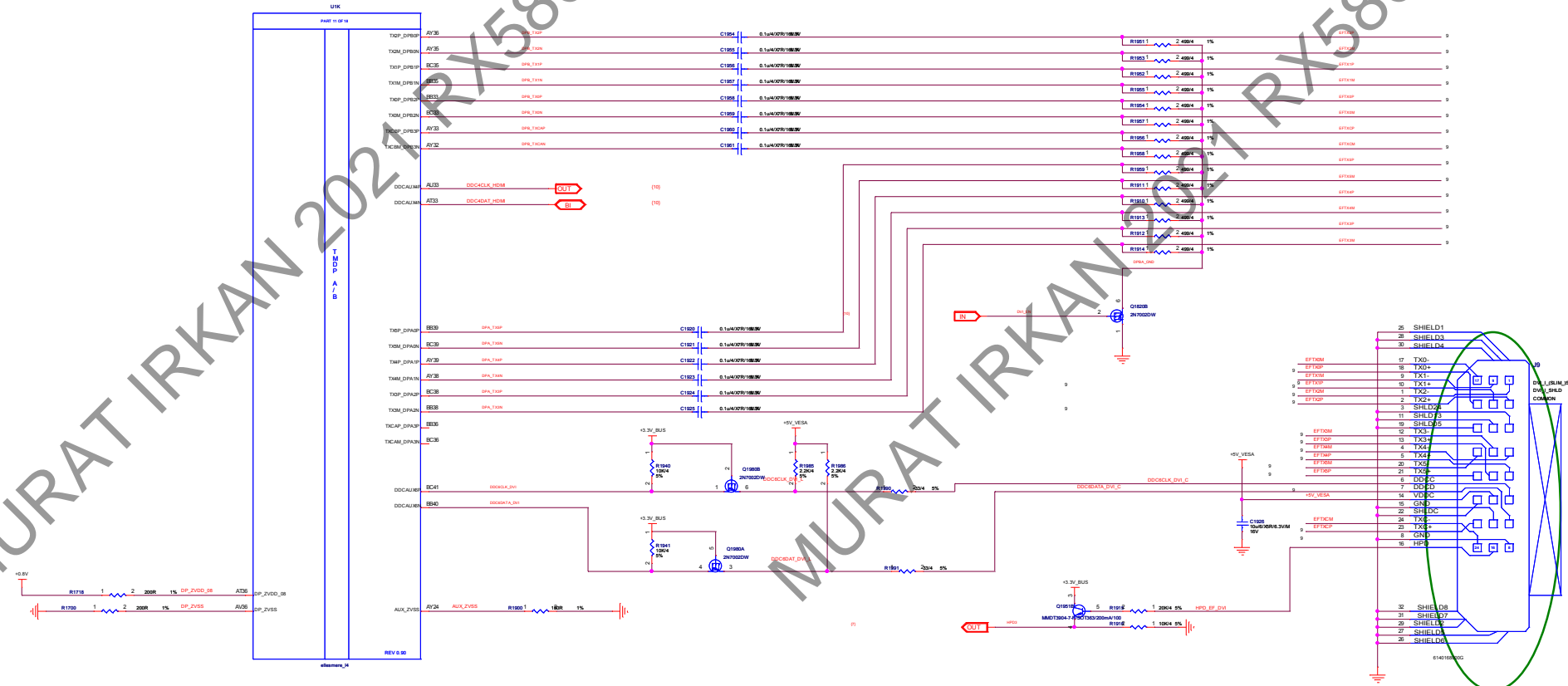


GIGABYTE			
Title: ELLESMERE GPIO STRAP CF XTAL			
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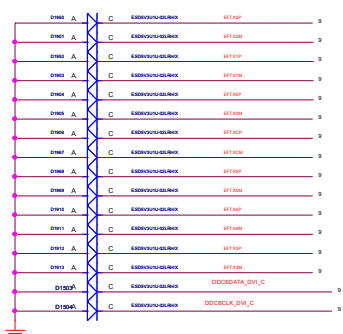
(8) ELLESMERE DAC1 LOCK



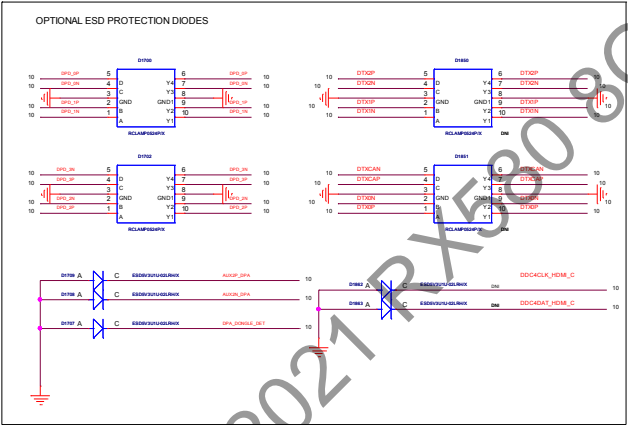
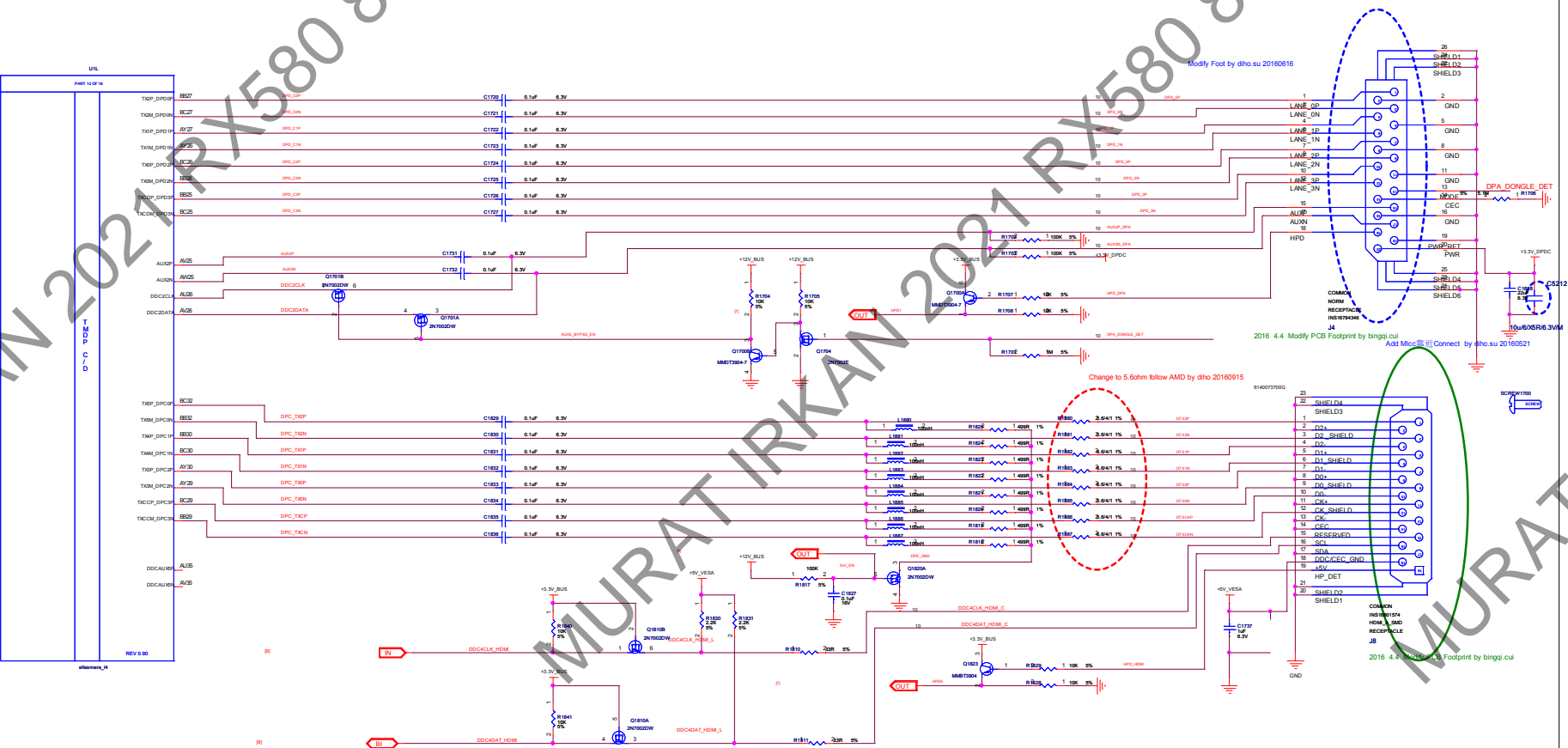
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Title ELLESMERE DAC1 LOCK			
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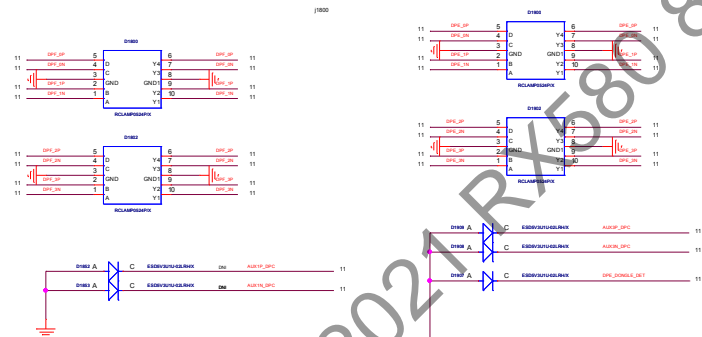
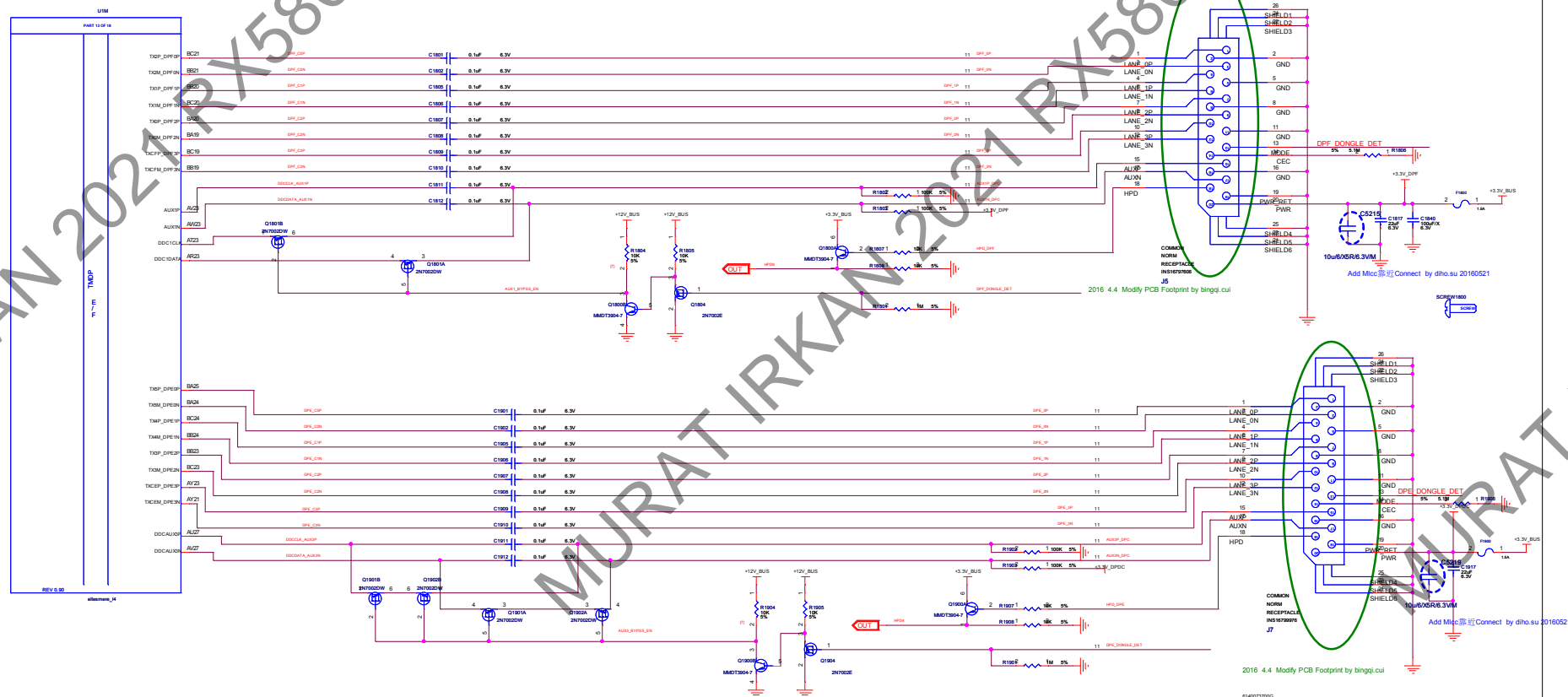
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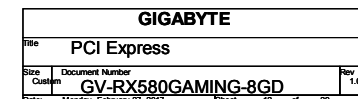
GIGABYTE			
ELLESMERE TMDP A/B			
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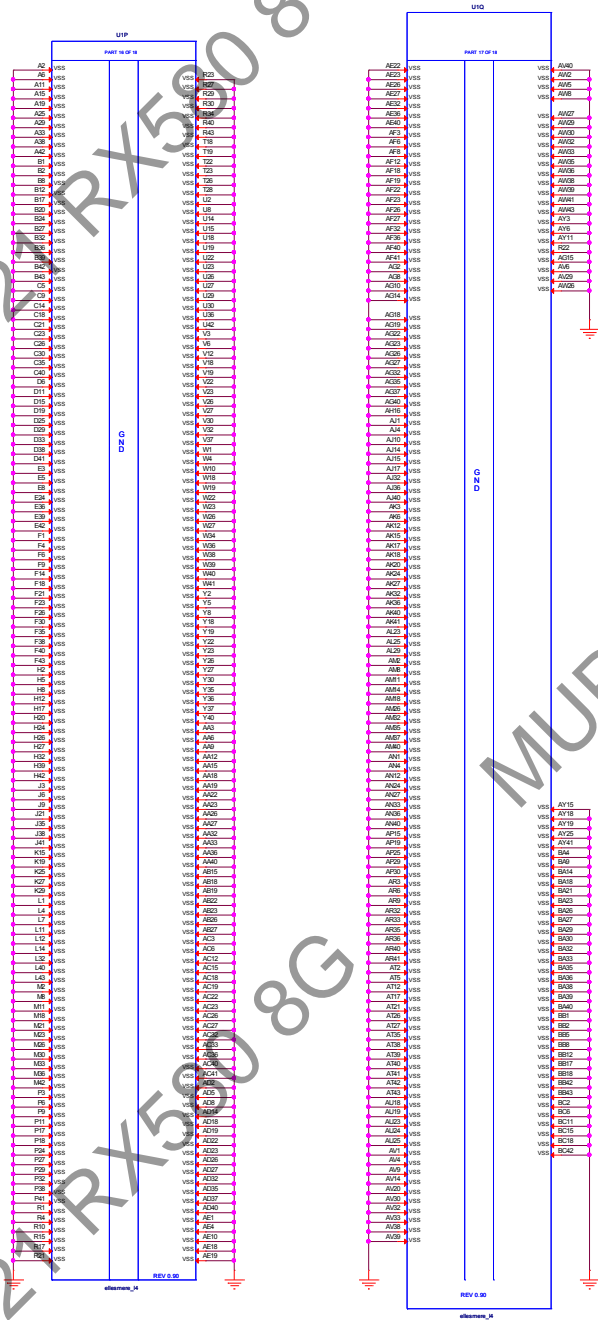


GIGABYTE			
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GIGABYTE			
Title ELLESMERE LVTMDP E/F			
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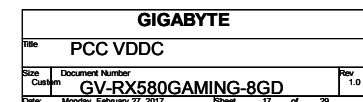




GIGABYTE			
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Size	Document Number	Rev	
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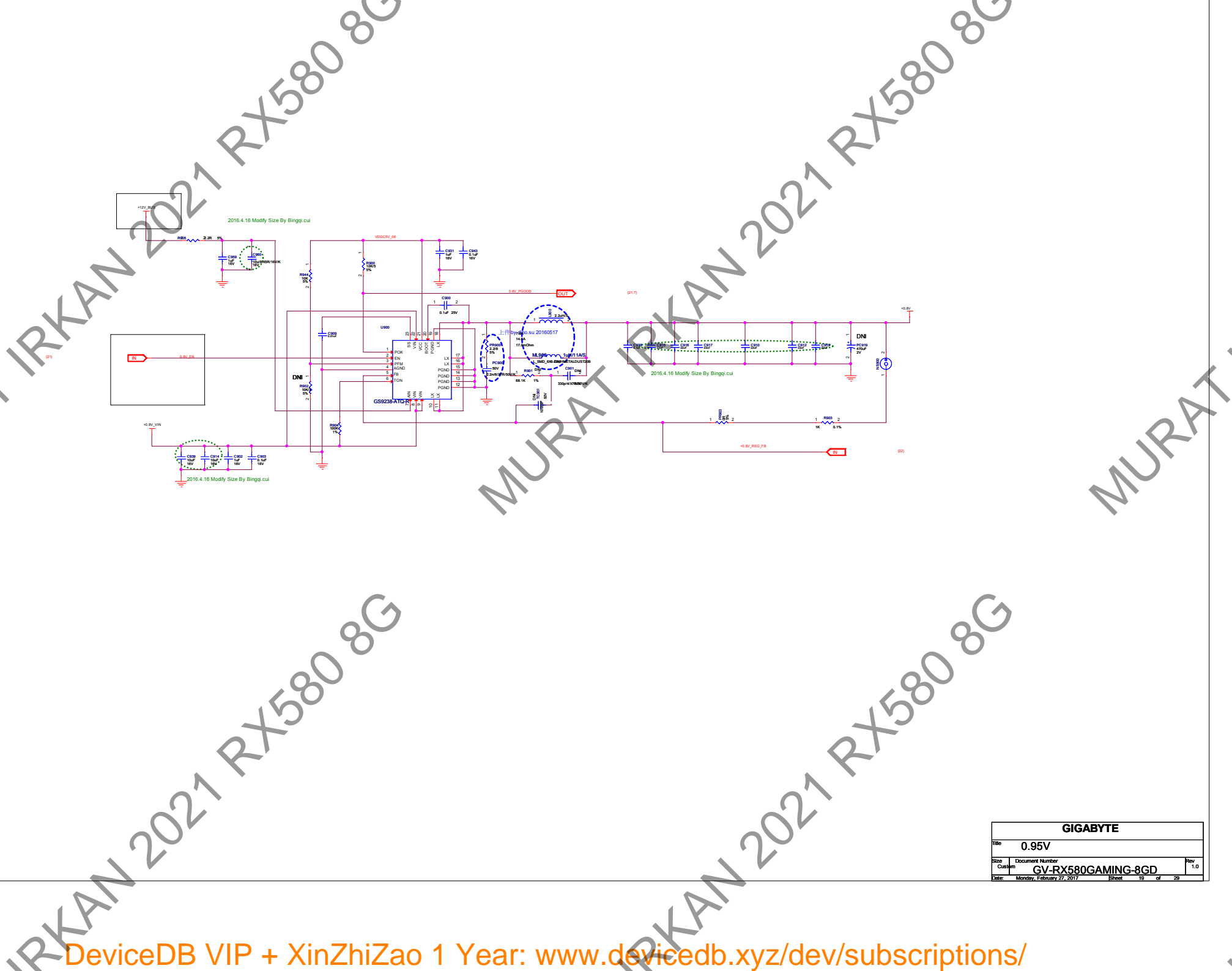




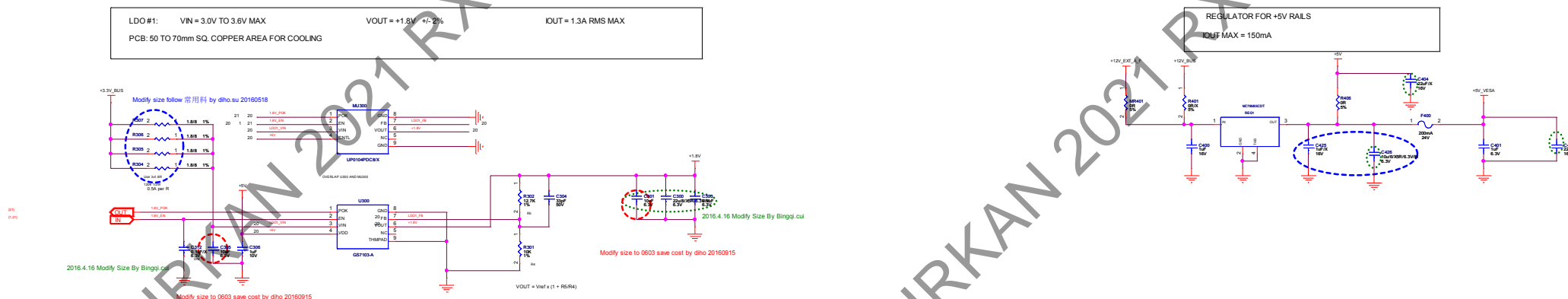




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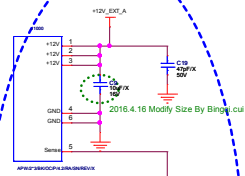


(18) SMALL RAIL REGULATORS

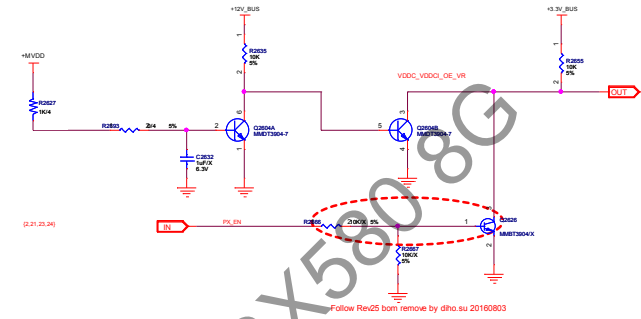
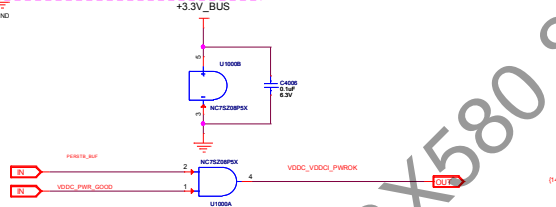
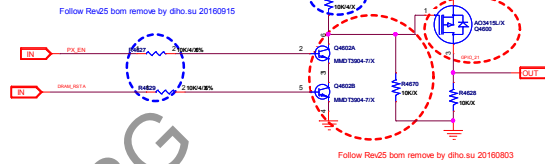
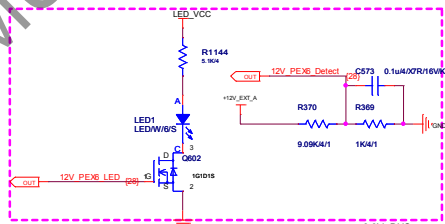
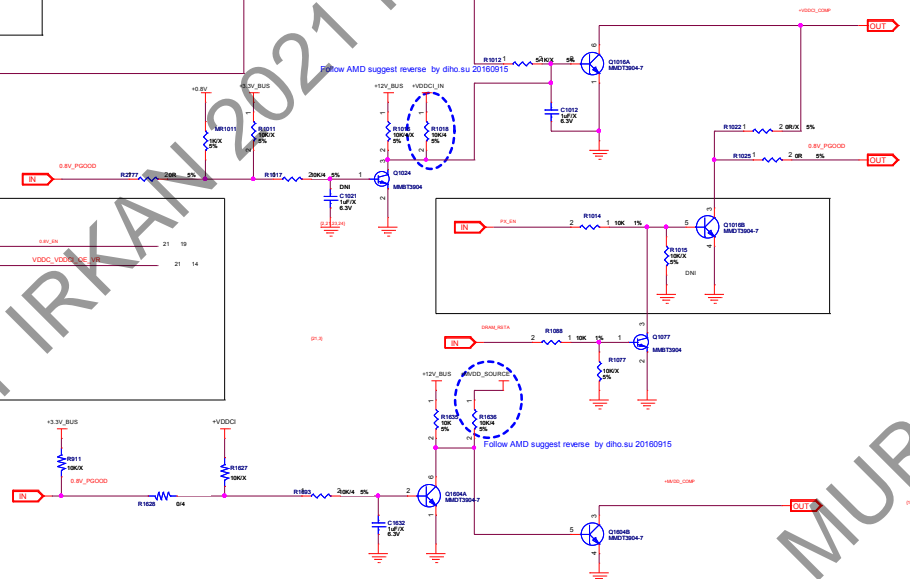
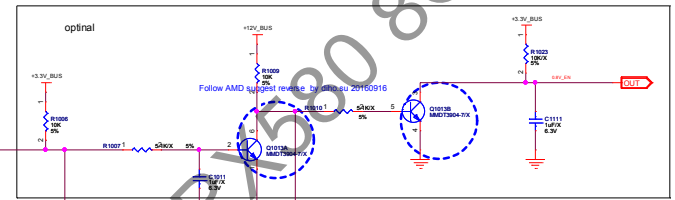
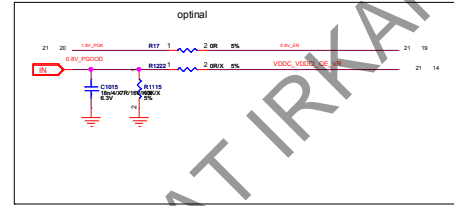
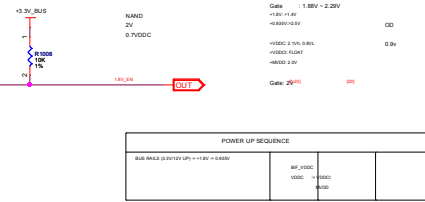
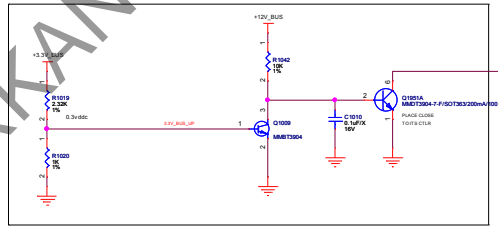
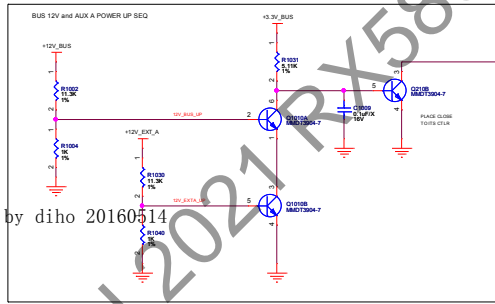


GIGABYTE			
SMALL RAIL REGULATORS			
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(19) POWER MANAGEMENT



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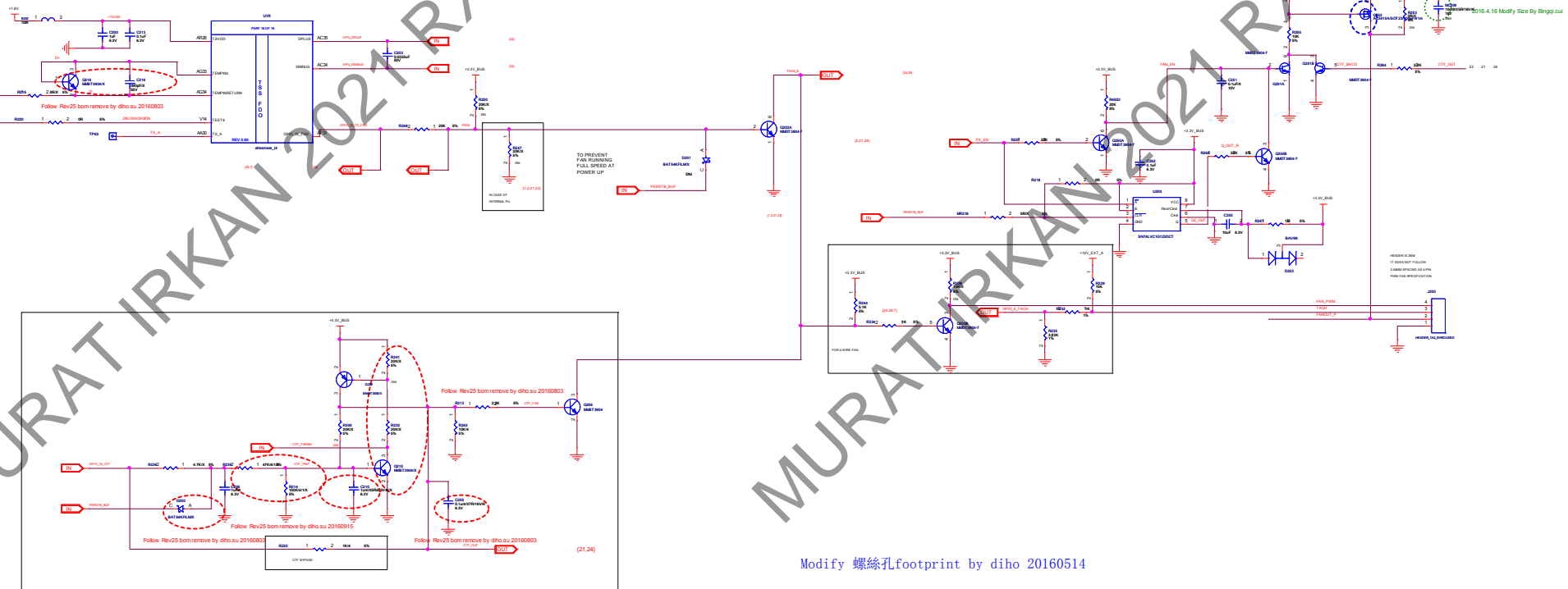


GIGABYTE			
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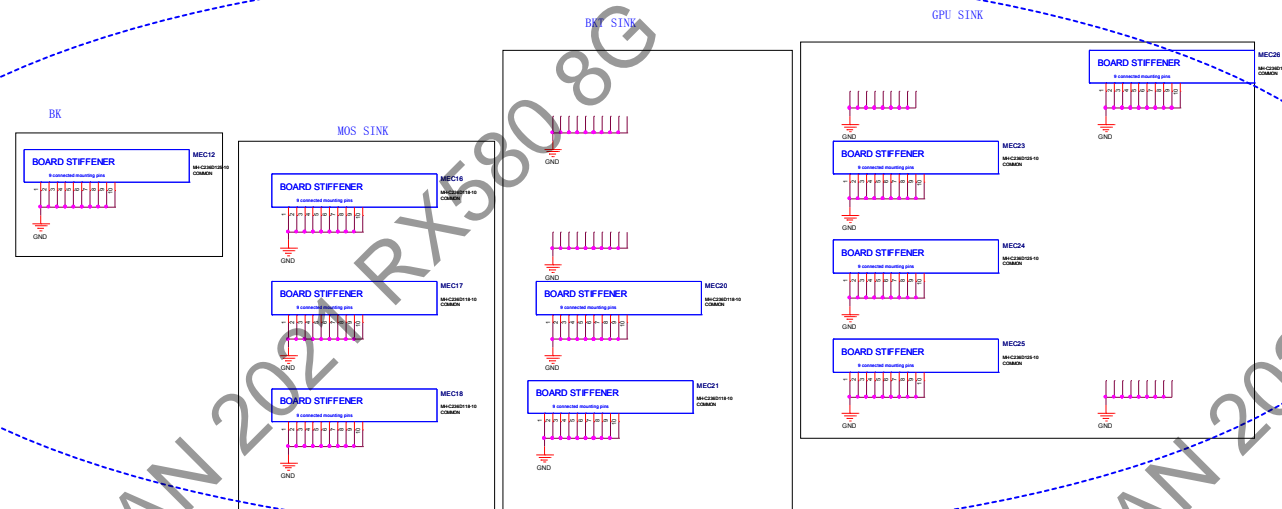
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(14,17,24)

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(20) MECHANICAL AND THERMAL MANAGEMENT

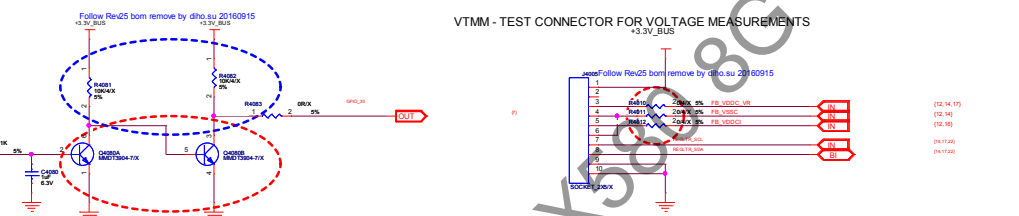
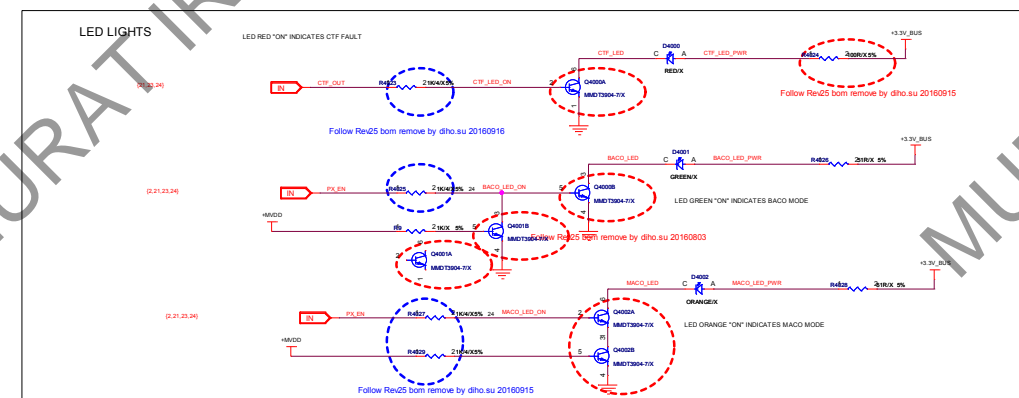
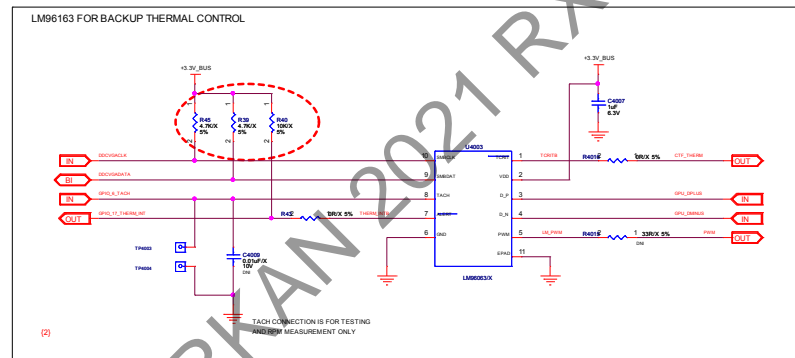
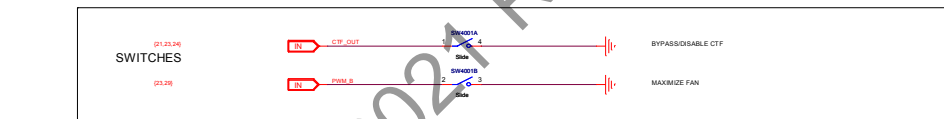
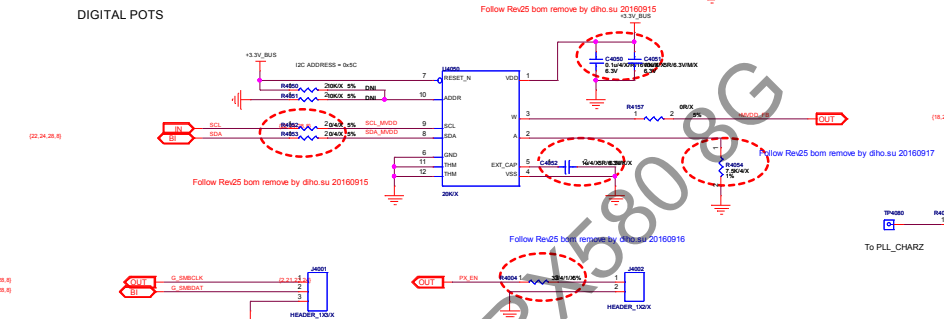
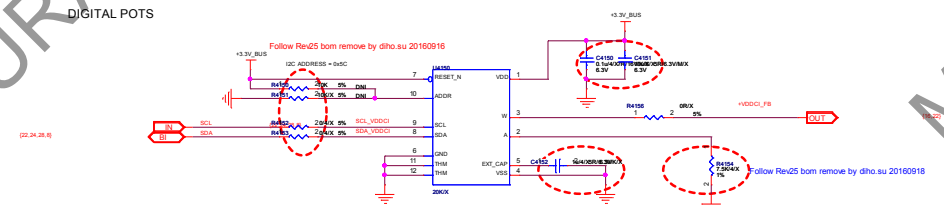
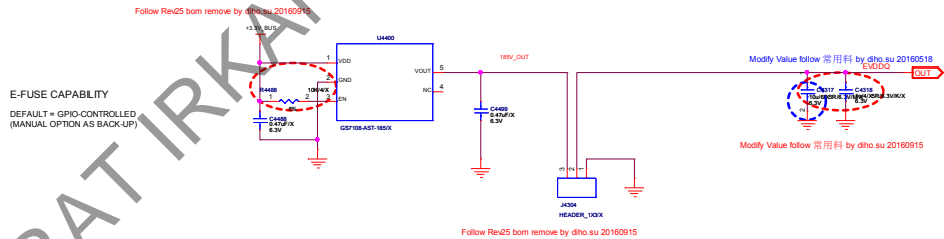
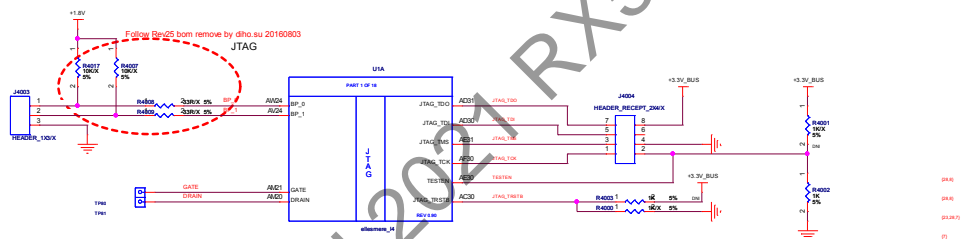


Modify 螺絲孔footprint by diho 20160514



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(21) DEBUG CIRCUITS

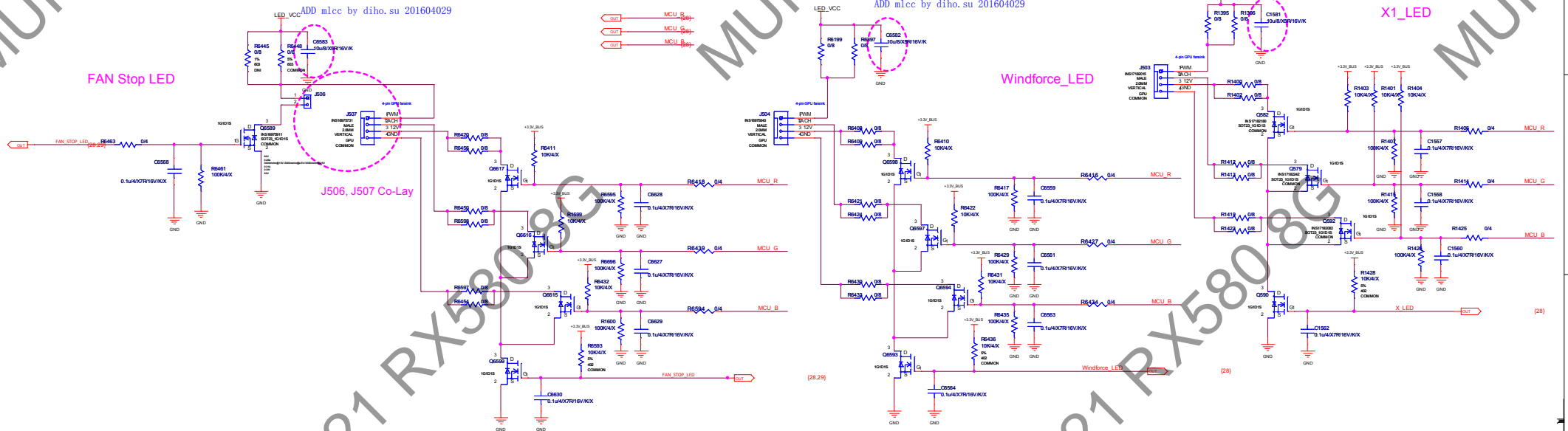
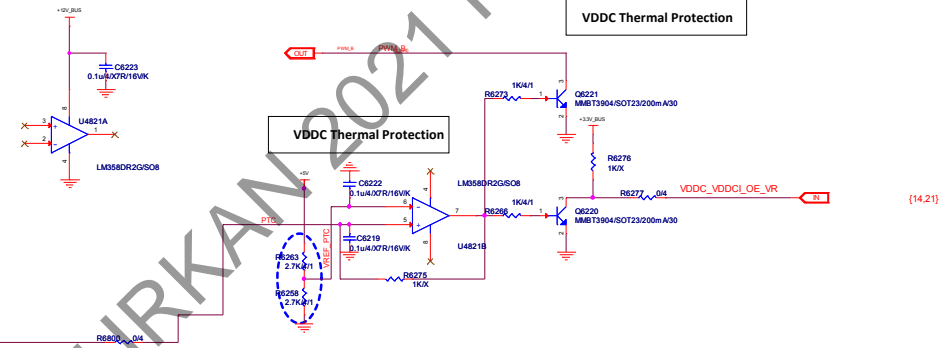
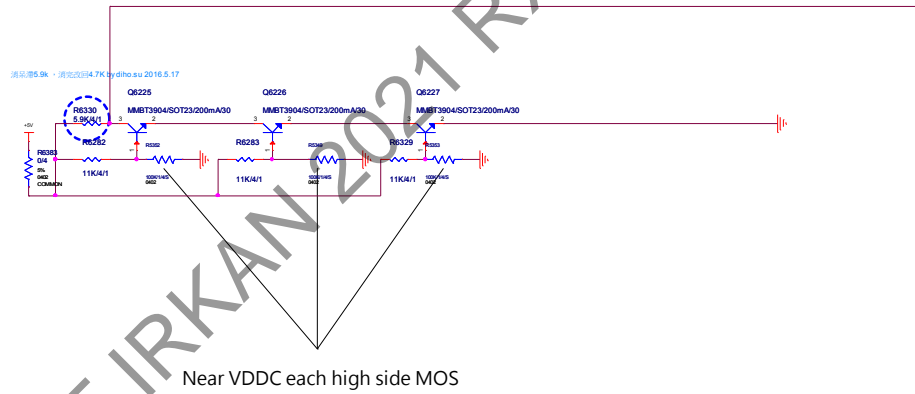


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#	Rev	Description
1	001	1. Add OP07 to operate 1500V 2. update 1500V drive response
2	002	PCB: - add series resistor R1500 - R1507 - add pull-down inductor L1500 - L1507 - remove C405, C403, VR405, C410, C414, R405
3	003	Modify 100V Design

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Add MOS VRHOT CIRCUIT



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