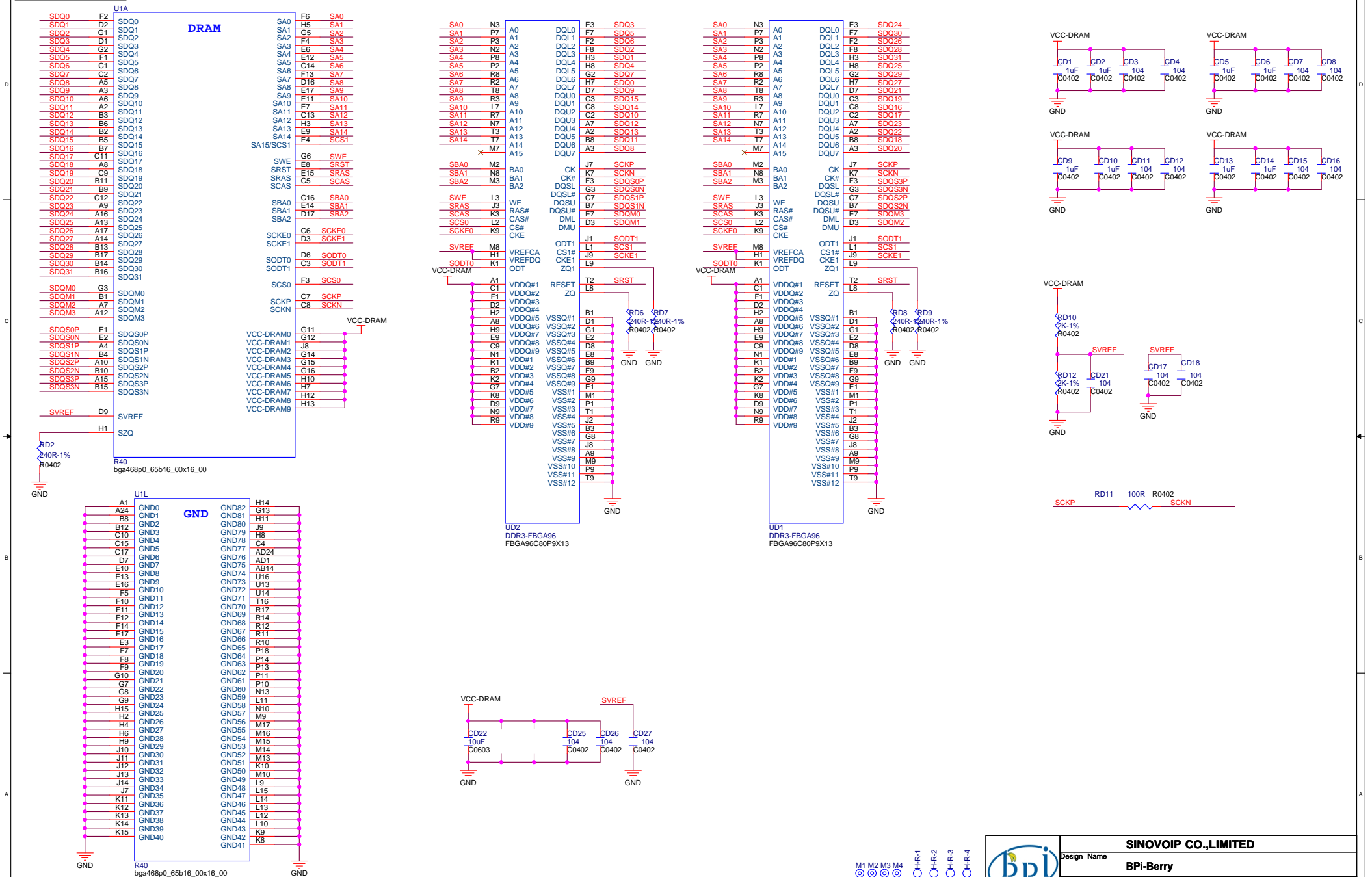
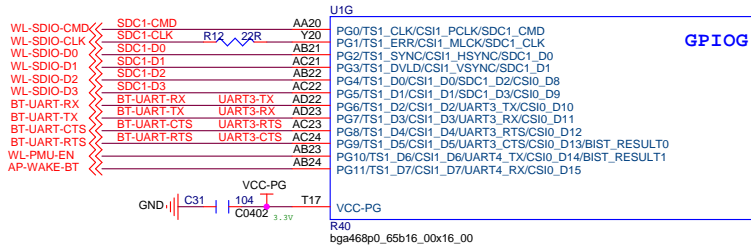
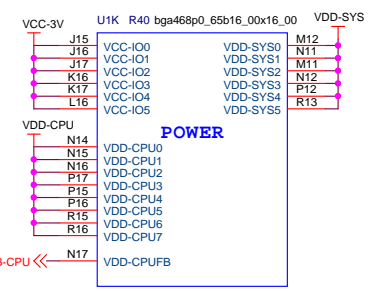
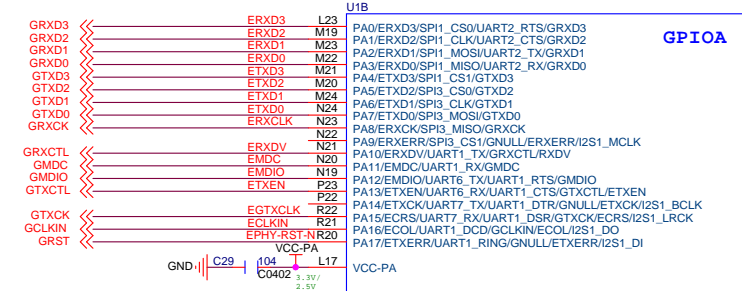
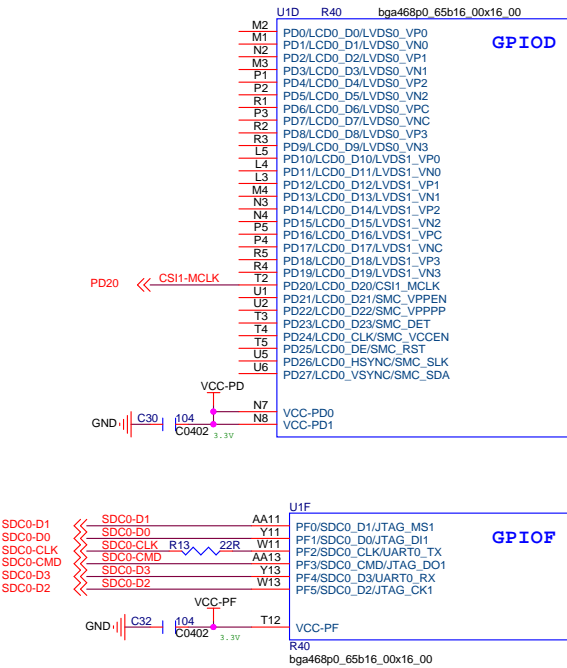
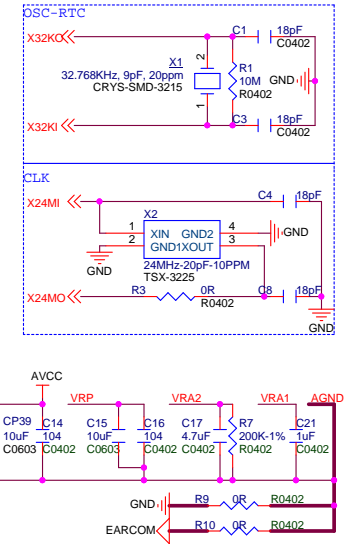
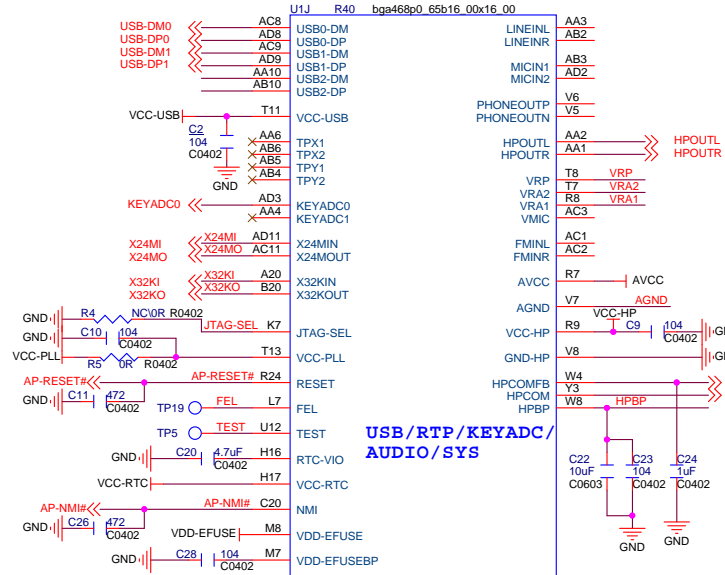
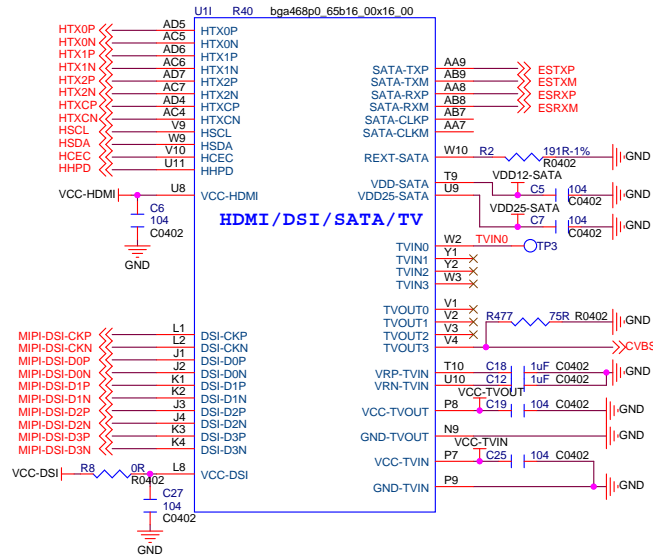


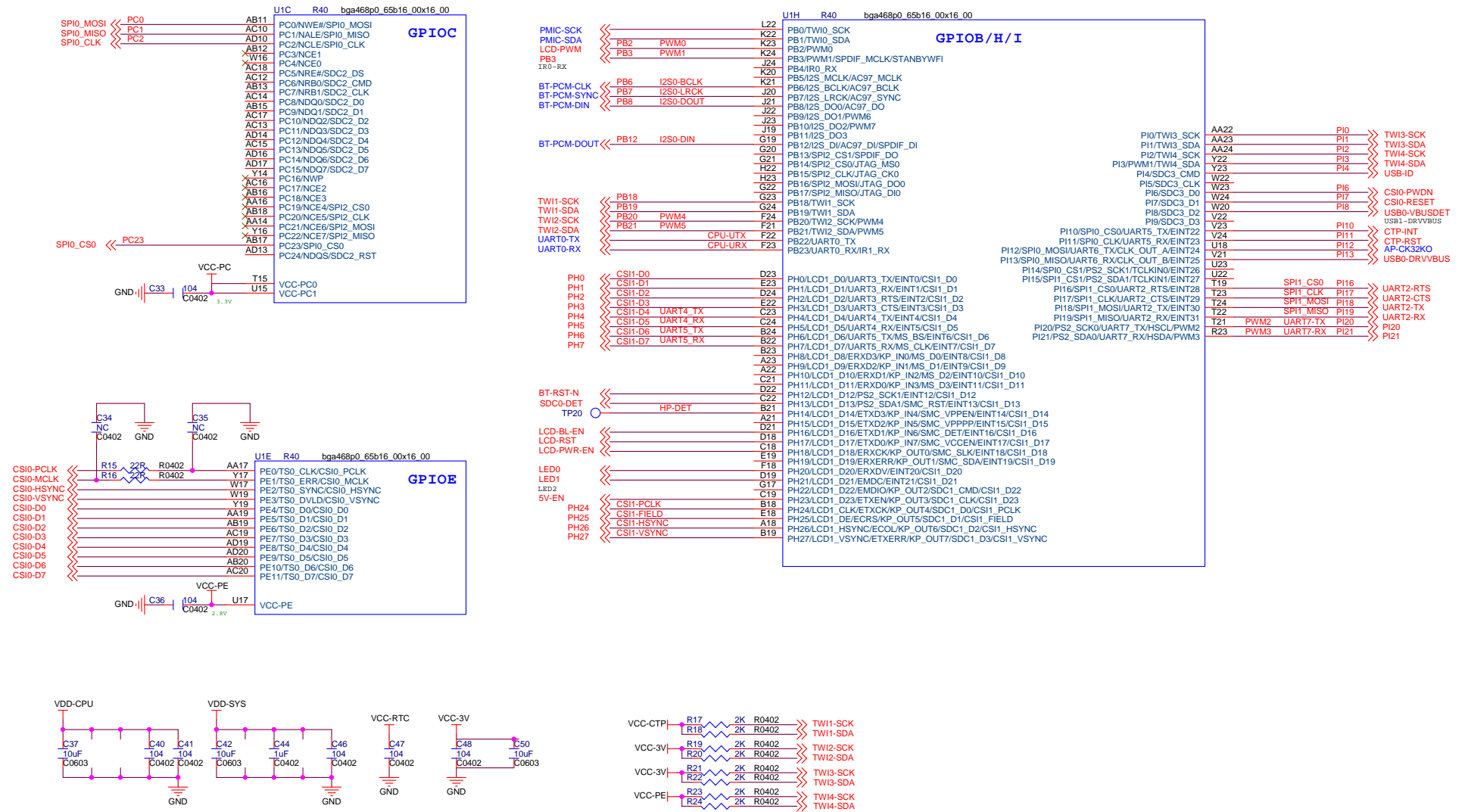
DDR3_16x2



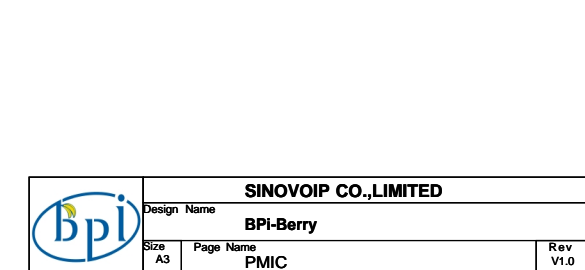
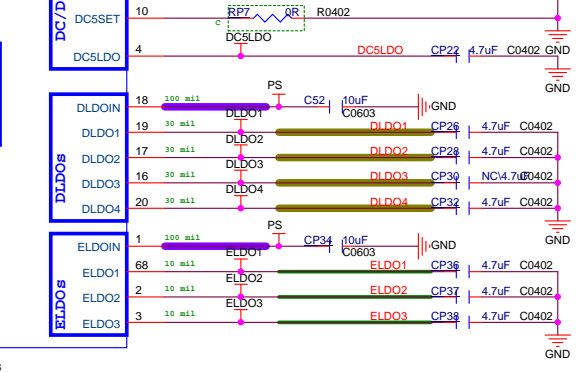
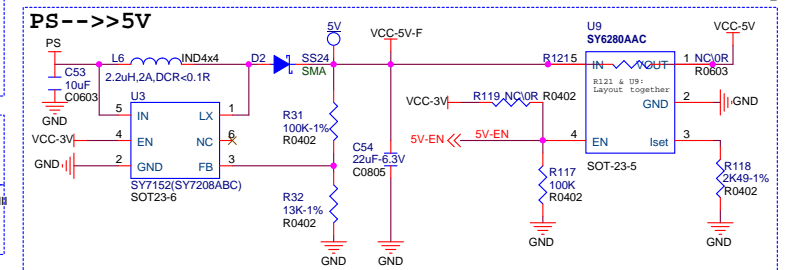
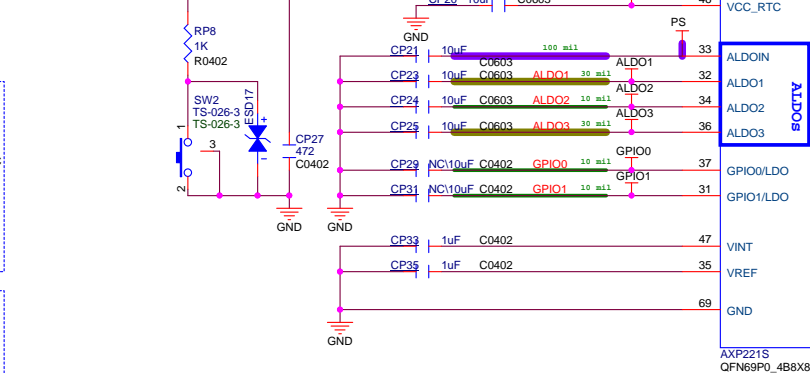
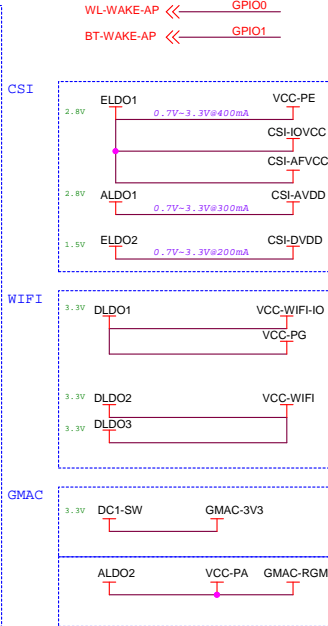
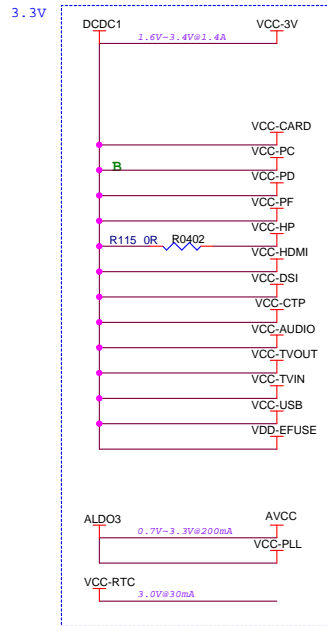
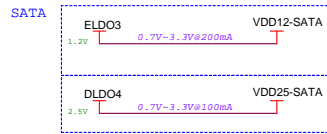
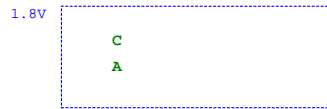
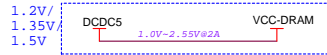
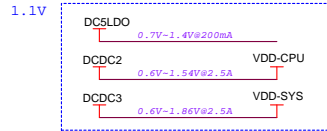
CPU1




CPU2

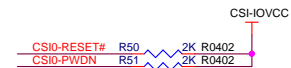
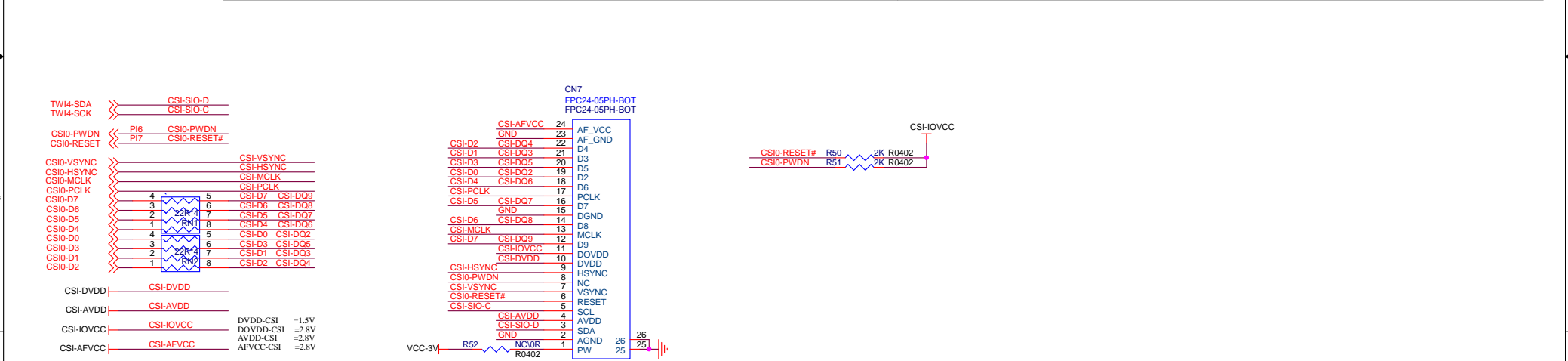


PMIC

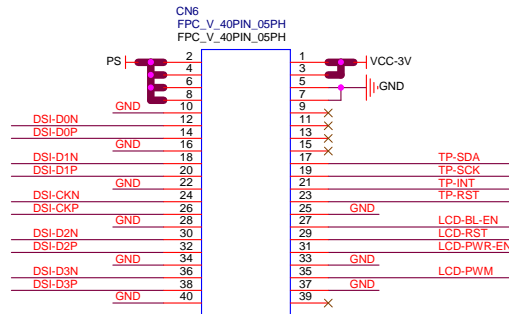
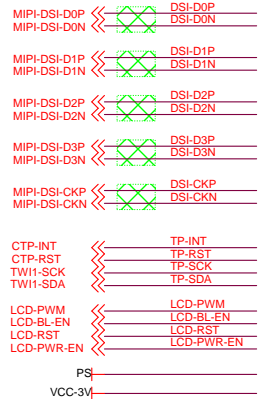
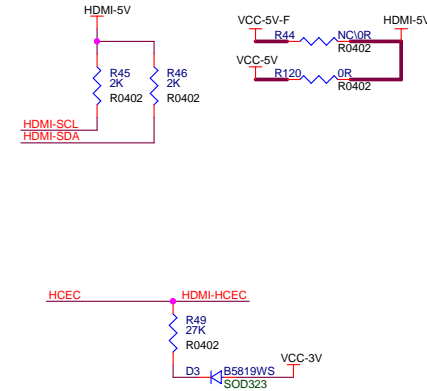
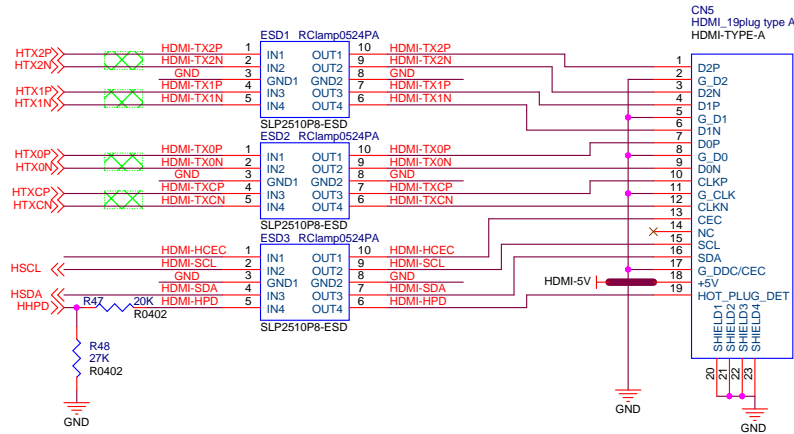
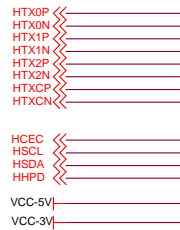


			
SINOVOIP CO.,LIMITED			
Design Name BPI-Berry			
Size A3	Page Name PMIC	Rev V1.0	
Date:	Tuesday, August 22, 2017	Sheet	4 of 15

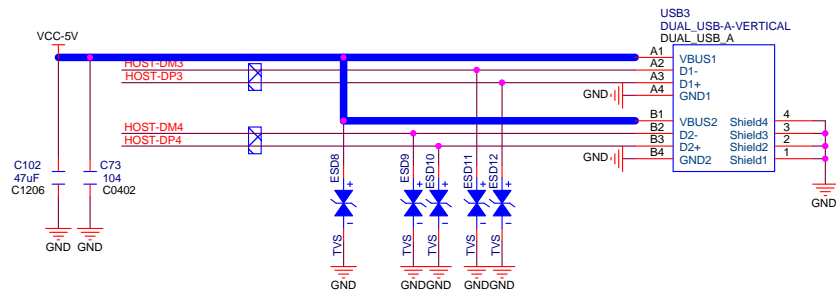
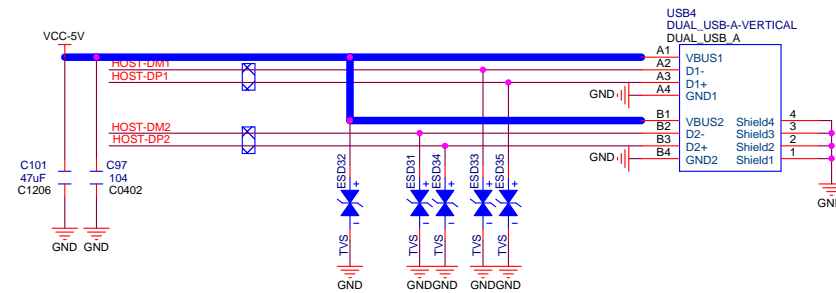
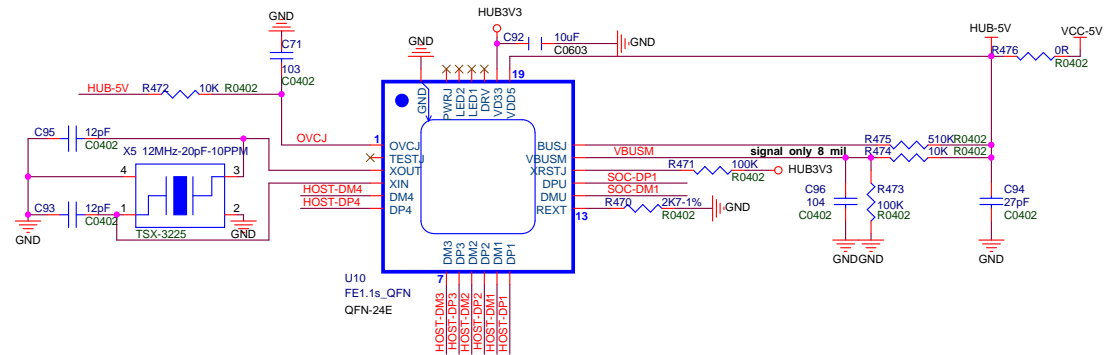
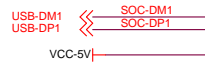
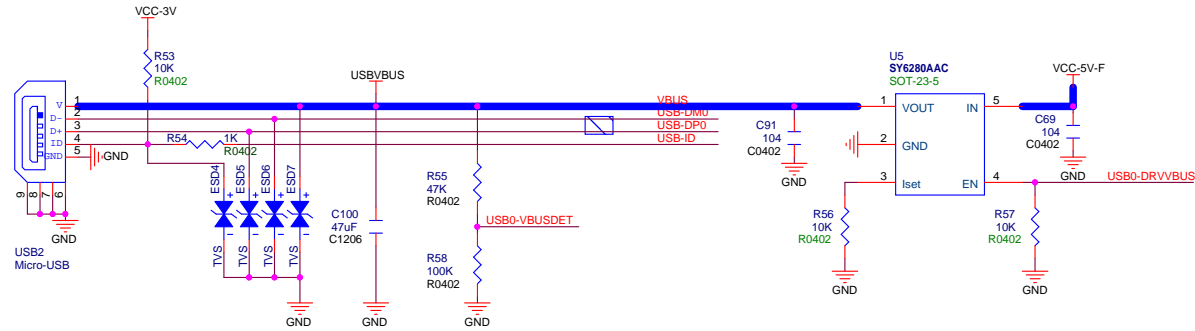
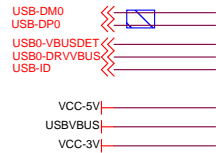
MicroSD/SATA/Camera



HDMI-MIPI_DSI



USB



WIFI+BT

WL-SDIO-CLK
WL-SDIO-CMD
WL-SDIO-D0
WL-SDIO-D1
WL-SDIO-D2
WL-SDIO-D3

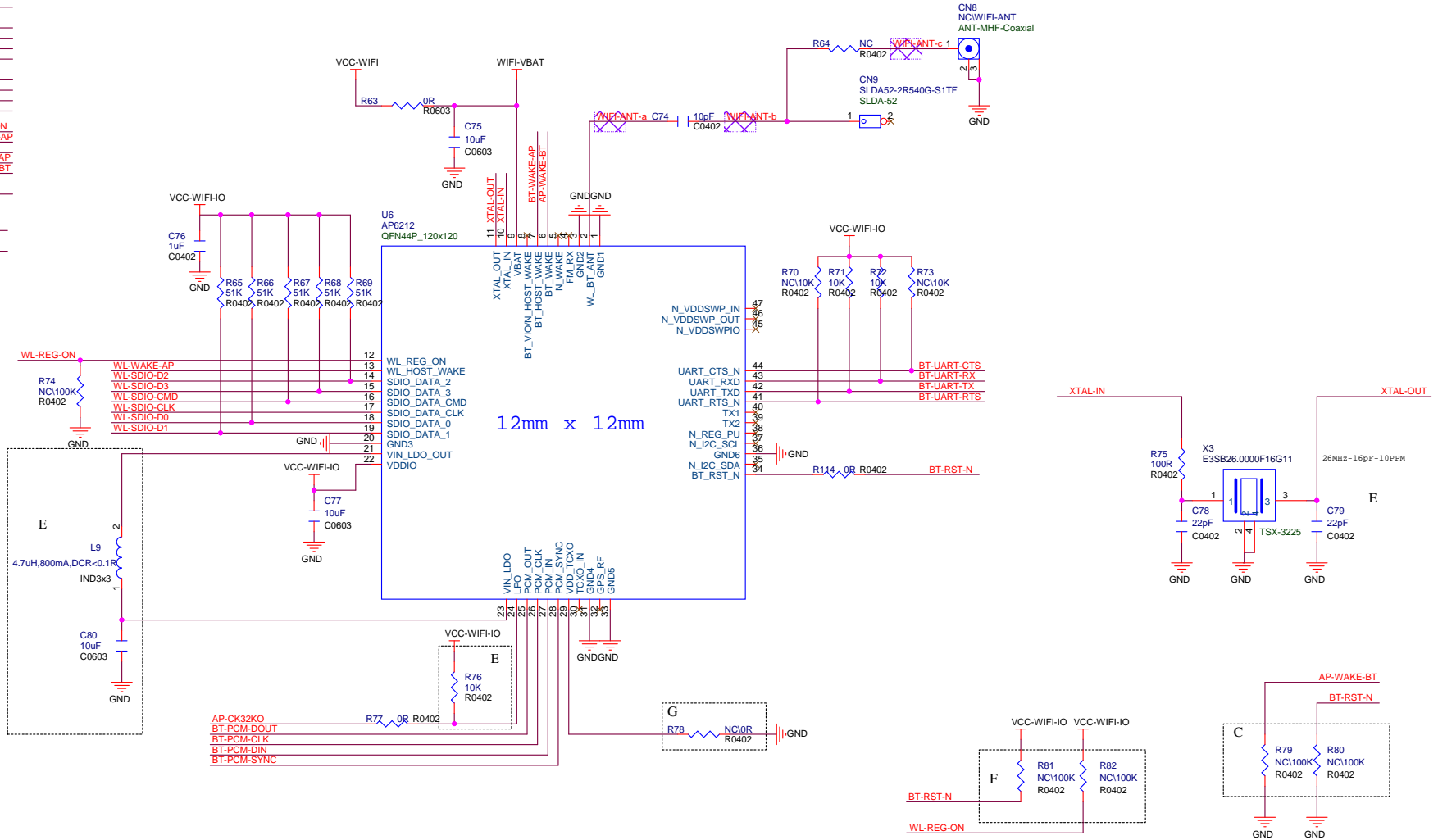
BT-UART-RX
BT-UART-TX
BT-UART-CTS
BT-UART-RTS

BT-PCM-SYNC
BT-PCM-CLK
BT-PCM-DIN
BT-PCM-DOUT

WL-REG-ON
WL-WAKE-AP
BT-RST-N
BT-WAKE-AP
AP-WAKE-BT

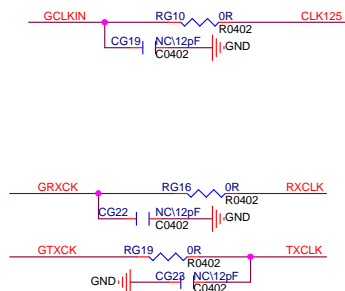
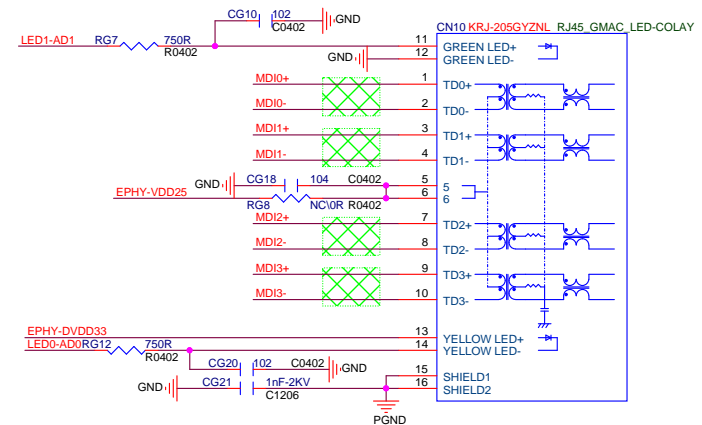
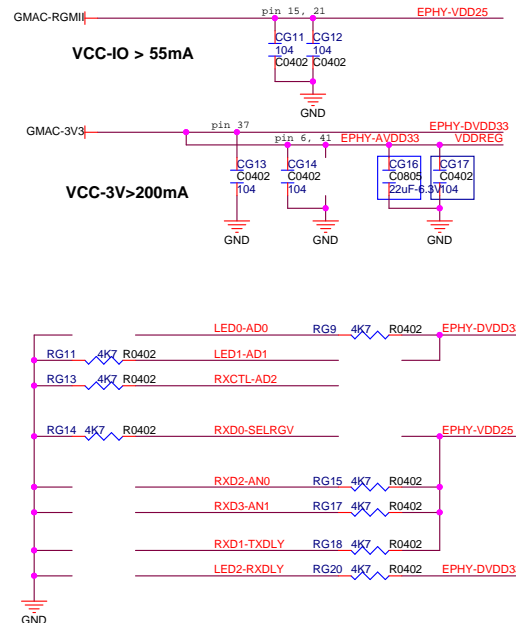
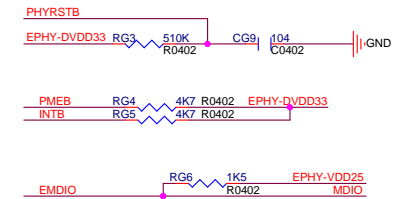
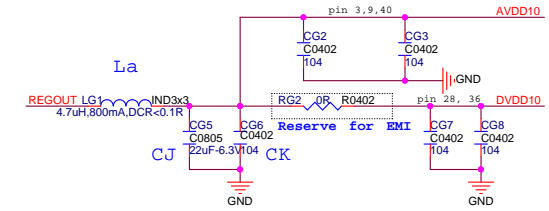
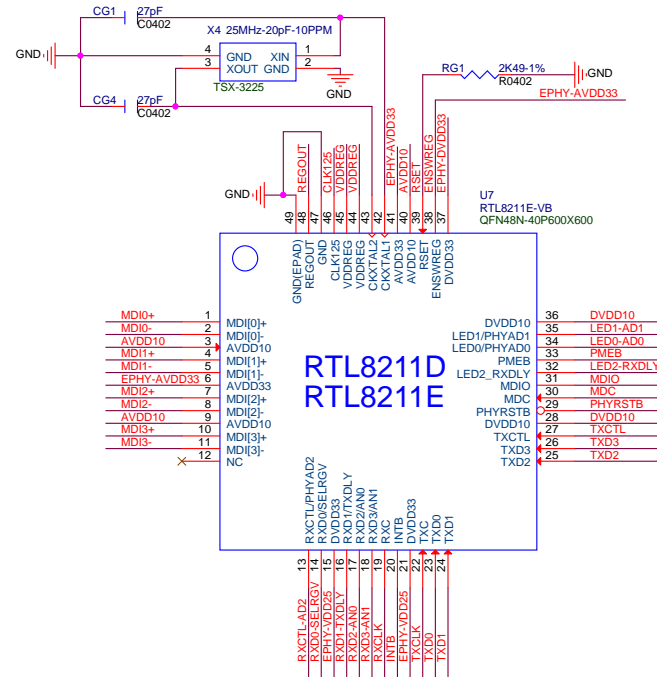
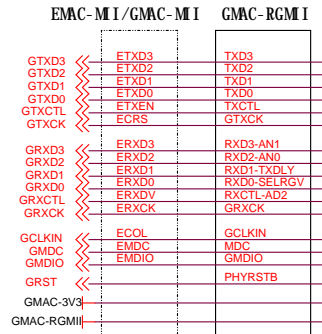
AP-CK32K0 << AP-CK32K0

VCC-WIFI
VCC-WIFI-IO



GMAC

10/100/1000 RGMII Ethernet PHY



GPIO-LED-Audio

