Content Indexing

6 LAYERS PCB STACK

TOP(Signal1) — Cu,thickness:0.7mil,Plating to 1oz

FR4,thickness:3.8mil,Dielectric Constant:4.3

GND1 — Cu,thickness:1.5mil, 1oz

FR4,thickness:8mil,Dielectric Constant:4.3

POWER — Cu,thickness:1.5mil, 1oz

FR4,thickness:adjust thickness according to the thickness of board ,Dielectric Constant:4.3

Signal2 — Cu,thickness:1.5mil, 1oz

FR4,thickness:8mil,Dielectric Constant:4.3

GND2 — Cu,thickness:1.5mil, 1oz

FR4,thickness:3.8mil,Dielectric Constant:4.3

BOTTOM(Signal3) — Cu,thickness:0.7mil,Plating to 1oz

Rockchip 瑞芯微电子 福州瑞芯微电子有限公司

Title: Index		
File: RK3288_BOX_Ref		REV:3.0
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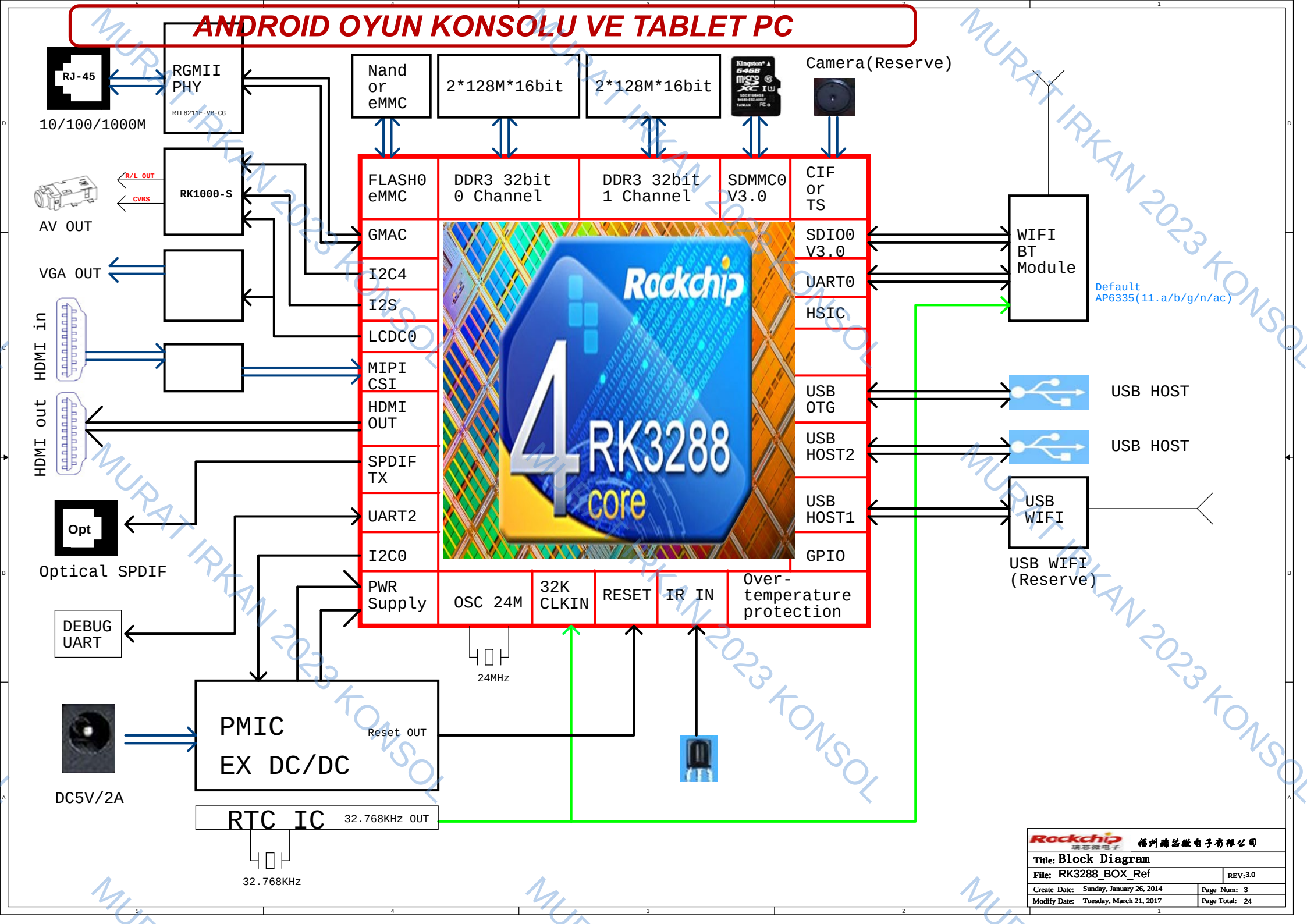
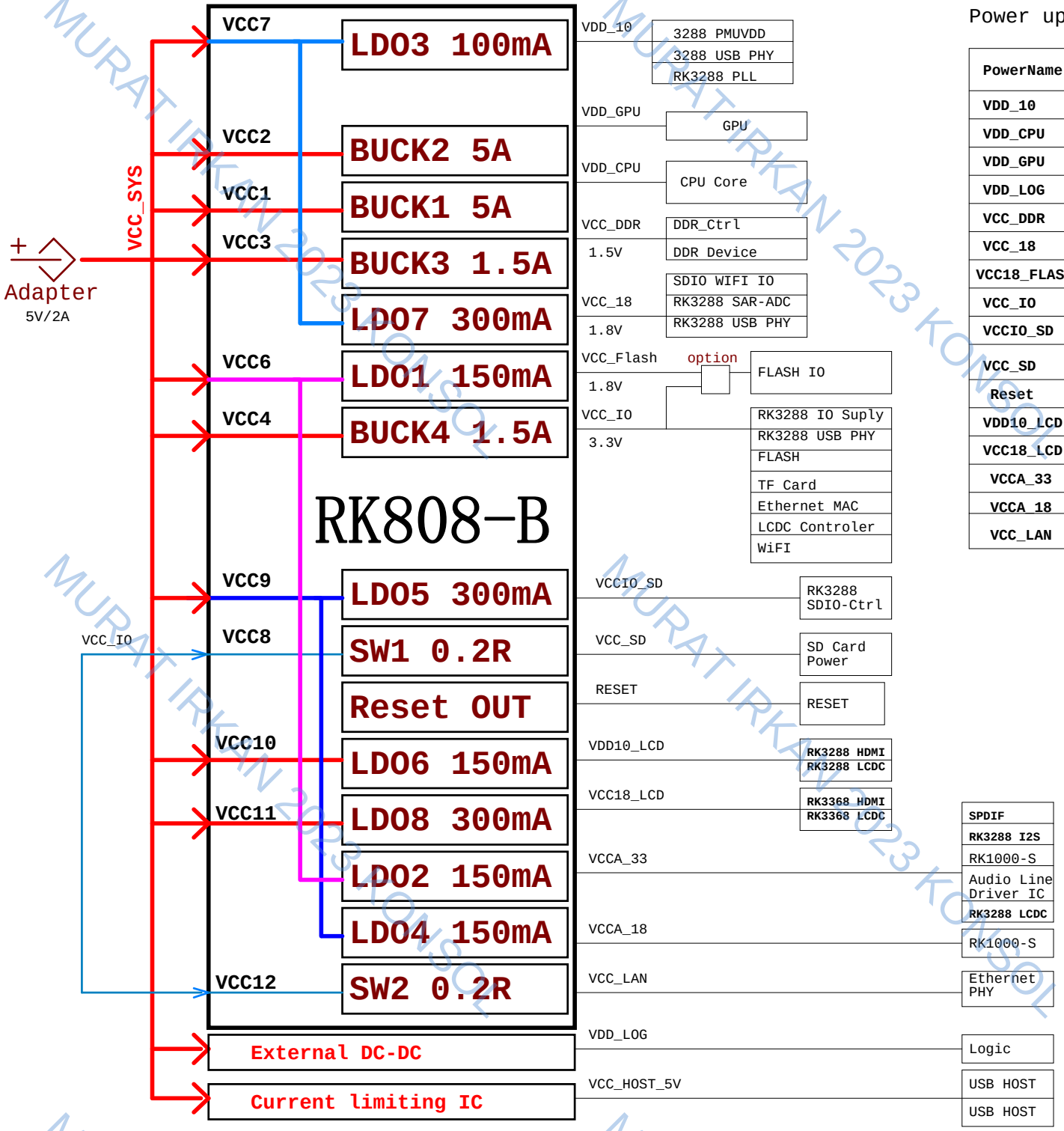
Version	Date	Author	Change Note	Approved
V2.0	20170207	LCH	Update	

Rockchip 瑞芯微电子	福州瑞芯微电子有限公司
Title: Modify note	
File: RK3288_BOX_Ref	REV:3.0
Create Date: Saturday, October 23, 2010	Page Num: 2
Modify Date: Tuesday, March 21, 2017	Page Total: 24

ANDROID OYUN KONSOLU VE TABLET PC



Title:	Block Diagram
File:	RK3288_BOX_Ref
REV:	3.0
Create Date:	Sunday, January 26, 2014
Page Num:	3
Modify Date:	Tuesday, March 21, 2017
Page Total:	24



Power up Timing

PowerName	RK808-B Channel	timer 2mS	Default voltage	Normal voltage
VDD_10	LDO3	Slot:1	1.0V	1.0V
VDD_CPU	BUCK1	Slot:2	1.1V	DVFS
VDD_GPU	BUCK2	Slot:3	1.1V	DVFS
VDD_LOG	External DC-DC	Slot:4A	1.0V	DVFS
VCC_DDR	BUCK3	Slot:3	1.5V	1.5V
VCC_18	LDO7	Slot:3	1.8V	1.8V
VCC18_FLASH	LDO1	Slot:4	1.8V	1.8V
VCC_IO	BUCK4	Slot:4	3.3V	3.3V
VCCIO_SD	LDO5	Slot:5	3.3V	1.8V or 3.3V
VCC_SD	SW1	Slot:5	3.3V	3.3V
Reset		16*2mS+50mS		
VDD10_LCD	LDO6	OFF	0V	1.0V
VCC18_LCD	LDO8	OFF	0V	1.8V
VCCA_33	LDO2	OFF	0V	3.3V
VCCA_18	LDO4	OFF	0V	1.8V
VCC_LAN	SW2	OFF	0V	3.3V

Rockchip 福州瑞芯微电子有限公司

Title: Power Tree

File: RK3288_BOX_Ref REV:3.0

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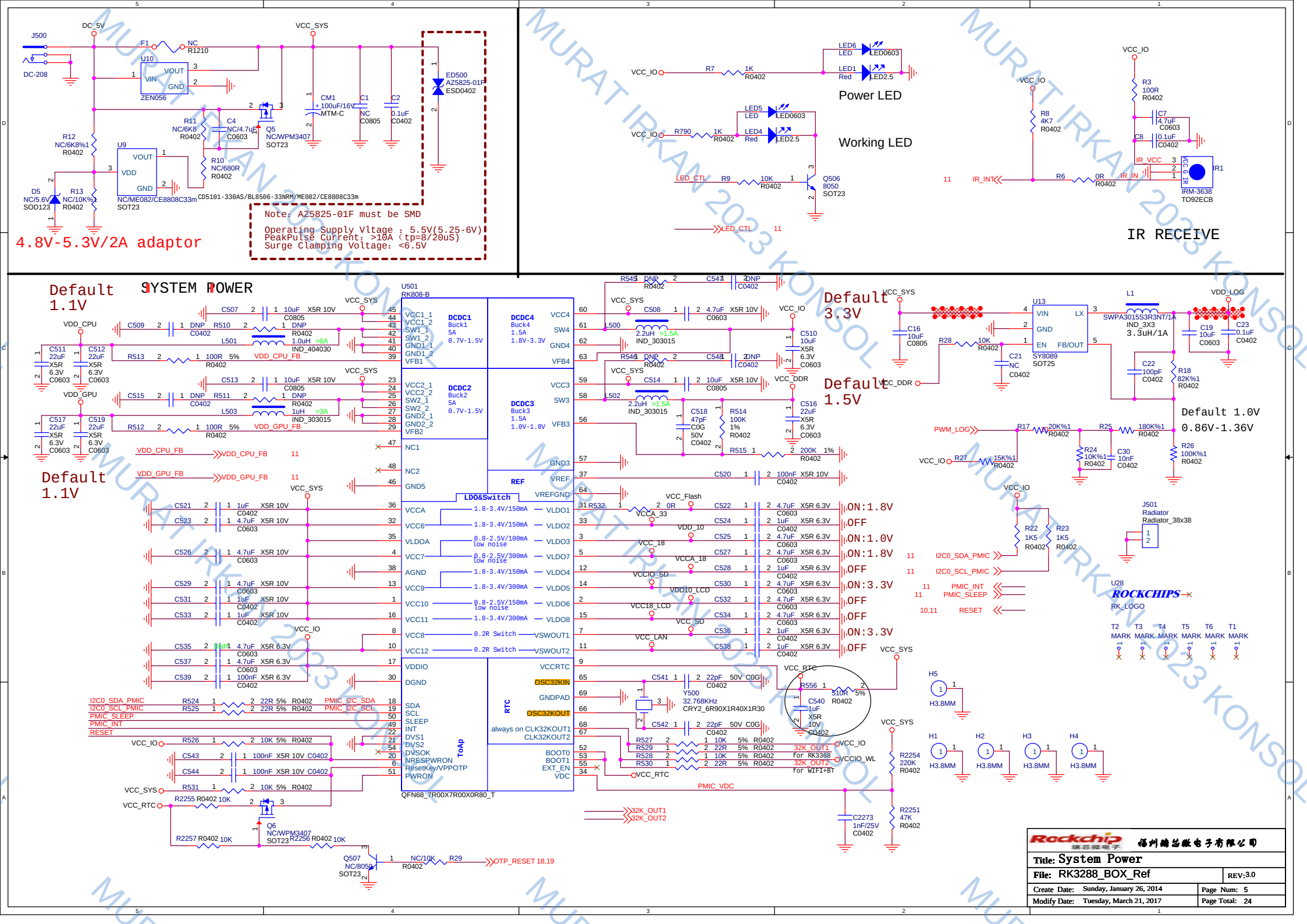
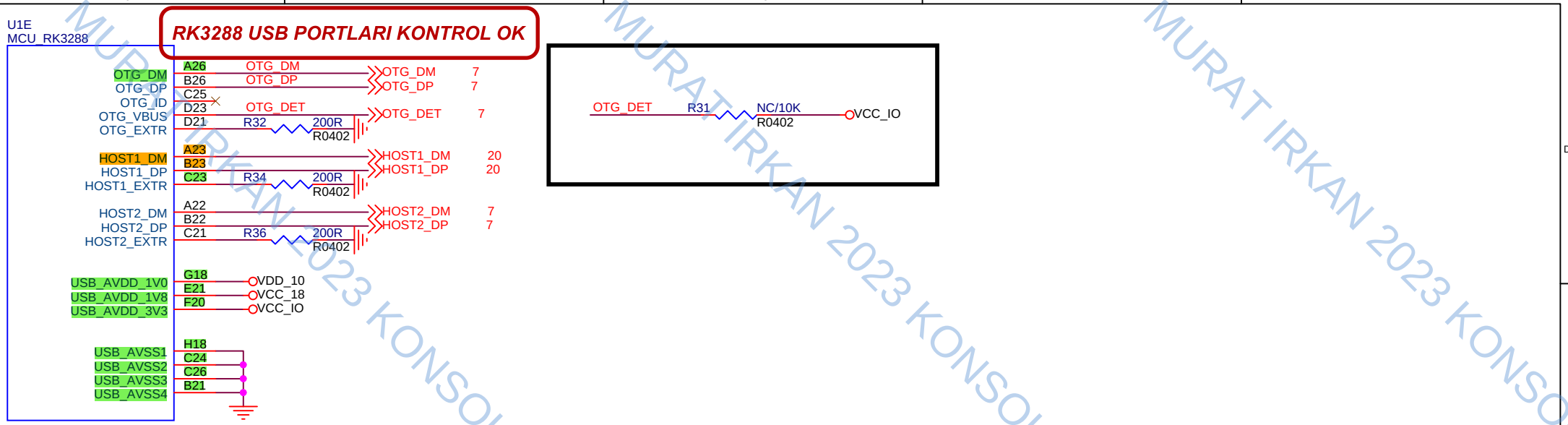
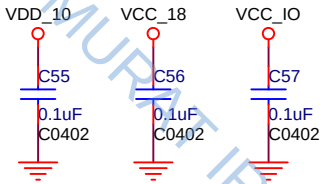
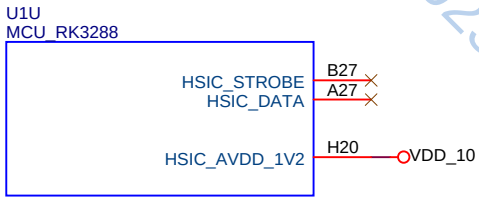

DC_5V
VCC_SYS
J500
DC-208
F1
NC
R1210
U10
VOUT
VIN
GND
ZEN056
CM1
100uF/16V
MTM-C
C1
NC
C0805
C2
0.1uF
C0402
ED500
AZ5825-01F
ESD0402
R11
NC/6K8
R0402
C4
NC/4.7uF
C0603
Q5
NC/WPM3407
SOT23
R12
NC/6K8%1
R0402
U9
VDD
NC/ME082/CE8808C33m
SOT23
R10
NC/680R
R0402
D5
NC/5.6V
SOD123
R13
NC/10K%1
R0402
CD5101-330AS/BL8506-33NRM/ME082/CE8808C33m
Note: AZ5825-01F must be SMD
Operating Supply Vltage : 5.5V(5.25-6V)
PeakPulse Current: >10A (tp=8/20uS)
Surge Clamping Voltage: <6.5V
4.8V-5.3V/2A adaptor
LED6
LED
LED0603
LED1
Red
LED2.5
R7
1K
R0402
Power LED
LED5
LED4
Working LED
R790
LED_CTL
R9
10K
Q506
8050
SOT23
11
VCC_IO
R3
100R
R0402
R8
4K7
C7
4.7uF
C8
IR_VCC
IR_INT
R6
0R
IR_IN
IR1
IRM-3638
TO92ECB
IR RECEIVE
Default
1.1V
SYSTEM POWER
U501
RK808-B
DCDC1
Buck1
5A
0.7V-1.5V
DCDC4
Buck4
1.5A
1.8V-3.3V
DCDC2
Buck2
DCDC3
Buck3
1.0V-1.8V
REF
LDO&Switch
RTC
ToAp
QFN68_7R00X7R00X0R80_T
VDD_CPU
VDD_GPU
VDD_CPU_FB
VDD_GPU_FB
Default
3.3V
Default
1.5V
VCC_DDR
SWPA3015S3R3NT/1A
IND_3X3
3.3uH/1A
SY8089
SOT25
Default 1.0V
0.86V-1.36V
PWM_LOG
VCC_Flash
VCCA_33
VDD_10
VCC_18
VCCA_18
VCCIO_SD
VDD10_LCD
VCC18_LCD
VCC_SD
VCC_LAN
ON:1.8V
OFF
ON:1.0V
ON:1.8V
OFF
ON:3.3V
OFF
OFF
ON:3.3V
OFF
Y500
32.768KHz
CRY2_6R90X1R40X1R30
VCC_RTC
PMIC_VDC
32K_OUT1
for RK3368
32K_OUT2
for WIFI+BT
VCCIO_WL
I2C0_SDA_PMIC
I2C0_SCL_PMIC
PMIC_INT
PMIC_SLEEP
RESET
J501
Radiator
Radiator_38x38
U28
ROCKCHIPS
RK_LOGO
H1
H2
H3
H4
H5
H3.8MM
OTP_RESET 18,19
Rockchip
福州瑞芯微电子有限公司
Title: System Power
File: RK3288_BOX_Ref
REV:3.0
Create Date: Sunday, January 26, 2014
Page Num: 5
Modify Date: Tuesday, March 21, 2017
Page Total: 24



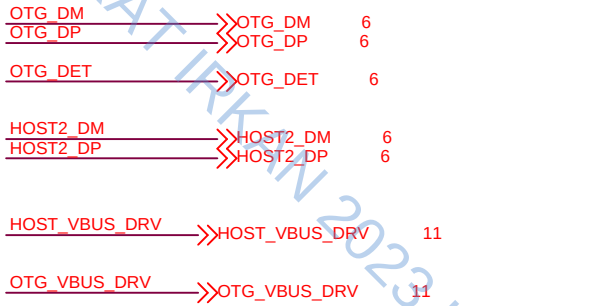
RK3288_E

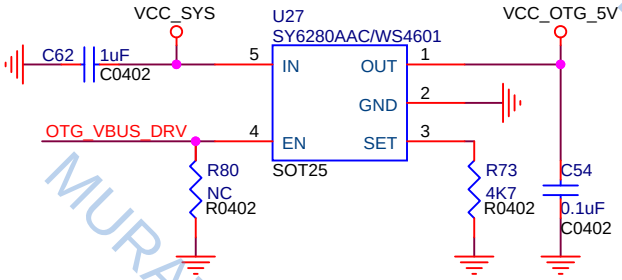


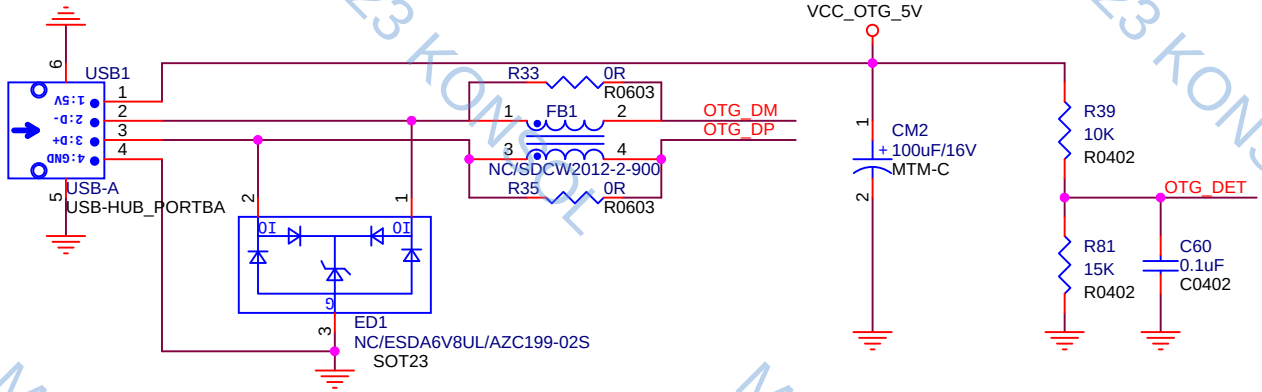


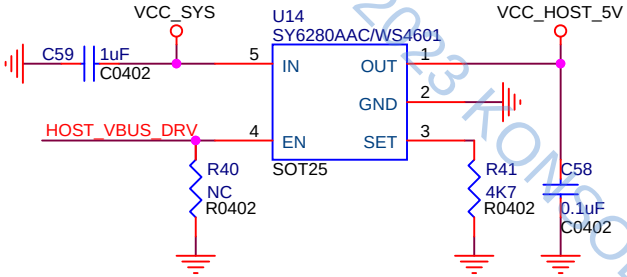
RK3288_U

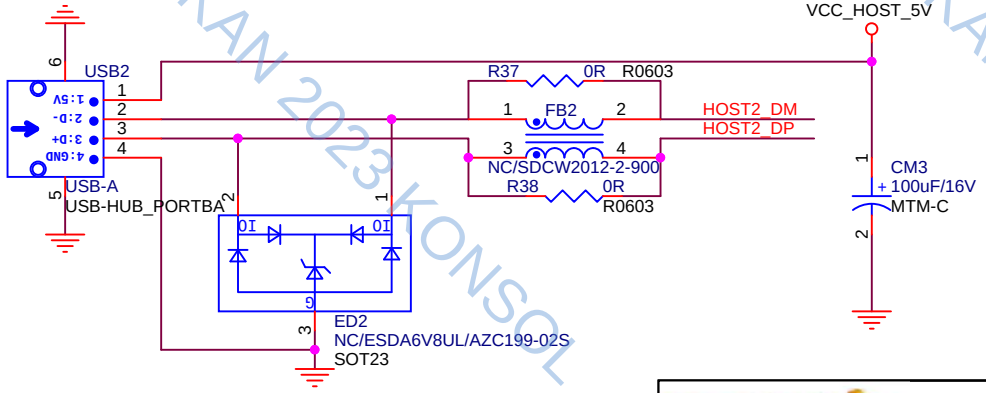
Rockchip 瑞芯微电子 福州瑞芯微电子有限公司		
Title: RK3288 USB/HSIC Controler		
File: RK3288_BOX_Ref		REV:3.0
Create Date: Monday, February 17, 2014		Page Num: 6
Modify Date: Tuesday, March 21, 2017		Page Total: 24





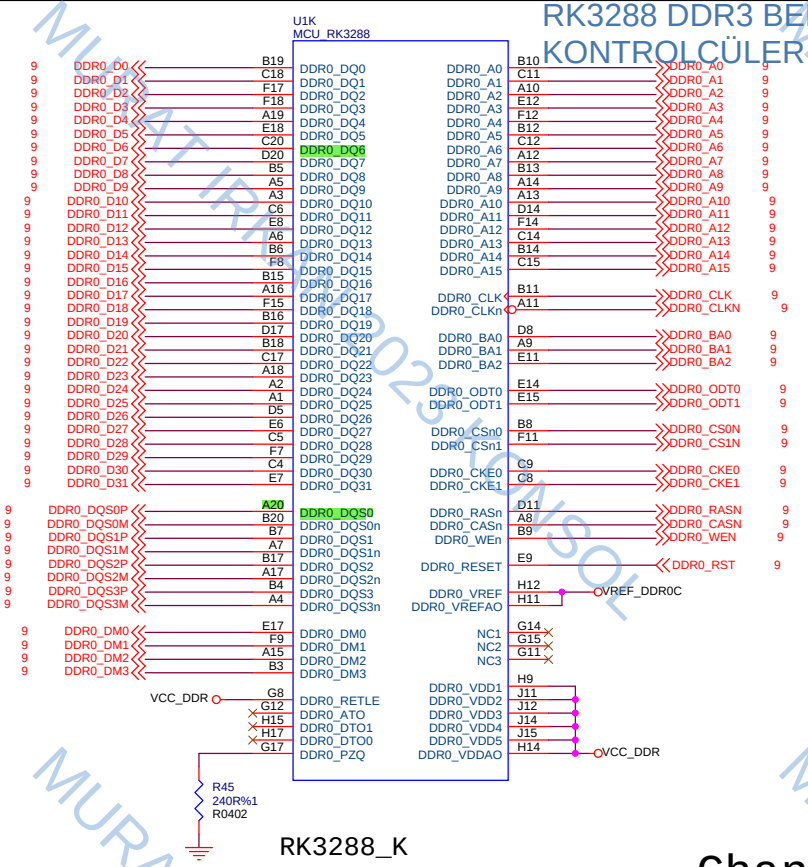






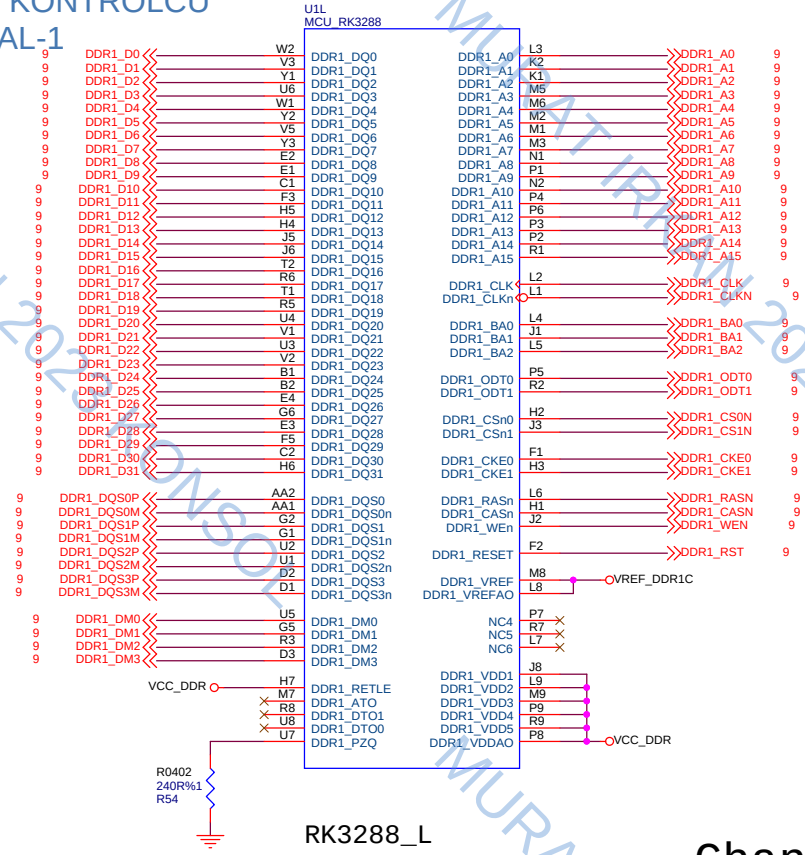
Rockchip 瑞芯微电子 福州瑞芯微电子有限公司		
Title: USB HOST		
File: RK3288_BOX_Ref		REV:3.0
Create Date: Sunday, January 26, 2014		Page Num: 7
Modify Date: Tuesday, March 21, 2017		Page Total: 24

RK3288 DDR3 BELLEK KONTROLCÜLER KANAL-0

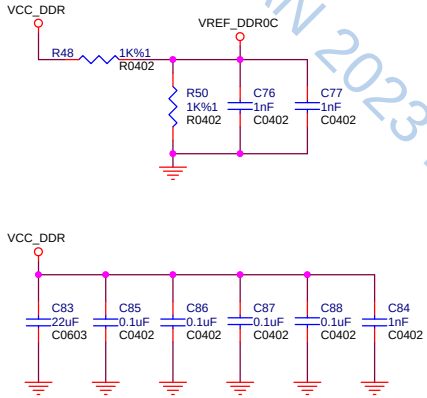


Channel 0

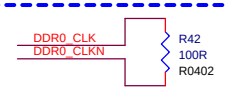
DDR KONTROLCÜ KANAL-1



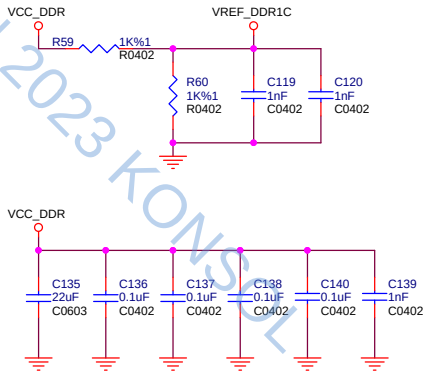
Channel 1



DDR0 FILTER



Note:
These termination resistors must be placed in the middle of trace, and the termination resistor of CLK must be placed in thebifurcation point.



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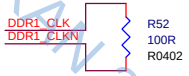

Note:
These termination resistors must be placed in the middle of trace, and the termination resistor of CLK must be placed in thebifurcation point.

Rockchip 瑞芯微电子	福州瑞芯微电子有限公司	
Title: RK3288 RAM Controler		
File: RK3288_BOX_Ref		REV:3.0
Create Date: Monday, February 17, 2014		Page Num: 8
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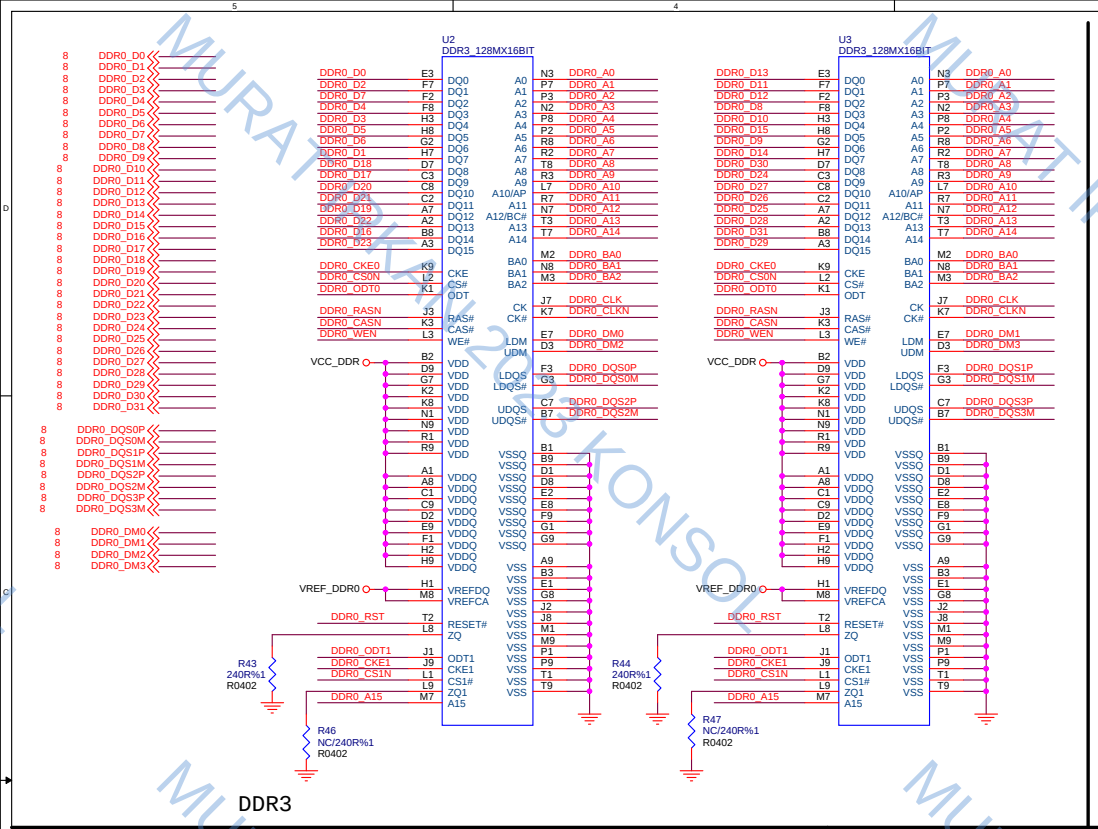
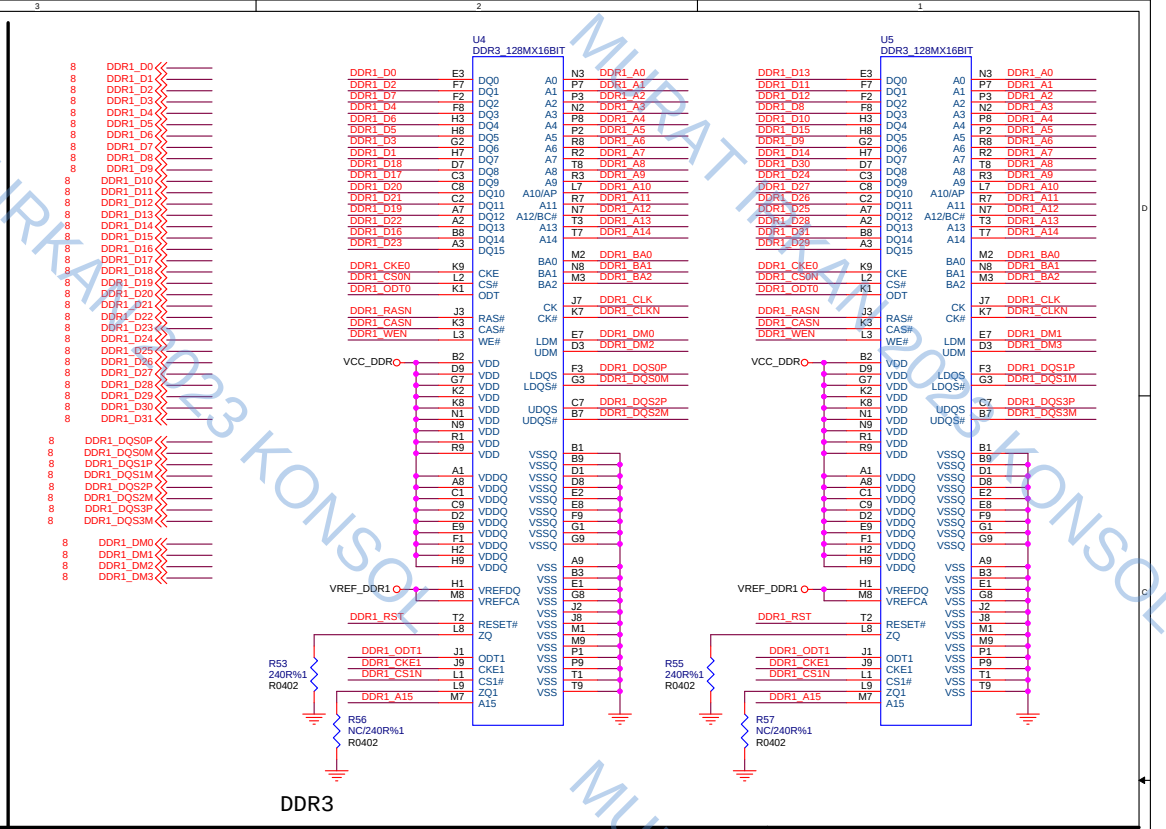
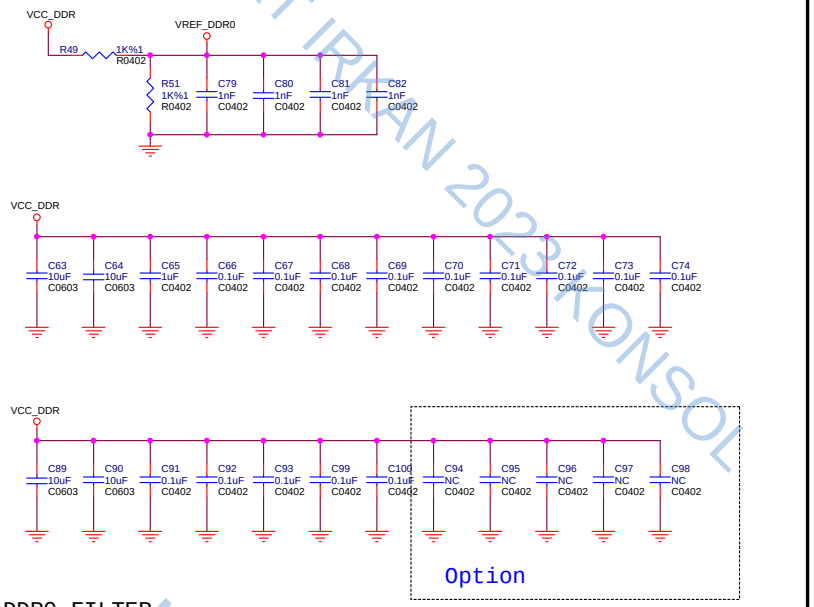


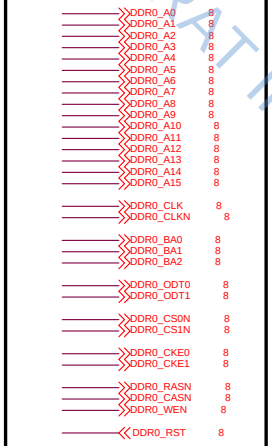
DDR3

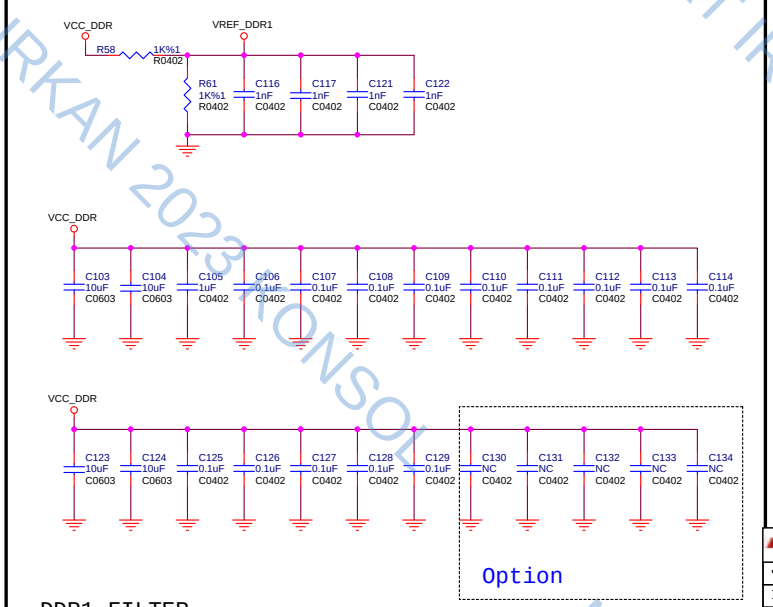


DDR3



DDR0 FILTER





DDR1 FILTER

Rockchip 瑞芯微电子	福州瑞芯微电子有限公司	
Title: RAM-DDR3-4X16bit		
File: RK3288_BOX_Ref		REV:3.0
Create Date: Tuesday, November 09, 2010		Page Num: 9
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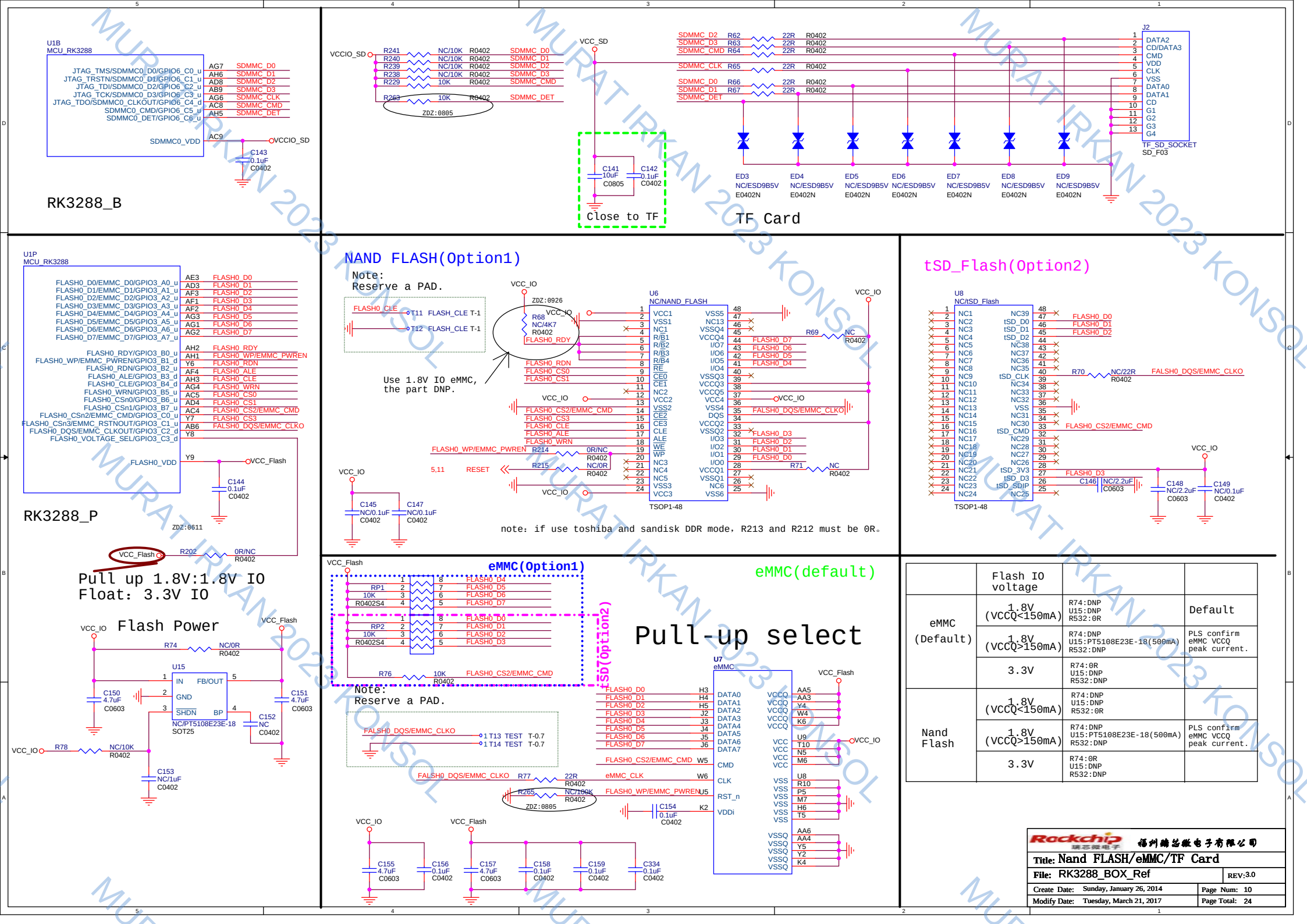

	Flash IO voltage		
eMMC (Default)	1.8V (VCCQ<150mA)	R74:DNP U15:DNP R532:0R	Default
	1.8V (VCCQ>150mA)	R74:DNP U15:PT5108E23E-18(500mA) R532:DNP	PLS confirm eMMC VCCQ peak current.
	3.3V	R74:0R U15:DNP R532:DNP	
Nand Flash	1.8V (VCCQ<150mA)	R74:DNP U15:DNP R532:0R	
	1.8V (VCCQ>150mA)	R74:DNP U15:PT5108E23E-18(500mA) R532:DNP	PLS confirm eMMC VCCQ peak current.
	3.3V	R74:0R U15:DNP R532:DNP	

Rockchip 瑞芯微电子	福州瑞芯微电子有限公司
Title:	Nand FLASH/eMMC/TF Card
File:	RK3288_BOX_Ref REV:3.0
Create Date: Sunday, January 26, 2014	Page Num: 10
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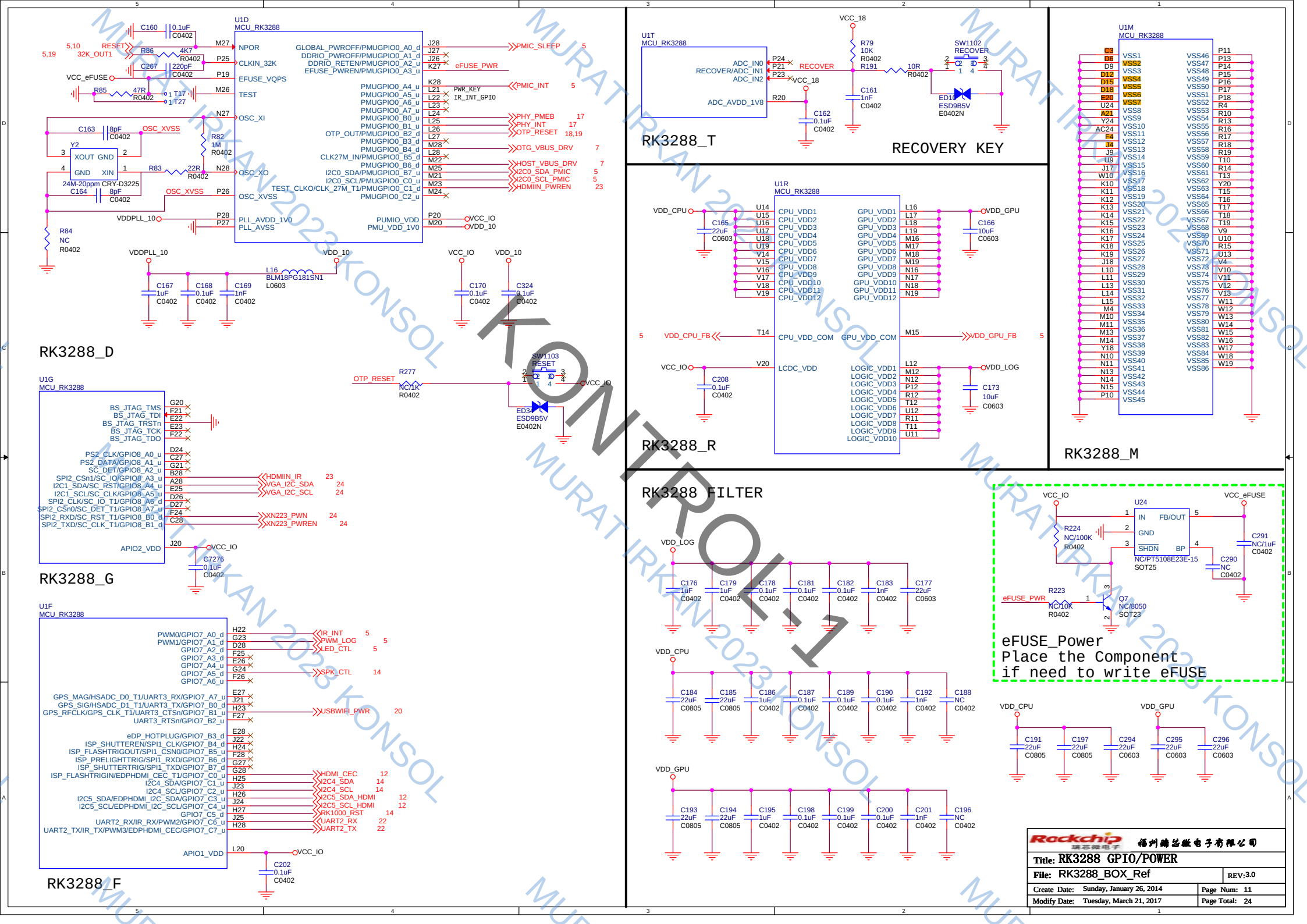
RK3288_D
RK3288_G
RK3288_F
RK3288_T
RECOVERY KEY
RK3288_R
RK3288_M
RK3288 FILTER
eFUSE_Power
Place the Component
if need to write eFUSE
Rockchip 瑞芯微电子
福州瑞芯微电子有限公司
Title: RK3288 GPIO/POWER
File: RK3288_BOX_Ref
REV:3.0
Create Date: Sunday, January 26, 2014
Page Num: 11
Modify Date: Tuesday, March 21, 2017
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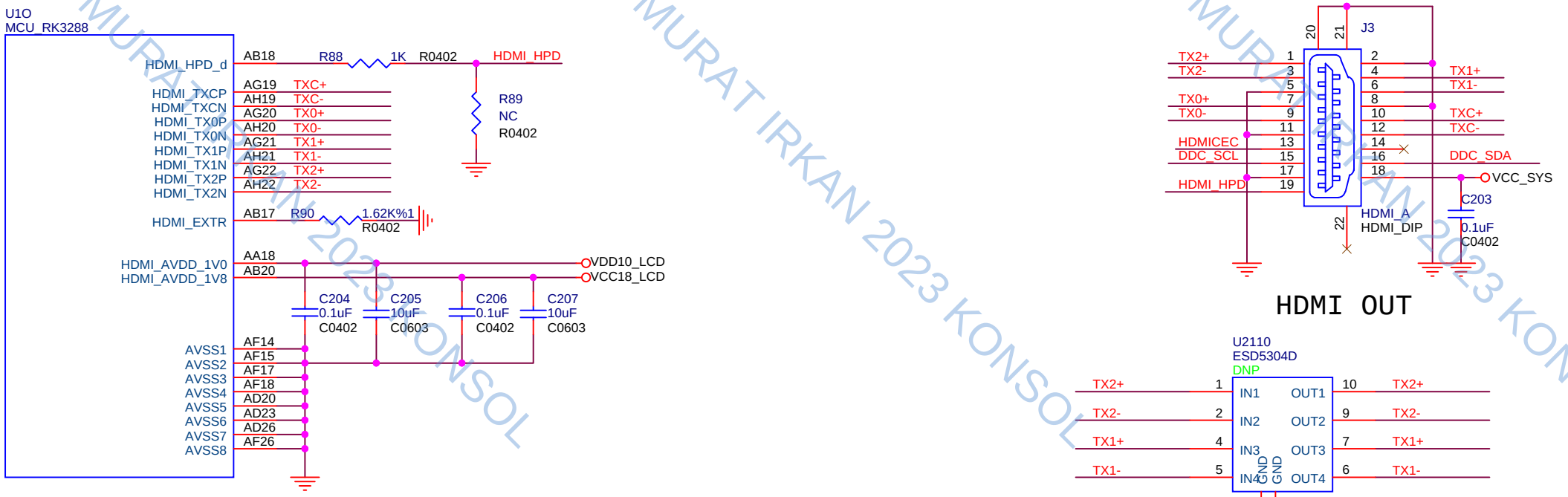
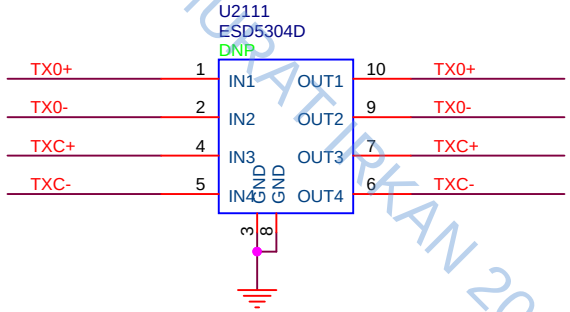
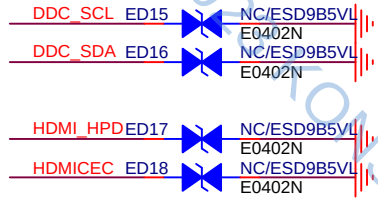
KONTROL-1
MURAT IRKAN 2023 KONSOL

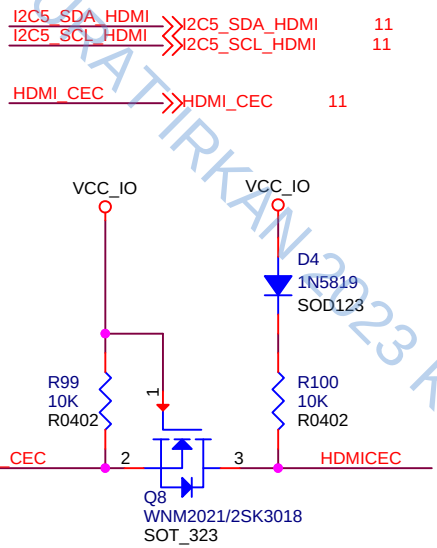


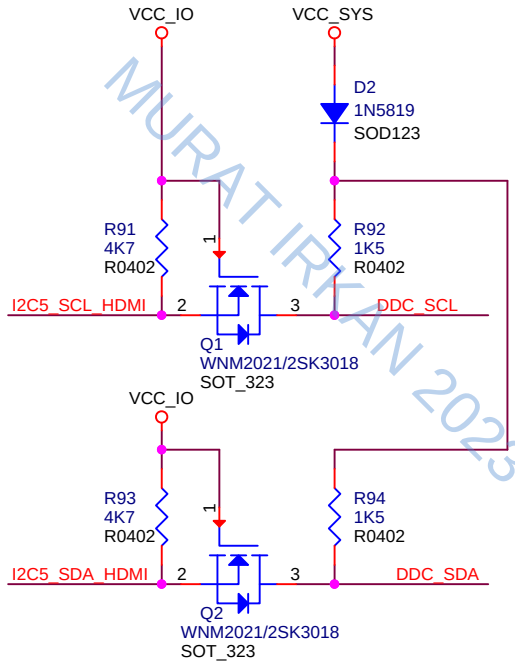
HDMI OUT





RK3288_0





Rockchip 瑞芯微电子	福州瑞芯微电子有限公司	
Title: HDMI OUT		
File: RK3288_BOX_Ref		REV:3.0
Create Date: Sunday, January 26, 2014		Page Num: 12
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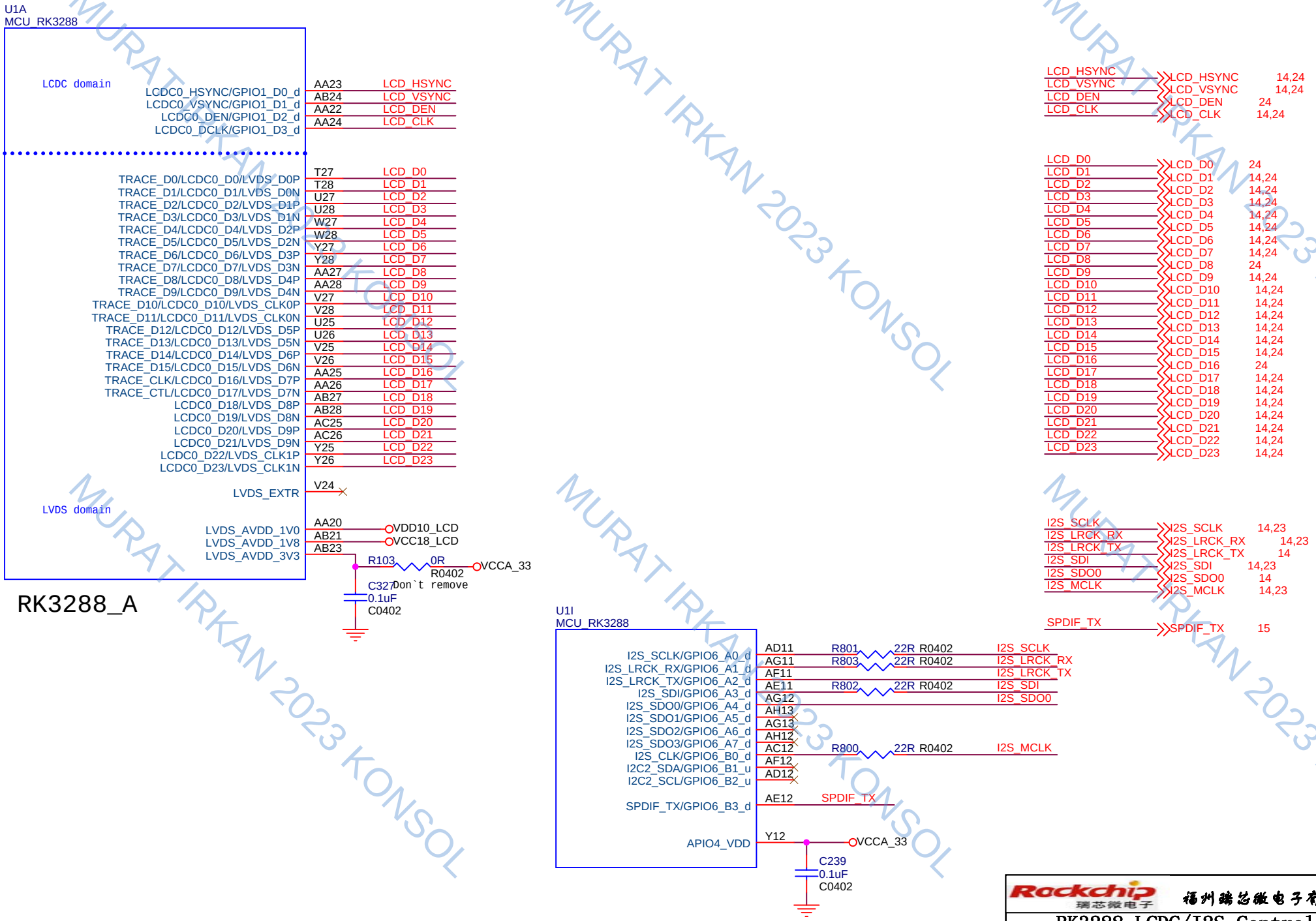
U1A
MCU_RK3288
LCDC domain
LCDC0_HSYNC/GPIO1_D0_d
LCDC0_VSYNC/GPIO1_D1_d
LCDC0_DEN/GPIO1_D2_d
LCDC0_DCLK/GPIO1_D3_d
AA23 LCD_HSYNC
AB24 LCD_VSYNC
AA22 LCD_DEN
AA24 LCD_CLK
TRACE_D0/LCDC0_D0/LVDS_D0P
TRACE_D1/LCDC0_D1/LVDS_D0N
TRACE_D2/LCDC0_D2/LVDS_D1P
TRACE_D3/LCDC0_D3/LVDS_D1N
TRACE_D4/LCDC0_D4/LVDS_D2P
TRACE_D5/LCDC0_D5/LVDS_D2N
TRACE_D6/LCDC0_D6/LVDS_D3P
TRACE_D7/LCDC0_D7/LVDS_D3N
TRACE_D8/LCDC0_D8/LVDS_D4P
TRACE_D9/LCDC0_D9/LVDS_D4N
TRACE_D10/LCDC0_D10/LVDS_CLK0P
TRACE_D11/LCDC0_D11/LVDS_CLK0N
TRACE_D12/LCDC0_D12/LVDS_D5P
TRACE_D13/LCDC0_D13/LVDS_D5N
TRACE_D14/LCDC0_D14/LVDS_D6P
TRACE_D15/LCDC0_D15/LVDS_D6N
TRACE_CLK/LCDC0_D16/LVDS_D7P
TRACE_CTL/LCDC0_D17/LVDS_D7N
LCDC0_D18/LVDS_D8P
LCDC0_D19/LVDS_D8N
LCDC0_D20/LVDS_D9P
LCDC0_D21/LVDS_D9N
LCDC0_D22/LVDS_CLK1P
LCDC0_D23/LVDS_CLK1N
T27 LCD_D0
T28 LCD_D1
U27 LCD_D2
U28 LCD_D3
W27 LCD_D4
W28 LCD_D5
Y27 LCD_D6
Y28 LCD_D7
AA27 LCD_D8
AA28 LCD_D9
V27 LCD_D10
V28 LCD_D11
U25 LCD_D12
U26 LCD_D13
V25 LCD_D14
V26 LCD_D15
AA25 LCD_D16
AA26 LCD_D17
AB27 LCD_D18
AB28 LCD_D19
AC25 LCD_D20
AC26 LCD_D21
Y25 LCD_D22
Y26 LCD_D23
LVDS_EXTR
V24
LVDS domain
LVDS_AVDD_1V0
LVDS_AVDD_1V8
LVDS_AVDD_3V3
AA20 VDD10_LCD
AB21 VCC18_LCD
AB23
R103 0R R0402 Don`t remove
VCCA_33
C327 0.1uF C0402
RK3288_A
LCD_HSYNC LCD_HSYNC 14,24
LCD_VSYNC LCD_VSYNC 14,24
LCD_DEN LCD_DEN 24
LCD_CLK LCD_CLK 14,24
LCD_D0 LCD_D0 24
LCD_D1 LCD_D1 14,24
LCD_D2 LCD_D2 14,24
LCD_D3 LCD_D3 14,24
LCD_D4 LCD_D4 14,24
LCD_D5 LCD_D5 14,24
LCD_D6 LCD_D6 14,24
LCD_D7 LCD_D7 14,24
LCD_D8 LCD_D8 24
LCD_D9 LCD_D9 14,24
LCD_D10 LCD_D10 14,24
LCD_D11 LCD_D11 14,24
LCD_D12 LCD_D12 14,24
LCD_D13 LCD_D13 14,24
LCD_D14 LCD_D14 14,24
LCD_D15 LCD_D15 14,24
LCD_D16 LCD_D16 24
LCD_D17 LCD_D17 14,24
LCD_D18 LCD_D18 14,24
LCD_D19 LCD_D19 14,24
LCD_D20 LCD_D20 14,24
LCD_D21 LCD_D21 14,24
LCD_D22 LCD_D22 14,24
LCD_D23 LCD_D23 14,24
I2S_SCLK I2S_SCLK 14,23
I2S_LRCK_RX I2S_LRCK_RX 14,23
I2S_LRCK_TX I2S_LRCK_TX 14
I2S_SDI I2S_SDI 14,23
I2S_SDO0 I2S_SDO0 14
I2S_MCLK I2S_MCLK 14,23
SPDIF_TX SPDIF_TX 15
U1I
MCU_RK3288
I2S_SCLK/GPIO6_A0_d
I2S_LRCK_RX/GPIO6_A1_d
I2S_LRCK_TX/GPIO6_A2_d
I2S_SDI/GPIO6_A3_d
I2S_SDO0/GPIO6_A4_d
I2S_SDO1/GPIO6_A5_d
I2S_SDO2/GPIO6_A6_d
I2S_SDO3/GPIO6_A7_d
I2S_CLK/GPIO6_B0_d
I2C2_SDA/GPIO6_B1_u
I2C2_SCL/GPIO6_B2_u
SPDIF_TX/GPIO6_B3_d
APIO4_VDD
AD11 R801 22R R0402 I2S_SCLK
AG11 R803 22R R0402 I2S_LRCK_RX
AF11 I2S_LRCK_TX
AE11 R802 22R R0402 I2S_SDI
AG12 I2S_SDO0
AH13
AG13
AH12
AC12 R800 22R R0402 I2S_MCLK
AF12
AD12
AE12 SPDIF_TX
Y12
VCCA_33
C239 0.1uF C0402
RK3288_I
Rockchip 瑞芯微电子
福州瑞芯微电子有限公司
Title: RK3288 LCDC/I2S Controler
File: RK3288_BOX_Ref
REV:3.0
Create Date: Monday, February 17, 2014
Page Num: 13
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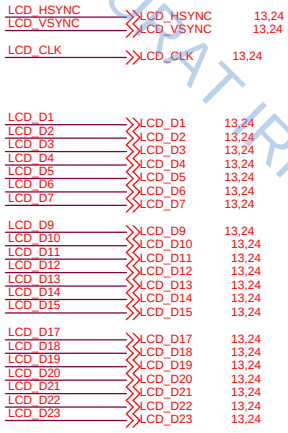
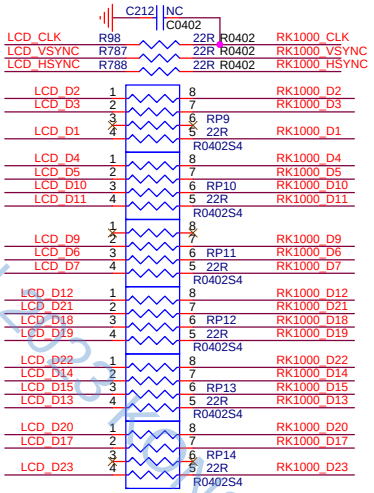
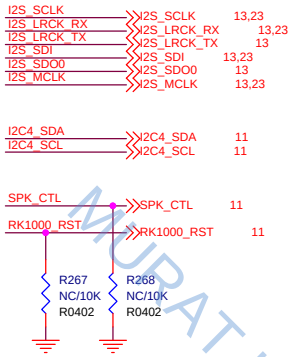
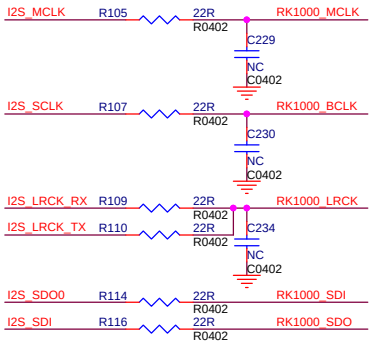
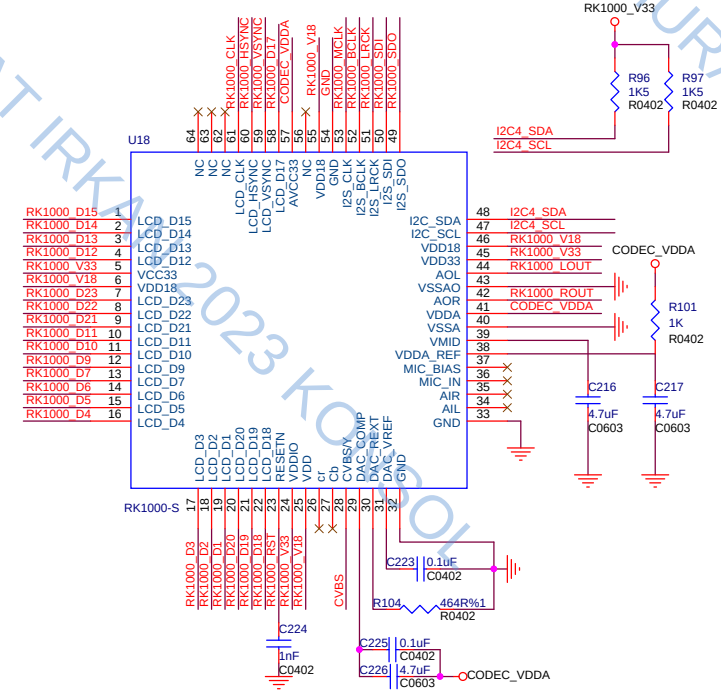
Page Total: 24

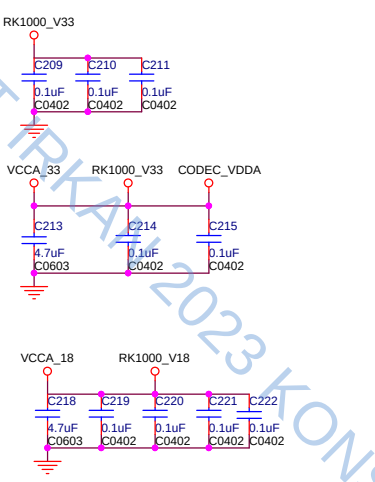


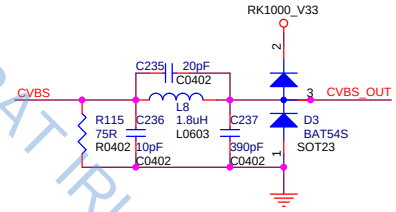


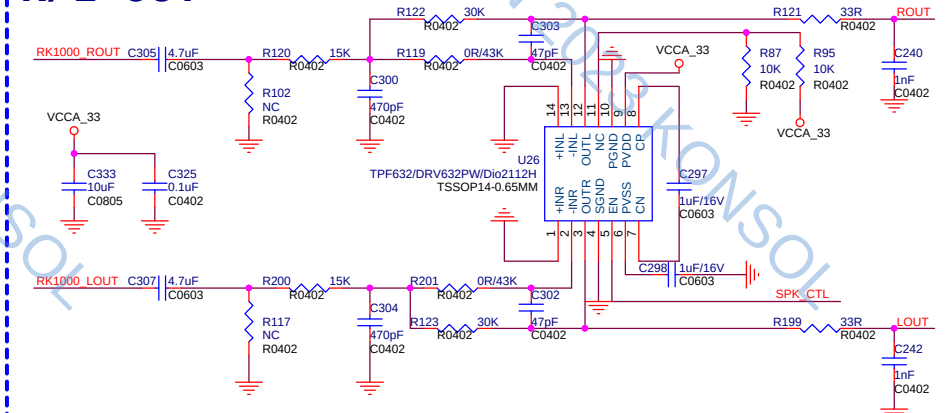


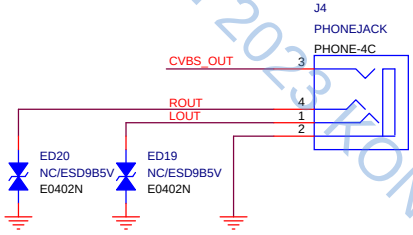












Rockchip 瑞芯微电子	福州瑞芯微电子有限公司
Title: RK1000-S-AV OUT	
File: RK3288_BOX_Ref	REV:3.0
Create Date: Sunday, January 26, 2014	Page Num: 14
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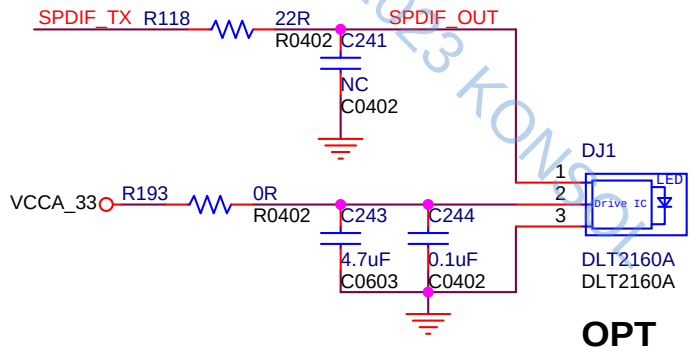
SPDIF_TX >> SPDIF_TX 13



Optical S/PDIF OUT

Rockchip 瑞芯微电子 福州瑞芯微电子有限公司	
Title: S/PDIF OUT	
File: RK3288_BOX_Ref	REV:3.0
Create Date: Monday, February 17, 2014	Page Num: 15
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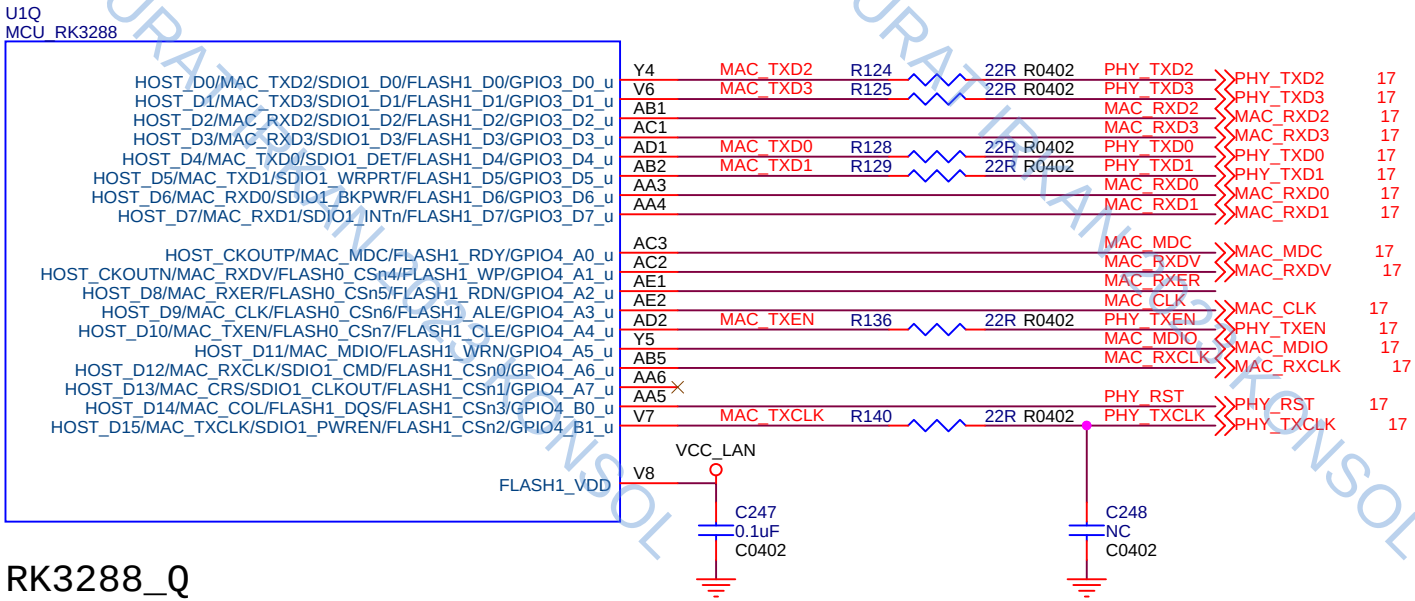
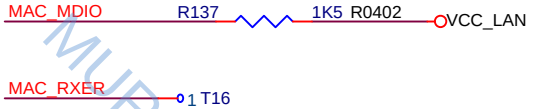
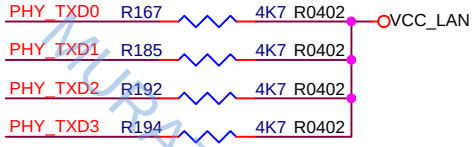


RK3288_Q





Rockchip 瑞芯微电子	福州瑞芯微电子有限公司	
Title: RK3288 EthernetMAC Controler		
File: RK3288_BOX_Ref		REV:3.0
Create Date: Monday, February 17, 2014		Page Num: 16
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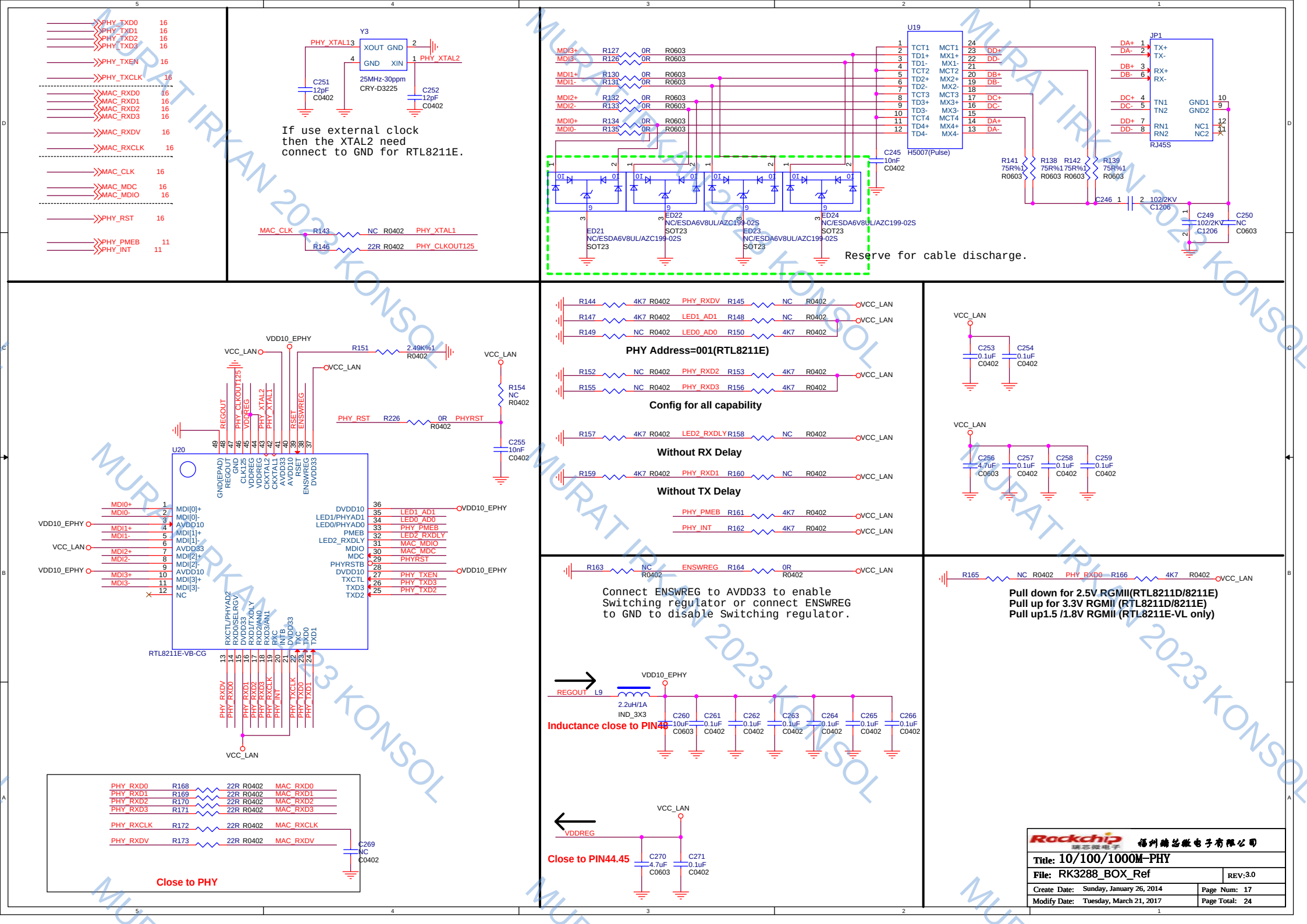
PHY_TXD0 16
PHY_TXD1 16
PHY_TXD2 16
PHY_TXD3 16
PHY_TXEN 16
PHY_TXCLK 16
MAC_RXD0 16
MAC_RXD1 16
MAC_RXD2 16
MAC_RXD3 16
MAC_RXDV 16
MAC_RXCLK 16
MAC_CLK 16
MAC_MDC 16
MAC_MDIO 16
PHY_RST 16
PHY_PMEB 11
PHY_INT 11
Y3
PHY_XTAL13
XOUT GND
GND XIN
PHY_XTAL2
25MHz-30ppm
CRY-D3225
C251 12pF C0402
C252 12pF C0402
If use external clock
then the XTAL2 need
connect to GND for RTL8211E.
MAC_CLK R143 NC R0402 PHY_XTAL1
R146 22R R0402 PHY_CLKOUT125
U19
MDI3+ R127 0R R0603
MDI3- R126 0R R0603
MDI1+ R130 0R R0603
MDI1- R131 0R R0603
MDI2+ R132 0R R0603
MDI2- R133 0R R0603
MDI0+ R134 0R R0603
MDI0- R135 0R R0603
TCT1 TD1+ TD1- TCT2 TD2+ TD2- TCT3 TD3+ TD3- TCT4 TD4+ TD4-
MCT1 MX1+ MX1- MCT2 MX2+ MX2- MCT3 MX3+ MX3- MCT4 MX4+ MX4-
H5007(Pulse)
C245 10nF C0402
ED21 NC/ESDA6V8UL/AZC199-02S SOT23
ED22 NC/ESDA6V8UL/AZC199-02S SOT23
ED23 NC/ESDA6V8UL/AZC199-02S SOT23
ED24 NC/ESDA6V8UL/AZC199-02S SOT23
Reserve for cable discharge.
JP1
DA+ DA- DB+ DB- DC+ DC- DD+ DD-
TX+ TX- RX+ RX- TN1 TN2 RN1 RN2 GND1 GND2 NC1 NC2
RJ45S
R141 75R%1 R0603
R138 75R%1 R0603
R142 75R%1 R0603
R139 75R%1 R0603
C246 102/2KV C1206
C249 102/2KV C1206
C250 NC C0603
U20
VDD10_EPHY
VCC_LAN
R151 2.49K%1 R0402
R154 NC R0402
PHY_RST R226 0R PHYRST R0402
C255 10nF C0402
GND(EPAD) REGOUT GND CLK125 VDDREG VDDREG CKXTAL2 CKXTAL1 AVDD33 AVDD10 RSET ENSWREG DVDD33
MDI[0]+ MDI[0]- AVDD10 MDI[1]+ MDI[1]- AVDD33 MDI[2]+ MDI[2]- AVDD10 MDI[3]+ MDI[3]- NC
DVDD10 LED1/PHYAD1 LED0/PHYAD0 PMEB LED2_RXDLY MDIO MDC PHYRSTB DVDD10 TXCTL TXD3 TXD2
RXCTL/PHYAD2 RXD0/SELRGV DVDD33 RXD1/TXDLY RXD2/AN0 RXD3/AN1 RXC INTB DVDD33 TXC TXD0 TXD1
RTL8211E-VB-CG
PHY_RXDV PHY_RXD0 PHY_RXD1 PHY_RXD2 PHY_RXD3 PHY_RXCLK PHY_INT PHY_TXCLK PHY_TXD0 PHY_TXD1
R144 4K7 R0402 PHY_RXDV R145 NC R0402
R147 4K7 R0402 LED1_AD1 R148 NC R0402
R149 NC R0402 LED0_AD0 R150 4K7 R0402
PHY Address=001(RTL8211E)
R152 NC R0402 PHY_RXD2 R153 4K7 R0402
R155 NC R0402 PHY_RXD3 R156 4K7 R0402
Config for all capability
R157 4K7 R0402 LED2_RXDLY R158 NC R0402
Without RX Delay
R159 4K7 R0402 PHY_RXD1 R160 NC R0402
Without TX Delay
PHY_PMEB R161 4K7 R0402
PHY_INT R162 4K7 R0402
VCC_LAN
C253 0.1uF C0402
C254 0.1uF C0402
C256 4.7uF C0603
C257 0.1uF C0402
C258 0.1uF C0402
C259 0.1uF C0402
R163 NC R0402 ENSWREG R164 0R R0402
Connect ENSWREG to AVDD33 to enable
Switching regulator or connect ENSWREG
to GND to disable Switching regulator.
R165 NC R0402 PHY_RXD0 R166 4K7 R0402
Pull down for 2.5V RGMII(RTL8211D/8211E)
Pull up for 3.3V RGMII (RTL8211D/8211E)
Pull up1.5 /1.8V RGMII (RTL8211E-VL only)
REGOUT L9
2.2uH/1A
IND_3X3
Inductance close to PIN48
C260 10uF C0603
C261 0.1uF C0402
C262 0.1uF C0402
C263 0.1uF C0402
C264 0.1uF C0402
C265 0.1uF C0402
C266 0.1uF C0402
PHY_RXD0 R168 22R R0402 MAC_RXD0
PHY_RXD1 R169 22R R0402 MAC_RXD1
PHY_RXD2 R170 22R R0402 MAC_RXD2
PHY_RXD3 R171 22R R0402 MAC_RXD3
PHY_RXCLK R172 22R R0402 MAC_RXCLK
PHY_RXDV R173 22R R0402 MAC_RXDV
C269 NC C0402
Close to PHY
VDDREG
Close to PIN44.45
C270 4.7uF C0603
C271 0.1uF C0402
Rockchip 瑞芯微电子
福州瑞芯微电子有限公司
Title: 10/100/1000M-PHY
File: RK3288_BOX_Ref
REV:3.0
Create Date: Sunday, January 26, 2014
Page Num: 17
Modify Date: Tuesday, March 21, 2017
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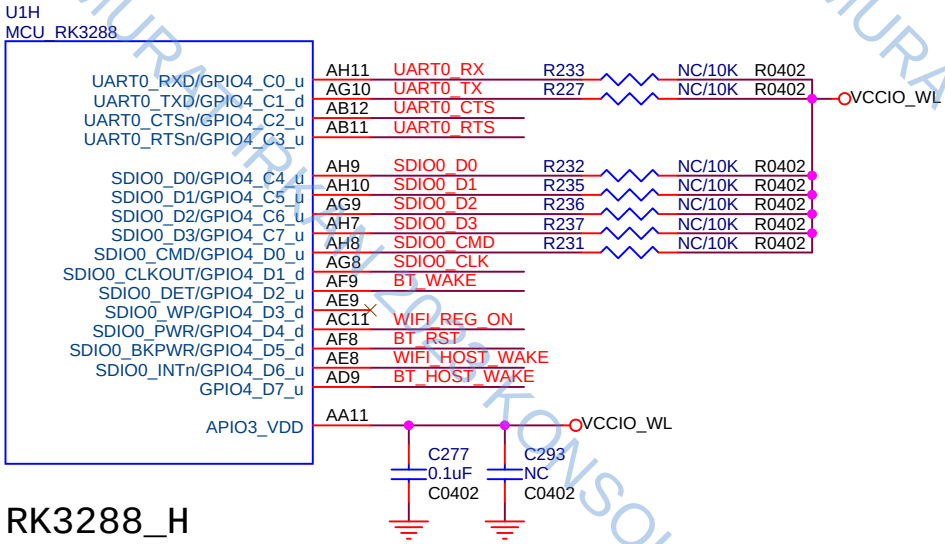
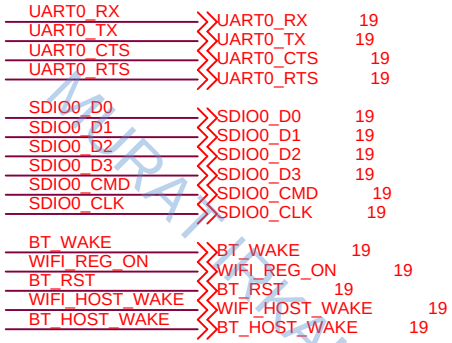


RK3288_H



Rockchip 瑞芯微电子 福州瑞芯微电子有限公司	
Title: RK3288 SDIO0 Controler	
File: RK3288_BOX_Ref	REV:3.0
Create Date: Monday, February 17, 2014	Page Num: 18
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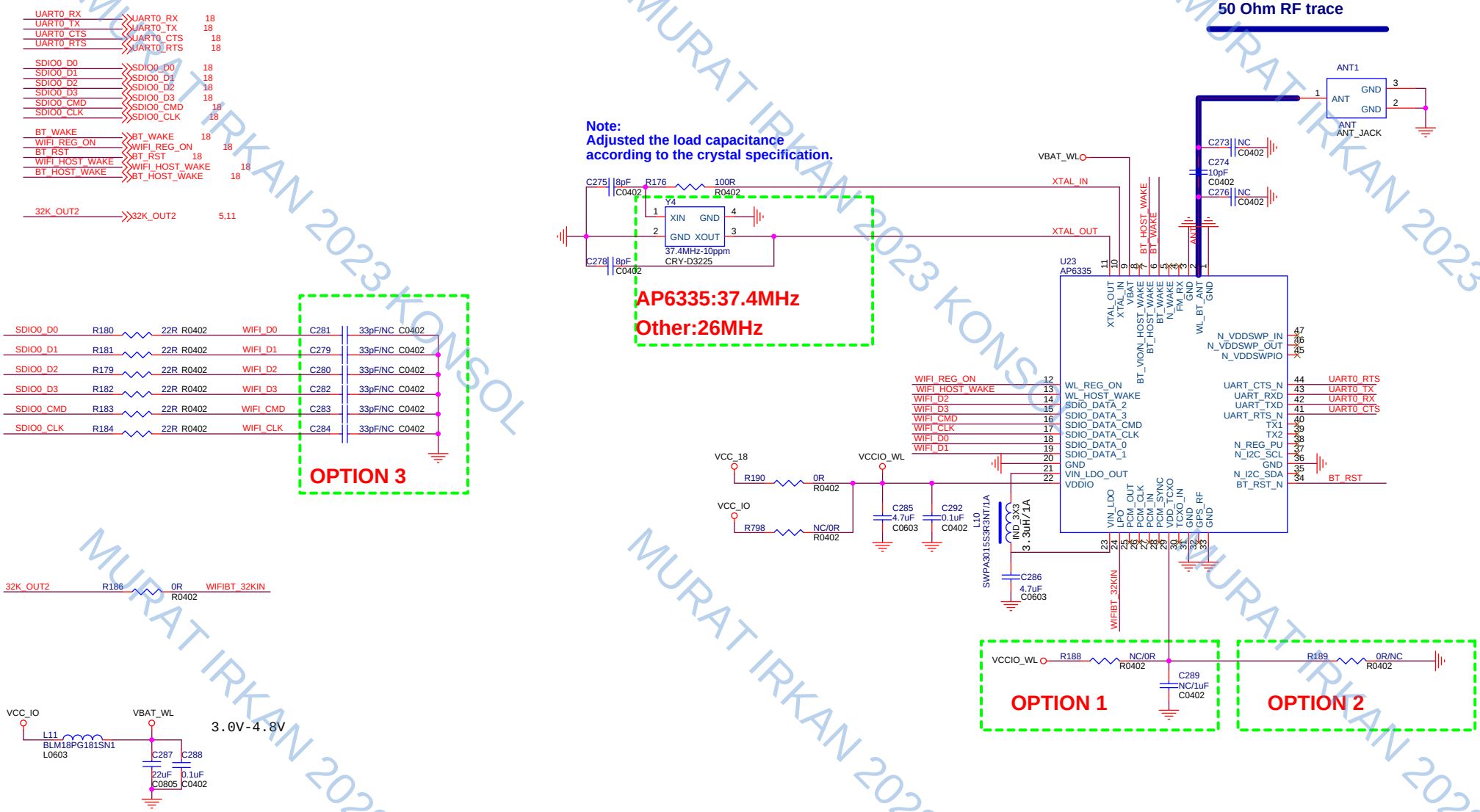

OPTION	WIFI				BT4.0	Crystals	VDDIO
	a	b/g/n	ac	5GHz			
AP6181		Yes				26MHz	1.71-3.6V
AP6212		Yes			Yes	26MHz	1.71-3.6V
XZ3538		Yes			Yes	26MHz	1.71-3.6V
XZ3660	Yes	Yes		Yes	Yes	26MHz	1.2-2.9V
AP6330	Yes	Yes		Yes	Yes	26MHz	1.2-2.9V
AP6335 (Default)	Yes	Yes	Yes	Yes	Yes	37.4MHz	1.71-3.63V

OPTION	1	2	3
AP6181	DNP	DNP	DNP
AP6212	DNP	DNP	DNP
XZ3538	DNP	DNP	DNP
XZ3660	DNP	DNP	DNP
AP6330	DNP	DNP	DNP
AP6335 (Default)	DNP	Paste	Paste

福州瑞芯微电子有限公司

Title: AP6XXX-WIFI/BT

File: RK3288_BOX_Ref REV:3.0

Create Date: Sunday, January 26, 2014 Page Num: 19

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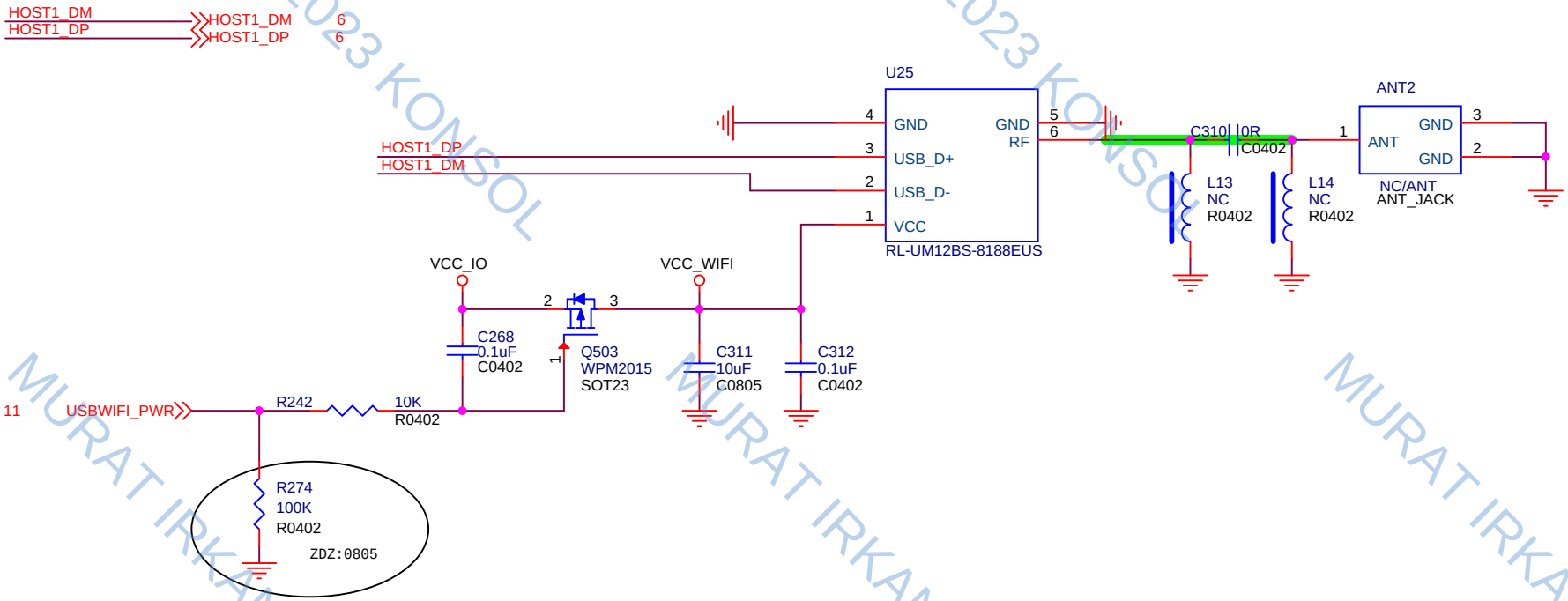

Rockchip 瑞芯微电子 福州瑞芯微电子有限公司	
Title: USB WIFI-(option)	
File: RK3288_BOX_Ref	REV:3.0
Create Date: Sunday, January 26, 2014	Page Num: 20
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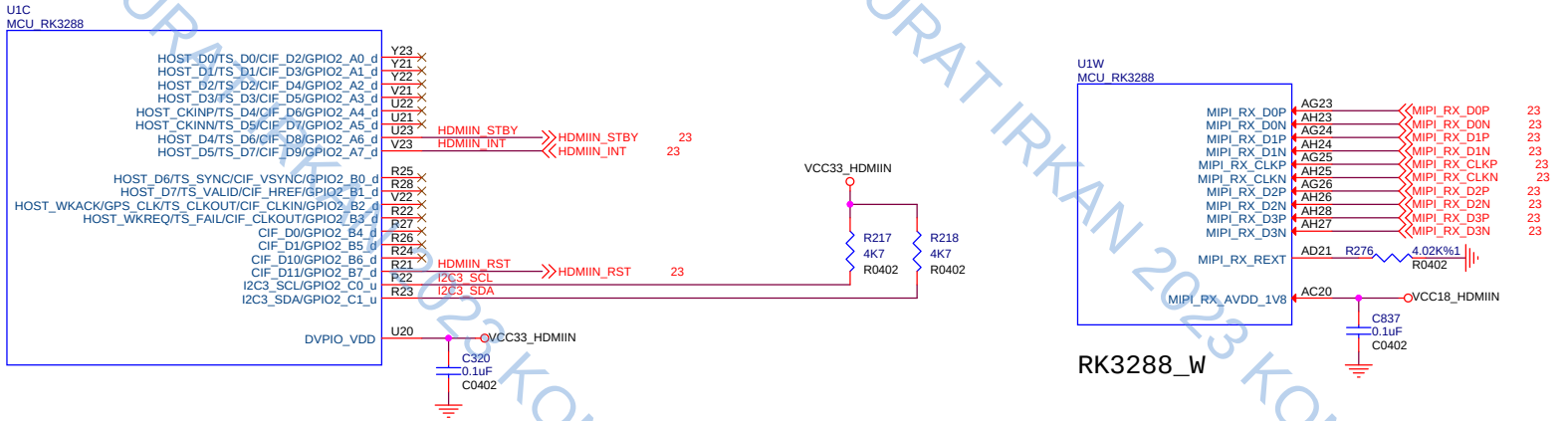

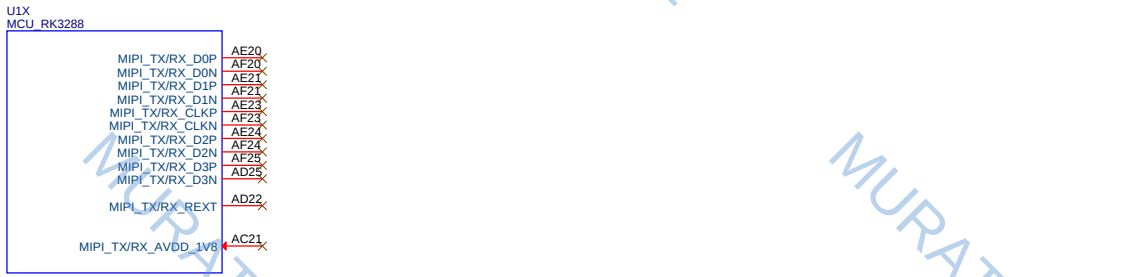

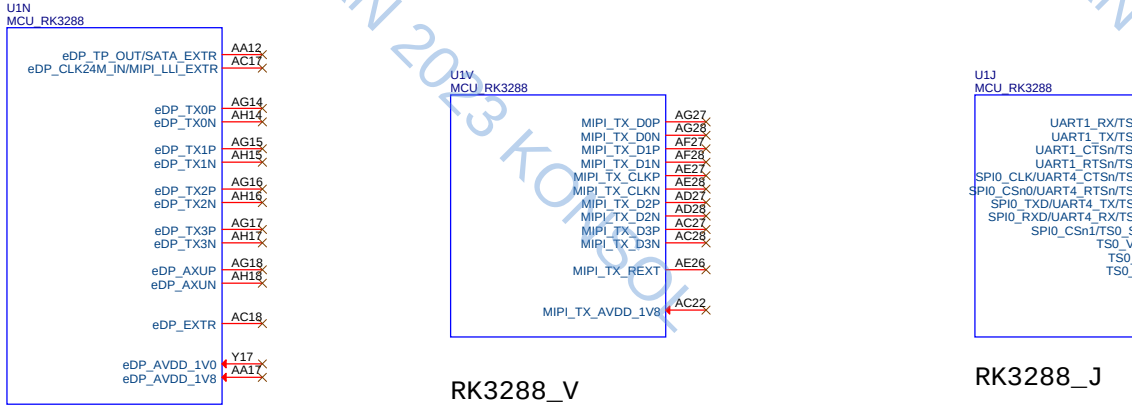

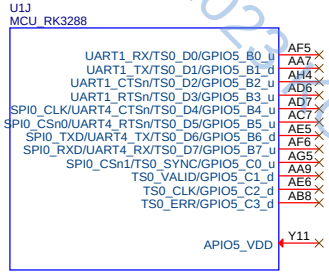

Rockchip 瑞芯微电子	福州瑞芯微电子有限公司	
Title: MIPI Interface		
File: RK3288_BOX_Ref		REV:3.0
Create Date: Sunday, January 26, 2014		Page Num: 21
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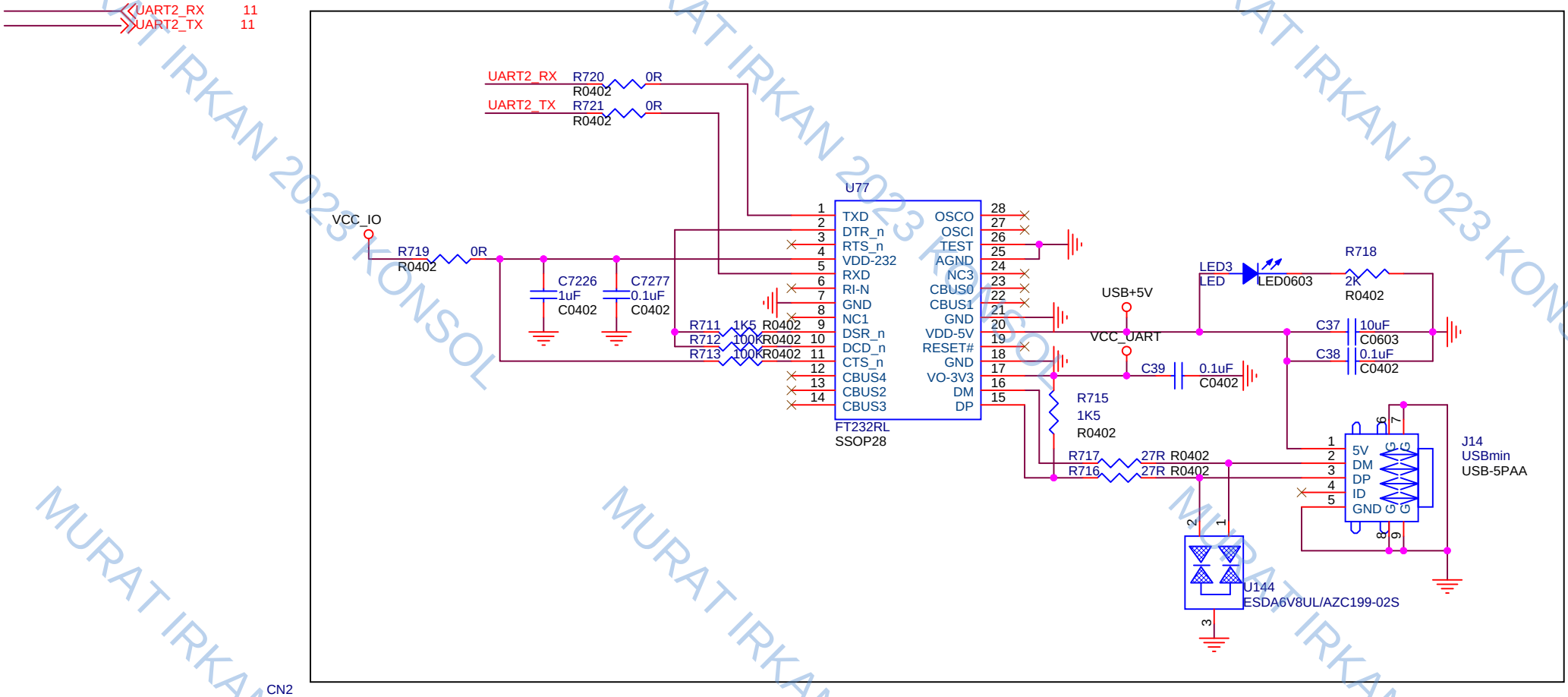

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Title: UART Debug	
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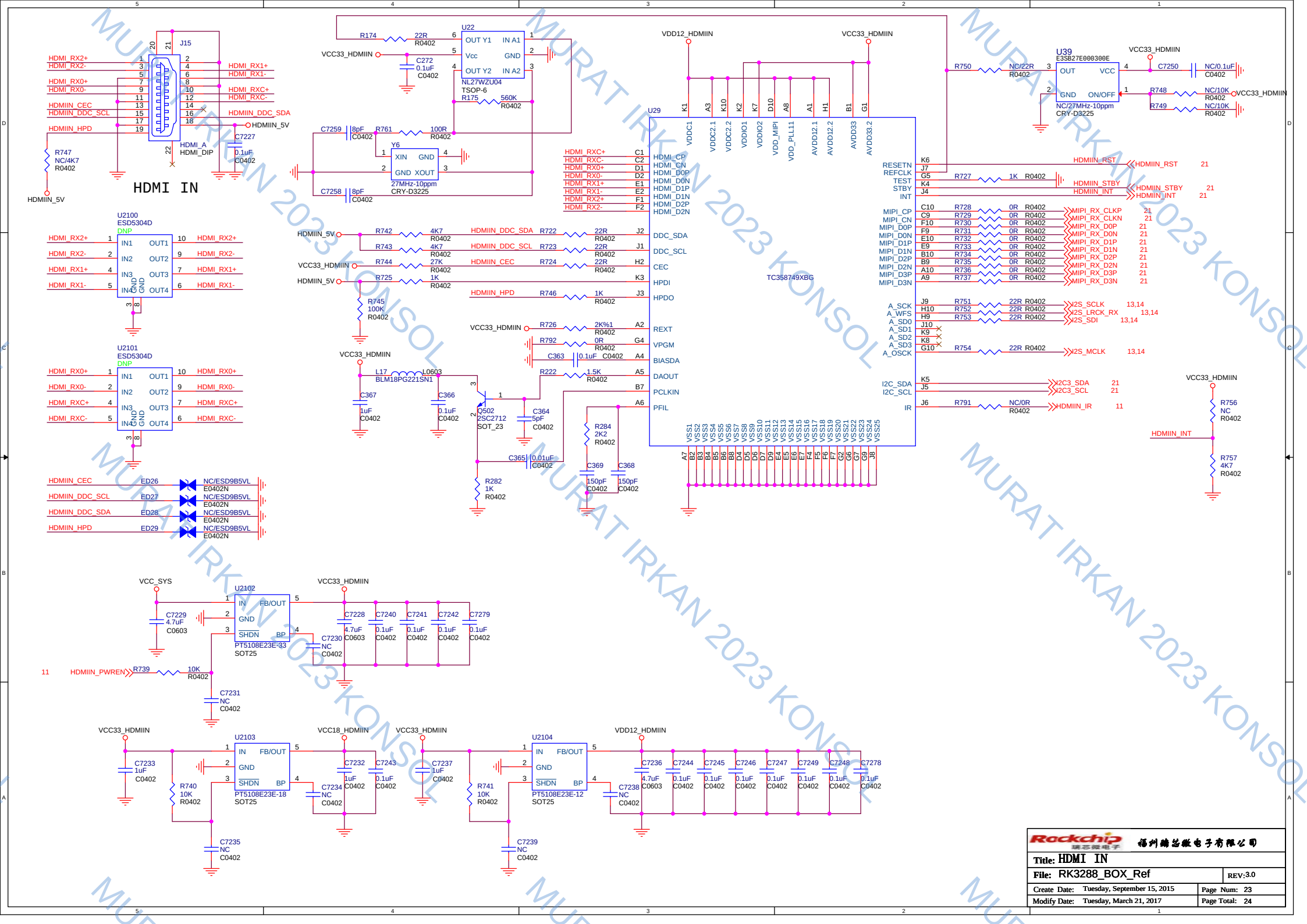
HDMI IN
U29
TC358749XBG
U22
NL27WZU04
TSOP-6
U39
E3SB27E000300E
J15
HDMI_A
HDMI_DIP
U2100
ESD5304D
DNP
U2101
ESD5304D
DNP
U2102
PT5108E23E-33
SOT25
U2103
PT5108E23E-18
SOT25
U2104
PT5108E23E-12
SOT25
Y6
27MHz-10ppm
CRY-D3225
NC/27MHz-10ppm
CRY-D3225
Q502
2SC2712
SOT_23
VDD12_HDMIIN
VCC33_HDMIIN
VCC_SYS
VCC18_HDMIIN
HDMIIN_5V
HDMIIN_PWREN
HDMIIN_RST
HDMIIN_STBY
HDMIIN_INT
HDMIIN_IR
HDMIIN_CEC
HDMIIN_DDC_SCL
HDMIIN_DDC_SDA
HDMIIN_HPD
HDMI_RX2+
HDMI_RX2-
HDMI_RX1+
HDMI_RX1-
HDMI_RX0+
HDMI_RX0-
HDMI_RXC+
HDMI_RXC-
MIPI_RX_CLKP
MIPI_RX_CLKN
MIPI_RX_D0P
MIPI_RX_D0N
MIPI_RX_D1P
MIPI_RX_D1N
MIPI_RX_D2P
MIPI_RX_D2N
MIPI_RX_D3P
MIPI_RX_D3N
I2S_SCLK
I2S_LRCK_RX
I2S_SDI
I2S_MCLK
I2C3_SDA
I2C3_SCL
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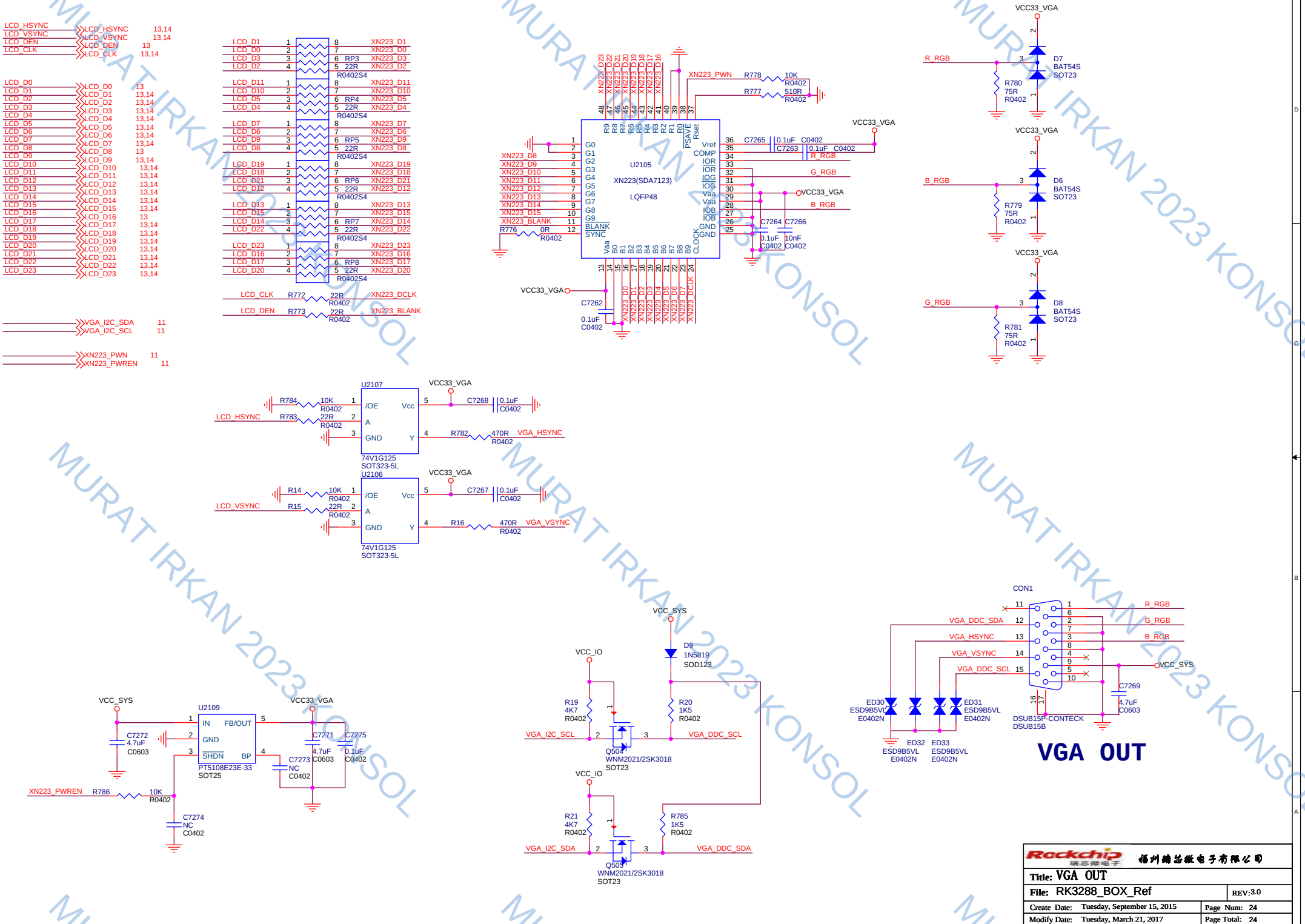
LCD_HSYNC 13,14
LCD_VSYNC 13,14
LCD_DEN 13
LCD_CLK 13,14
LCD_D0 13
LCD_D1 13,14
LCD_D2 13,14
LCD_D3 13,14
LCD_D4 13,14
LCD_D5 13,14
LCD_D6 13,14
LCD_D7 13,14
LCD_D8 13
LCD_D9 13,14
LCD_D10 13,14
LCD_D11 13,14
LCD_D12 13,14
LCD_D13 13,14
LCD_D14 13,14
LCD_D15 13,14
LCD_D16 13
LCD_D17 13,14
LCD_D18 13,14
LCD_D19 13,14
LCD_D20 13,14
LCD_D21 13,14
LCD_D22 13,14
LCD_D23 13,14
VGA_I2C_SDA 11
VGA_I2C_SCL 11
XN223_PWN 11
XN223_PWREN 11
R772 22R R0402
R773 22R R0402
U2105
XN223(SDA7123)
LQFP48
XN223_PWN
R778 10K R0402
R777 510R R0402
R776 0R R0402
C7262 0.1uF C0402
C7265 0.1uF C0402
C7263 0.1uF C0402
C7264 0.1uF C0402
C7266 10nF C0402
VCC33_VGA
R_RGB
G_RGB
B_RGB
D7 BAT54S SOT23
D6 BAT54S SOT23
D8 BAT54S SOT23
R780 75R R0402
R779 75R R0402
R781 75R R0402
U2107
U2106
74V1G125 SOT323-5L
R784 10K R0402
R783 22R R0402
R782 470R R0402
R14 10K R0402
R15 22R R0402
R16 470R R0402
C7268 0.1uF C0402
C7267 0.1uF C0402
VGA_HSYNC
VGA_VSYNC
U2109
PT5108E23E-33 SOT25
VCC_SYS
C7272 4.7uF C0603
C7271 4.7uF C0603
C7275 0.1uF C0402
C7273 NC C0402
C7274 NC C0402
R786 10K R0402
D9 1N5819 SOD123
R19 4K7 R0402
R20 1K5 R0402
R21 4K7 R0402
R785 1K5 R0402
Q504 WNM2021/2SK3018 SOT23
Q505 WNM2021/2SK3018 SOT23
VGA_I2C_SCL
VGA_I2C_SDA
VGA_DDC_SCL
VGA_DDC_SDA
CON1
DSUB15P-CONTECK DSUB15B
ED30 ESD9B5VL E0402N
ED31 ESD9B5VL E0402N
ED32 ESD9B5VL E0402N
ED33 ESD9B5VL E0402N
C7269 4.7uF C0603
VGA OUT
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