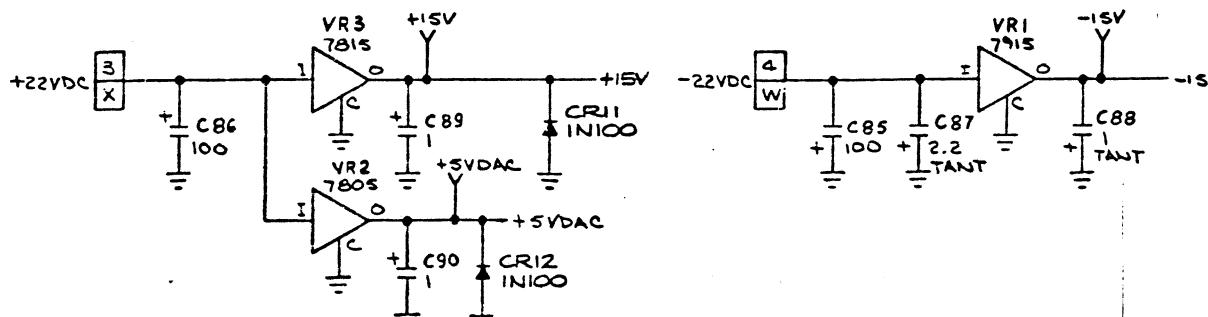
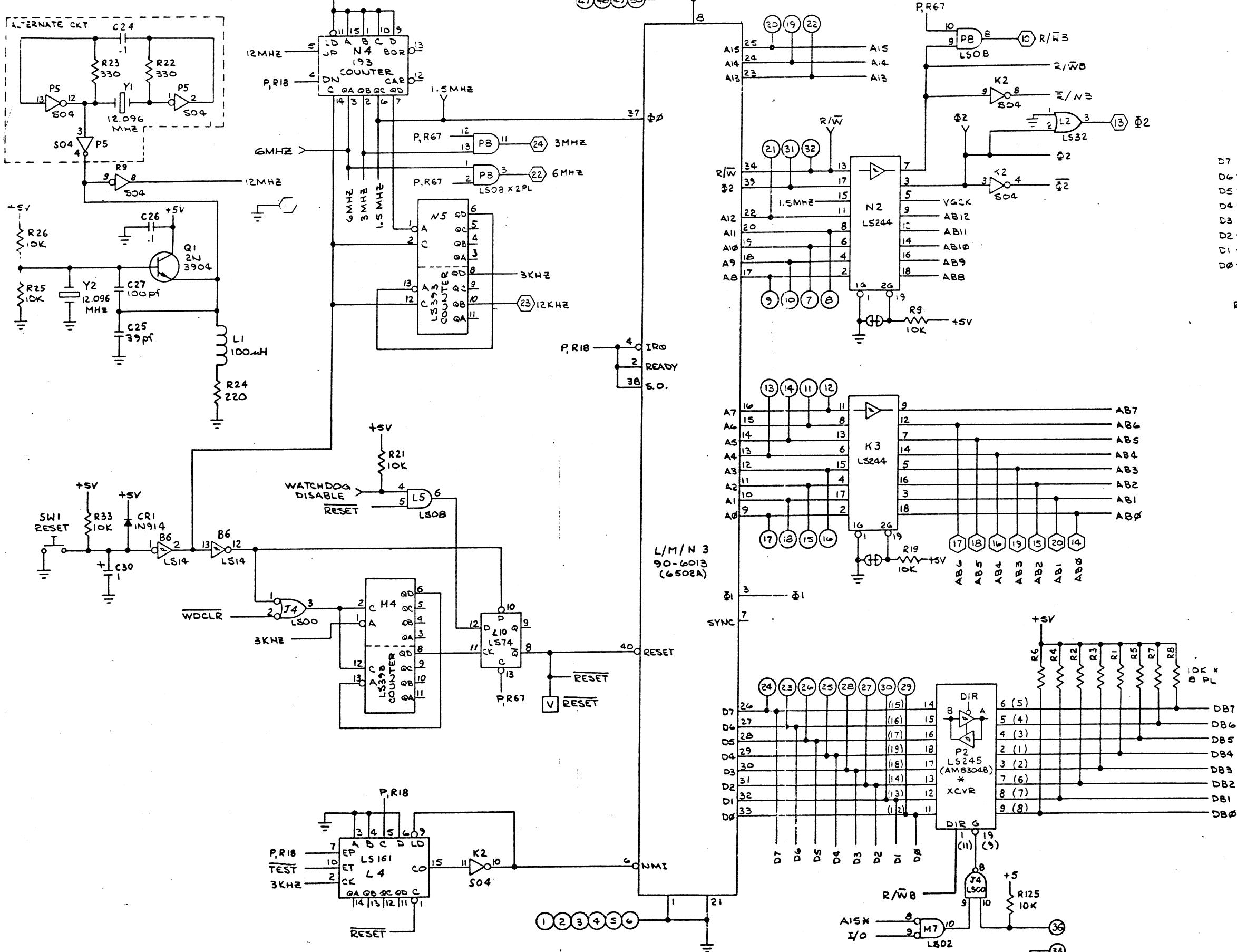


IC CHART

TYPE	VOLTAGE / PIN NO.							UNUSED GATES
	GND	+5V	+5V DAC	+12V	+15V	-5V	-5V	
74S03	7	14						
74LS00	7	14						
74S02	7	14						
74LS02	7	14						
74S04	7	14						P5, P5, P5,
74LS04	7	14						
74LS06	7	14						
74LS08	7	14						
74LS12	7	14						K4
74LS11	7	14						
74LS14	7	14						
74LS22	7	14						M5
74LS27	7	14						
74LS32	7	14						L2
74LS42	8	16						
74S74	7	14						
74LS74	7	14						
74S86	7	14						B7
74LS86	7	14						
74LS129	8	16						
74LS139	8	16						
74LS157	8	16						
74LS161	8	16						
74LS164	7	14						
74LS174	8	16						
74LS175	8	16						
74LS191	8	16						
74193	8	16						
74LS193	8	16						
74LS194	8	16						
74LS244	10	20						
74LS245	10	20						
74S260	7	14						
74LS273	10	20						
74LS367	8	16						
74LS393	7	14						
74LS670	8	16						
2114 (RAM)	9	18						
8316E (ROM)	12	21,24	19					
2708 (ZOM)	12	24						21
823124 (PROM)	8	16						
6502A (MICROPROCESSOR)	1,21	8						
AM6012	13	20						17
MC3410	2	14						1
DAC-08		13						3
LM319	3,2							11 6
TL082								8 4
TL084								4 11
LF13201	5							13 4
14050	8	1						
7404	7	14						HII, HII, HII, HII
74LS399	8	16						



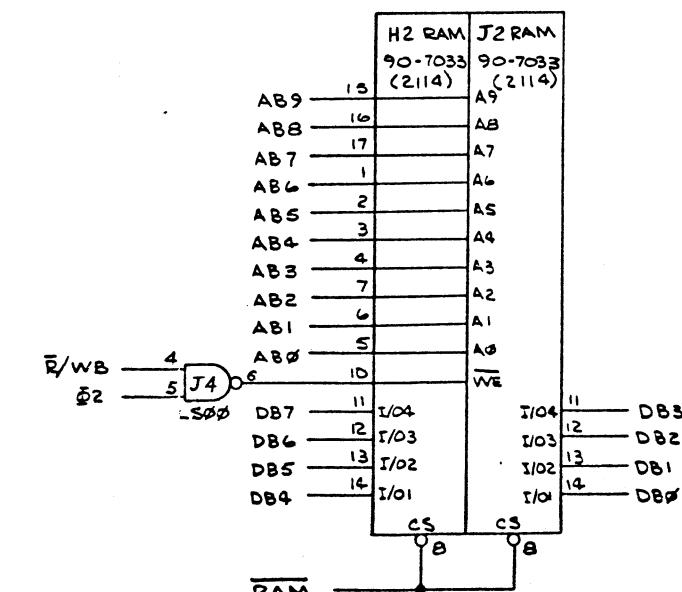
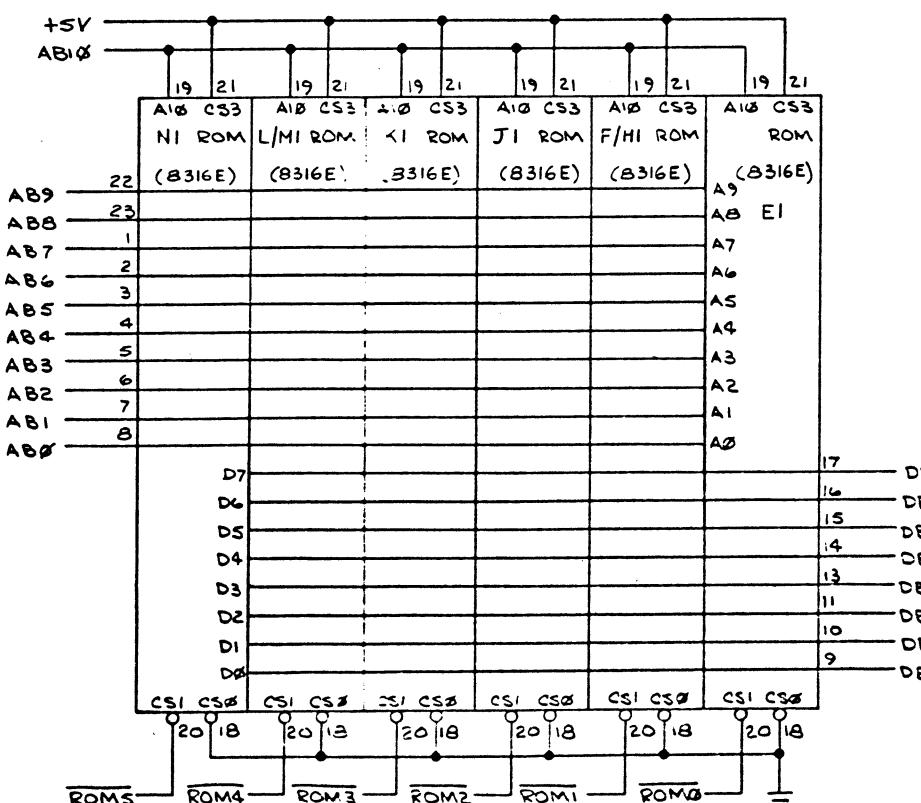
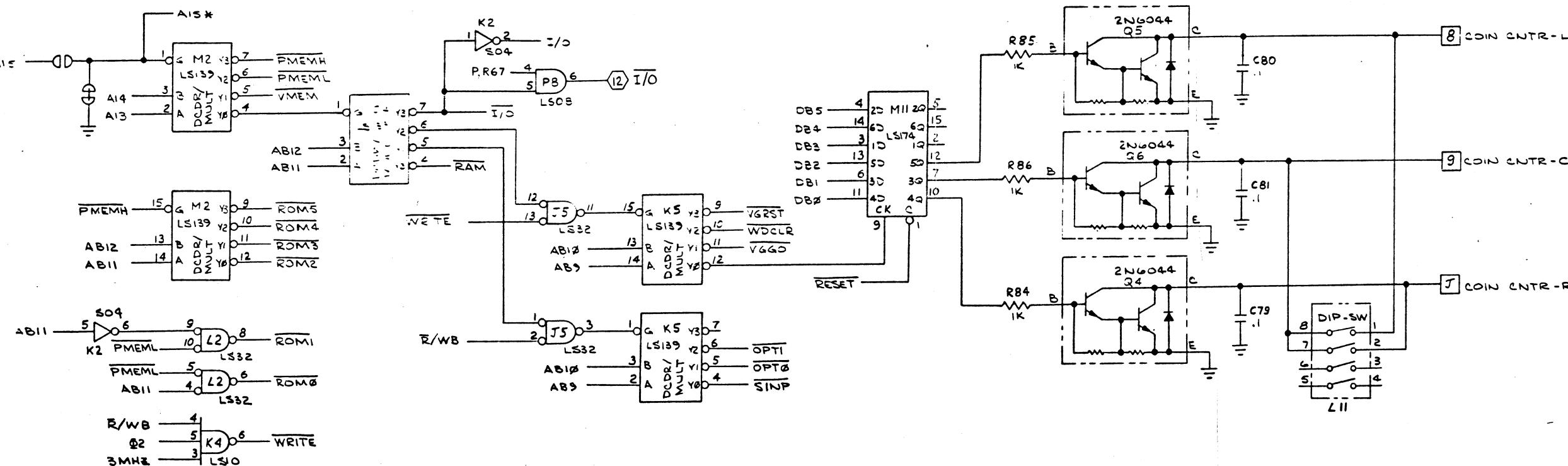
REVISIONS				
ON	DATE	INCORP	CHECK	APPROVED
VE				



NOTE:
* USE NUMBERS IN PARENTHESES IF
BOARD CONTAINS AM8304B
INSTEAD OF 74LS245, 2 PLACES
THIS PAGE.

The image shows a schematic diagram for an Atari 6502A Analog Vector Generator. The diagram is titled 'SCHEMATIC, 6502A ANALOG VECTOR GENERATOR P.C. BD'. It features several circuit boards labeled A through F. Board A contains a central microprocessor and memory chips. Board B includes a 741 operational amplifier. Board C has a 7414 integrated circuit. Board D contains a 7413 integrated circuit. Board E includes a 7412 integrated circuit. Board F contains a 7410 integrated circuit. Various resistors, capacitors, and other electronic components are interconnected between the boards.

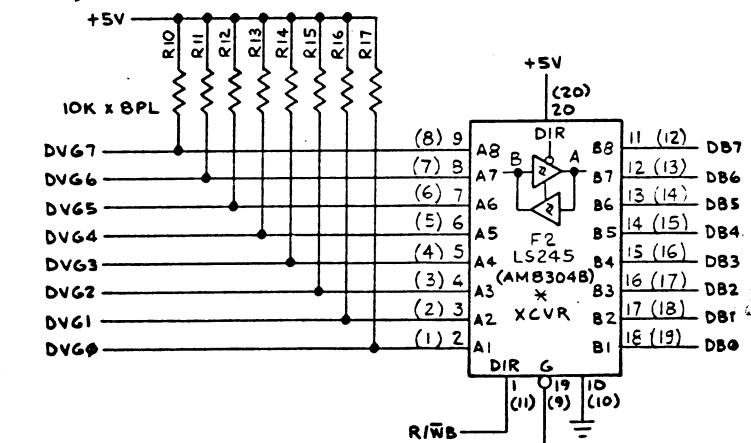
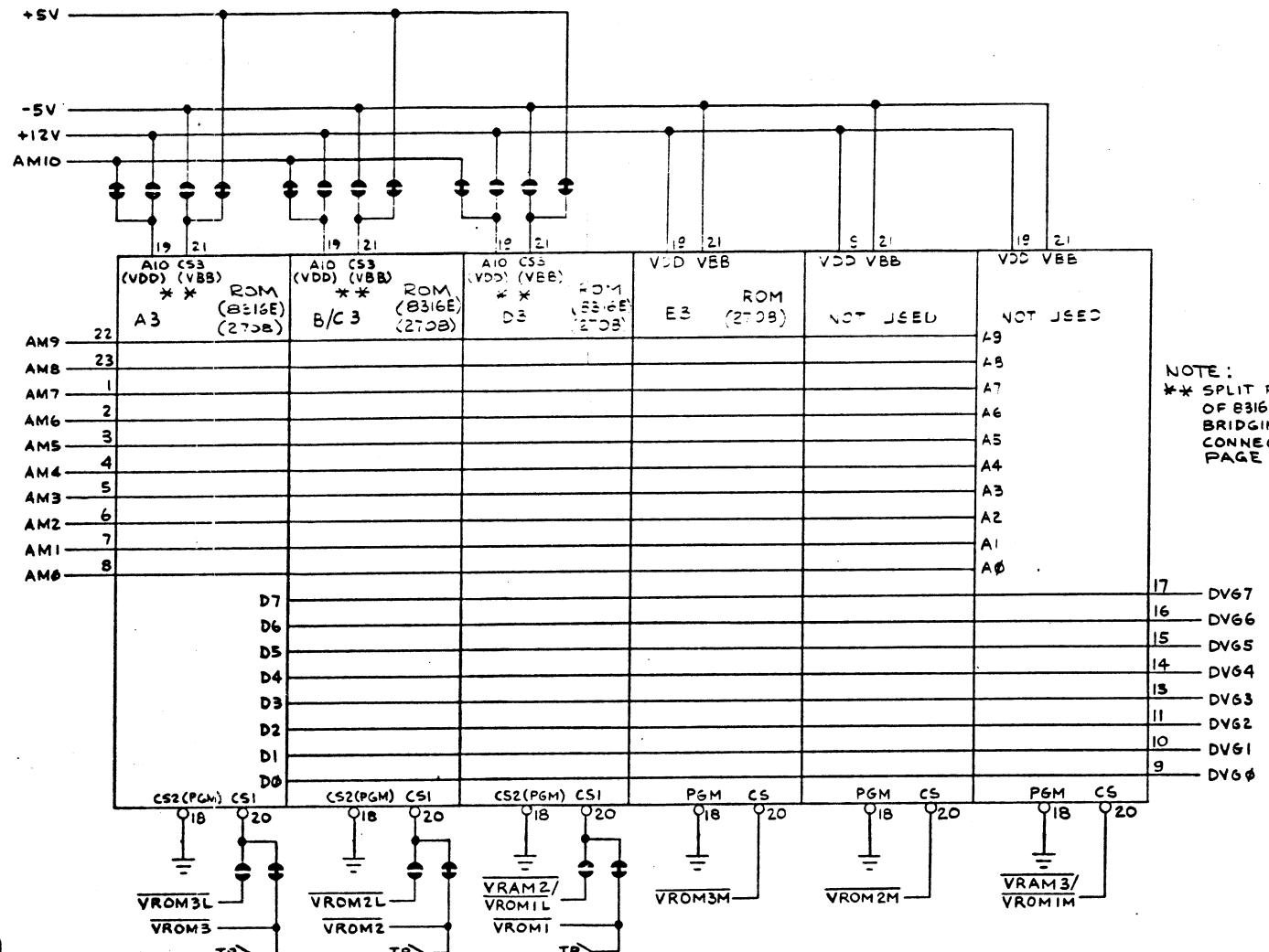
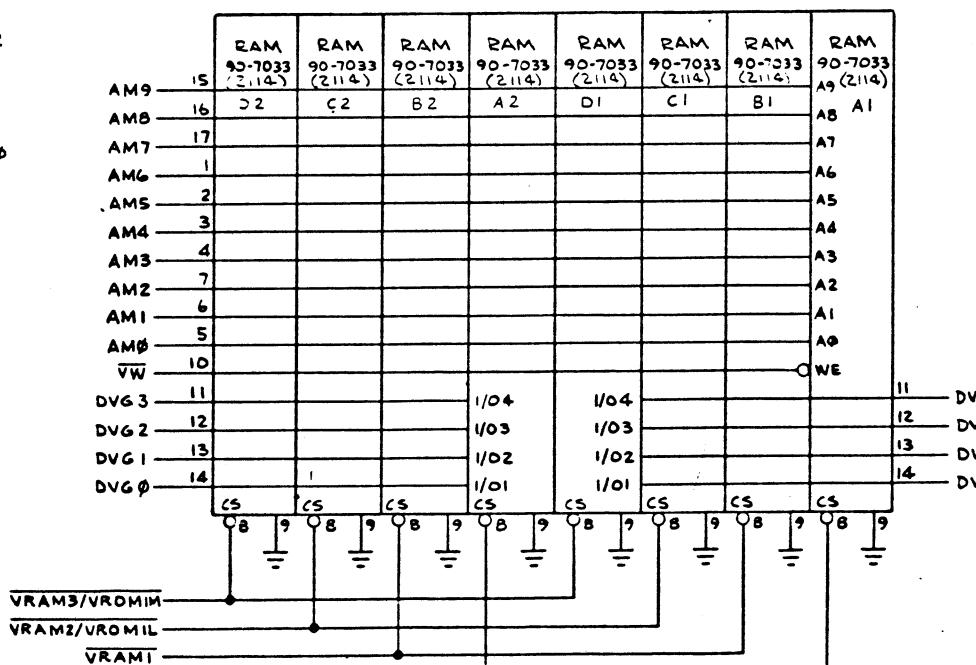
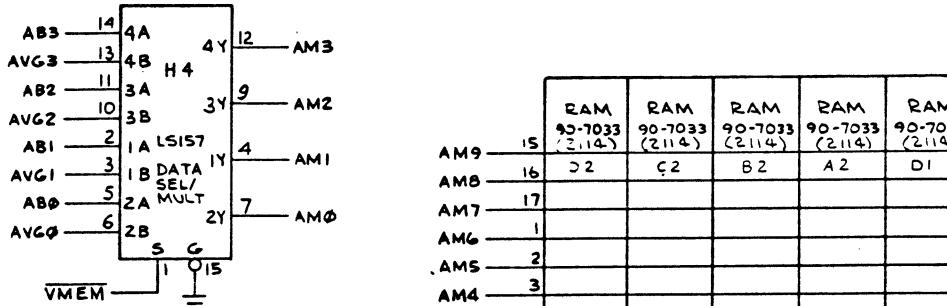
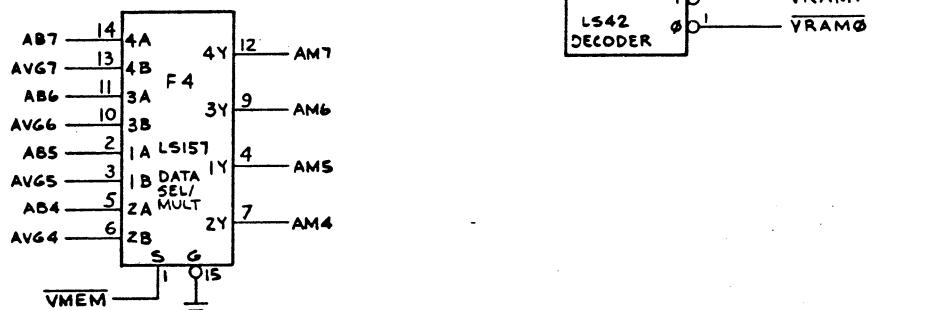
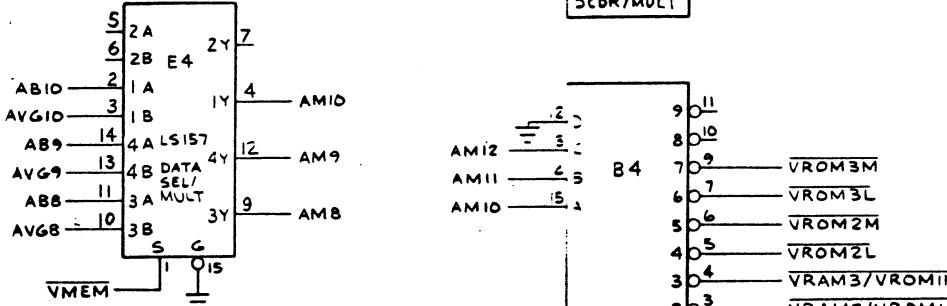
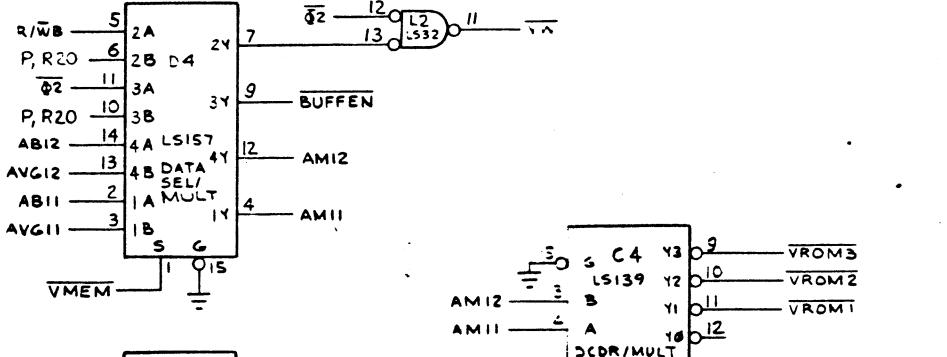
SYM	DESCRIPTION	DATE	INCORP	CHECK	APPROVED
- SEE SHEET ONE					



DECODE, COIN DRIVER, MEMORY

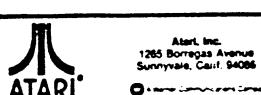
Atari, Inc. 1265 Boregas Avenue Sunnyvale, Calif. 94088	
ATARI	
TITLE	SCHEMATIC,
6502A ANALOG VECTOR	
GENERATOR P.C. BD.	
SIZE	DRAWING NO
D	035742-XX
REV	B
SCALE	—
SHEET 3 OF 7	

REVISIONS				
	DATE	INCORP	CHECK	APPROVED
				.

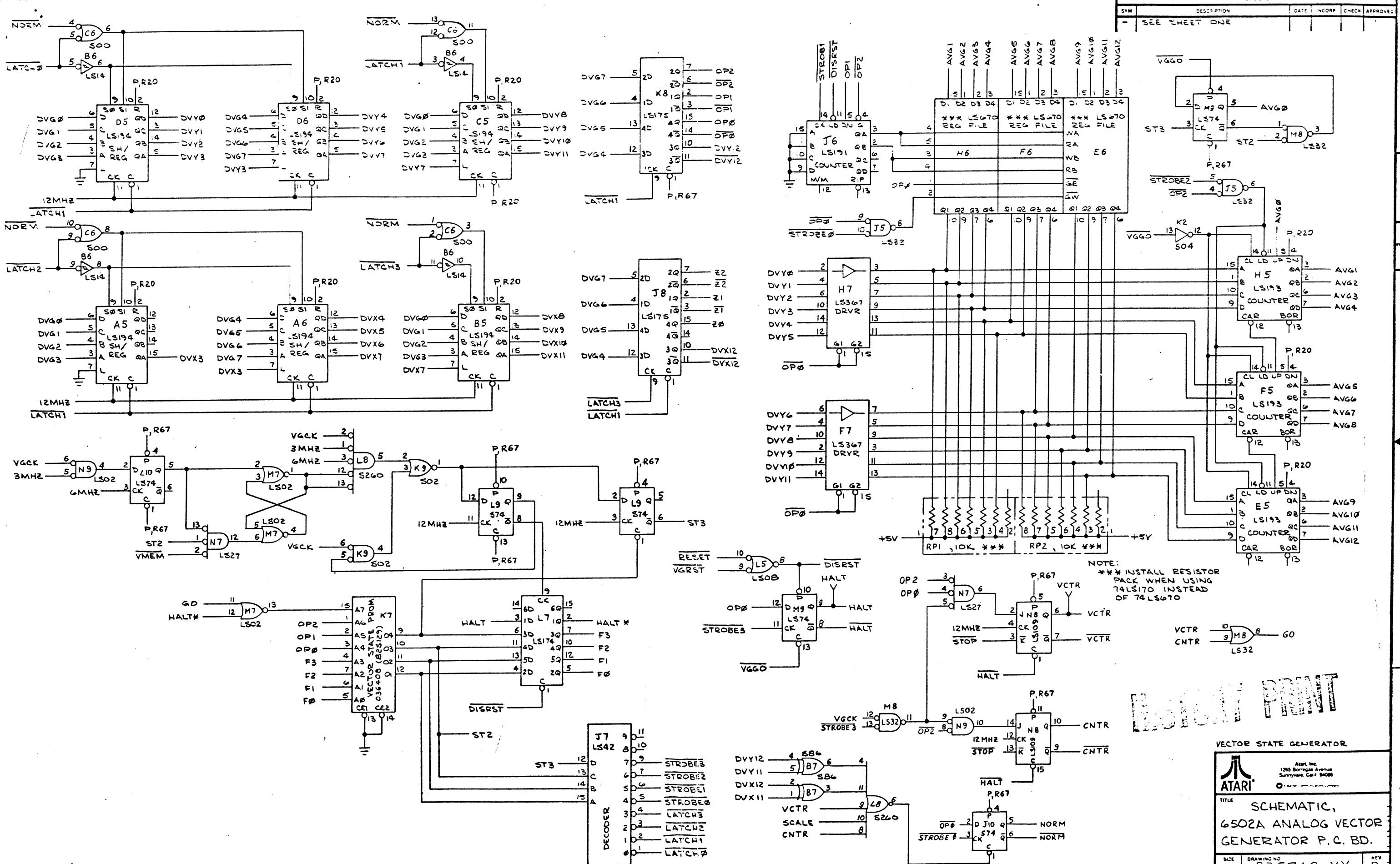


NOTE: * USE NUMBERS IN PARENTHESSES IF BOARD CONTAINS AMB8304B INSTEAD OF 7415245

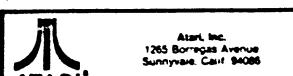
VECTOR MUX, VECTOR MEM



TITLE		SCHEMATIC, 6502A ANALOG VECTOR GENERATOR P.C. BD.	
SIZE	DRAWING NO	REV	
D	035742-XX	B	

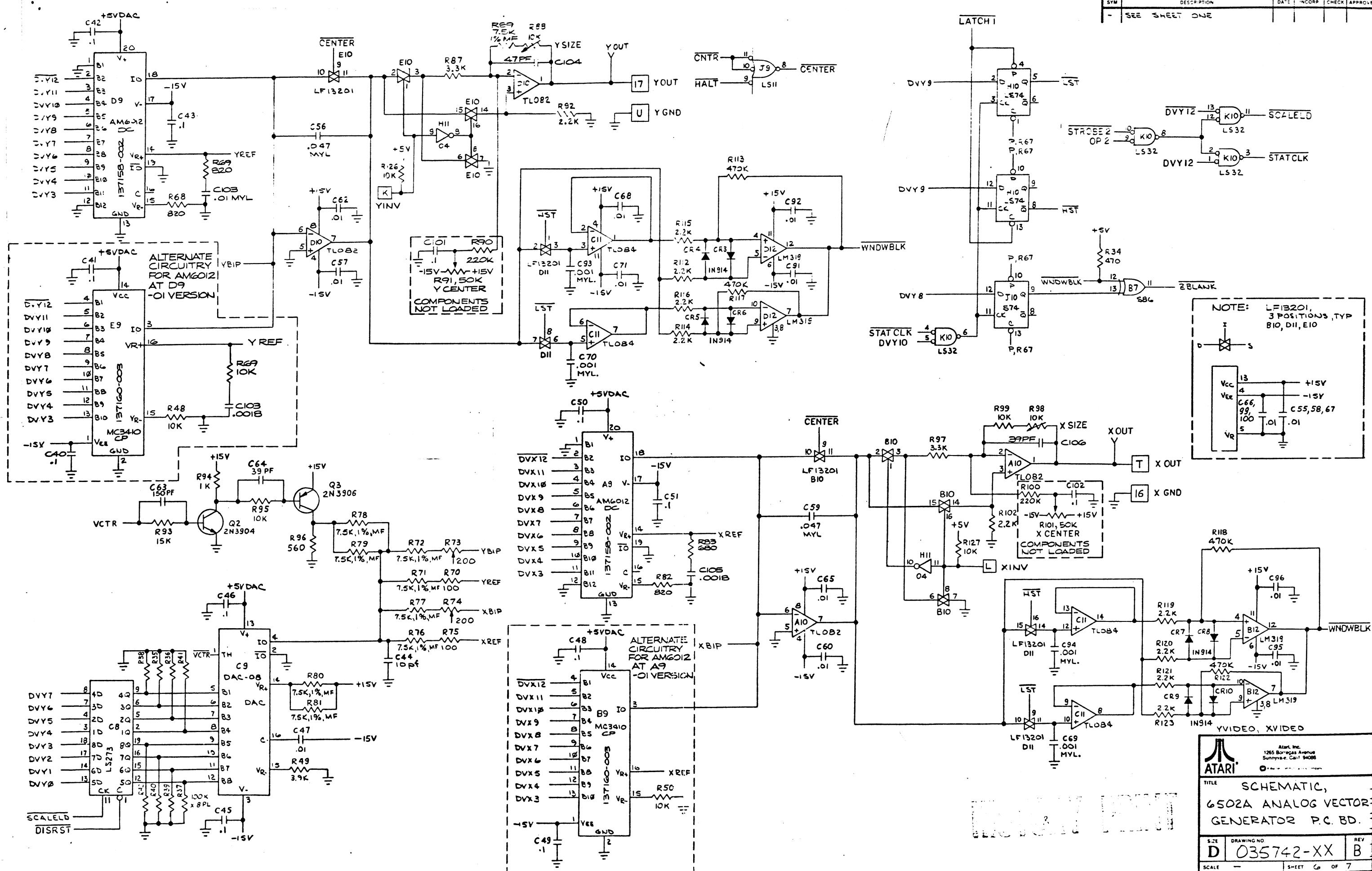


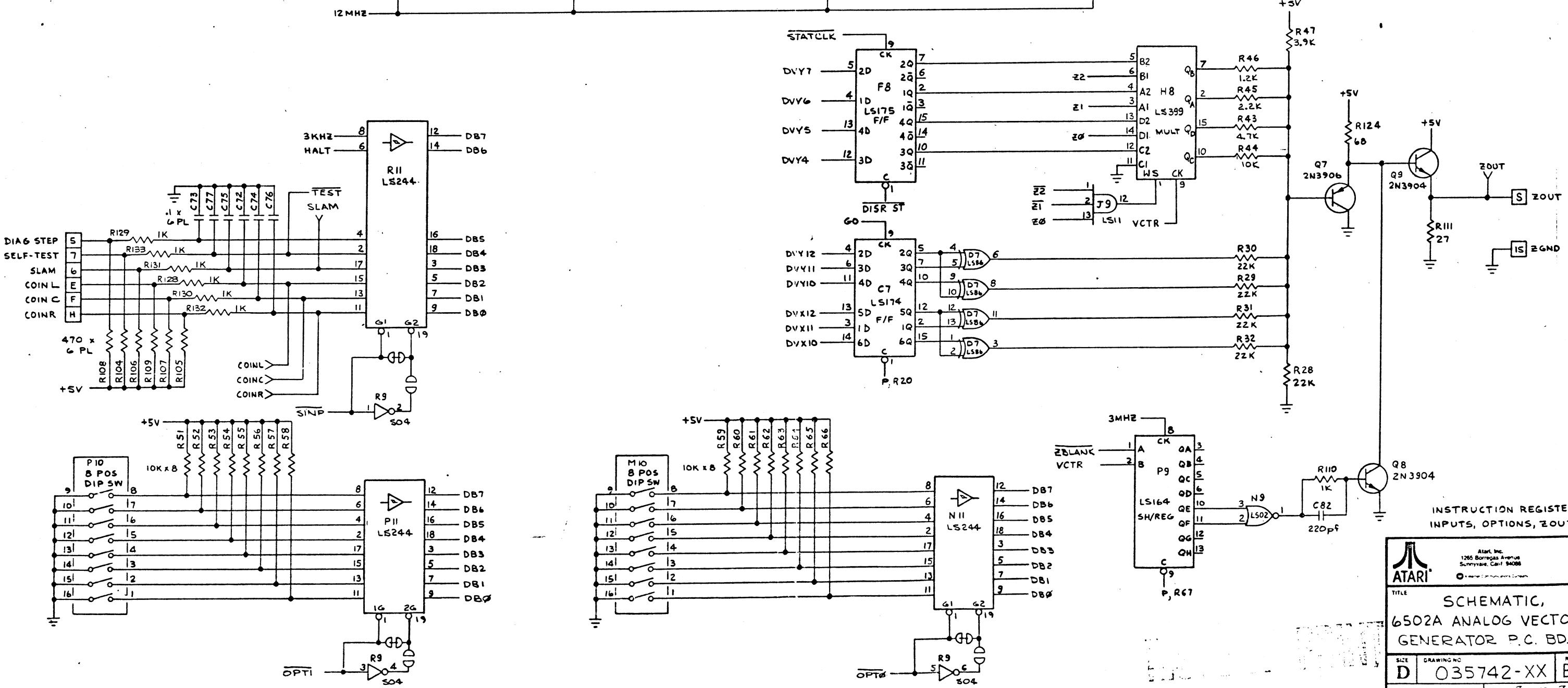
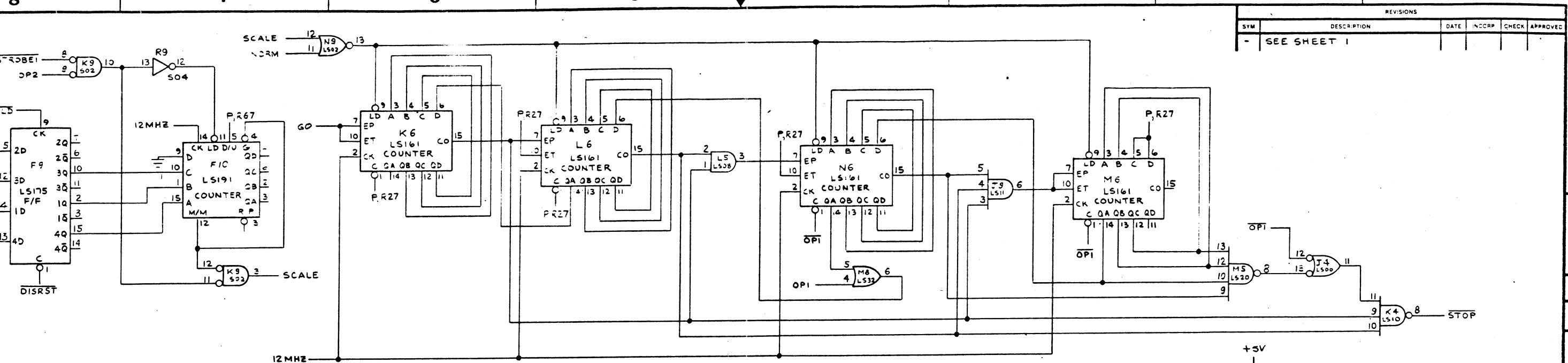
VECTOR STATE GENERATOR

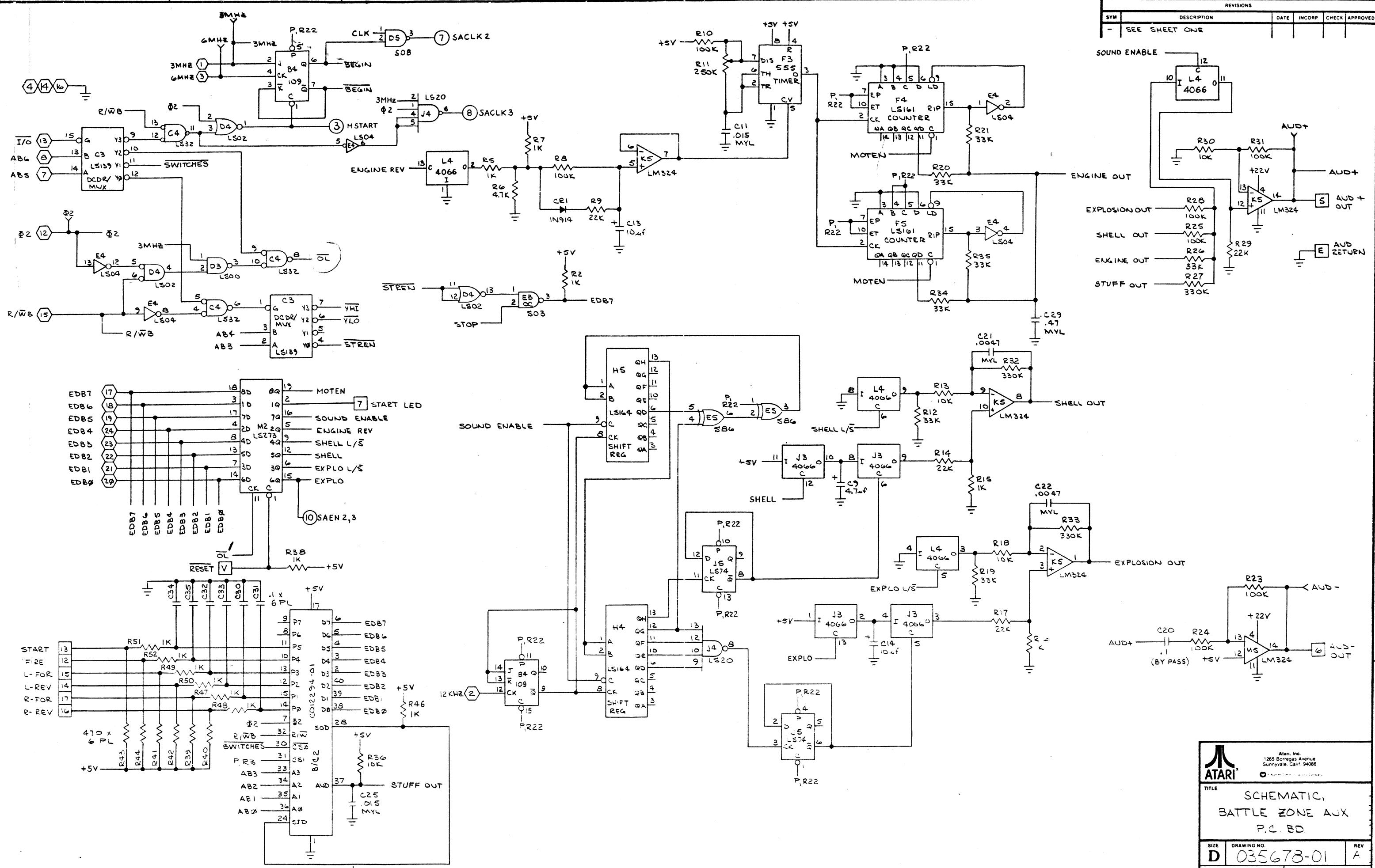


The image shows the Atari logo on the left, which consists of a stylized 'A' shape with horizontal bars extending from its sides. To the right of the logo is the company's address: "Atari, Inc." on top, followed by "1265 Borregas Avenue" and "Sunnyvale, Calif. 94086" stacked below it. There is also a small circular graphic element between the logo and the address.

REVISIONS			
SYM	DESCRIPTION	DATE	INCORP
- SEE SHEET ONE			

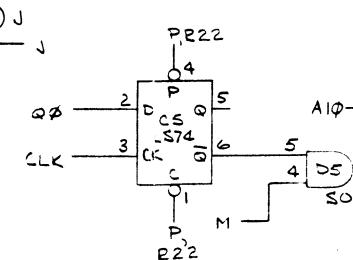
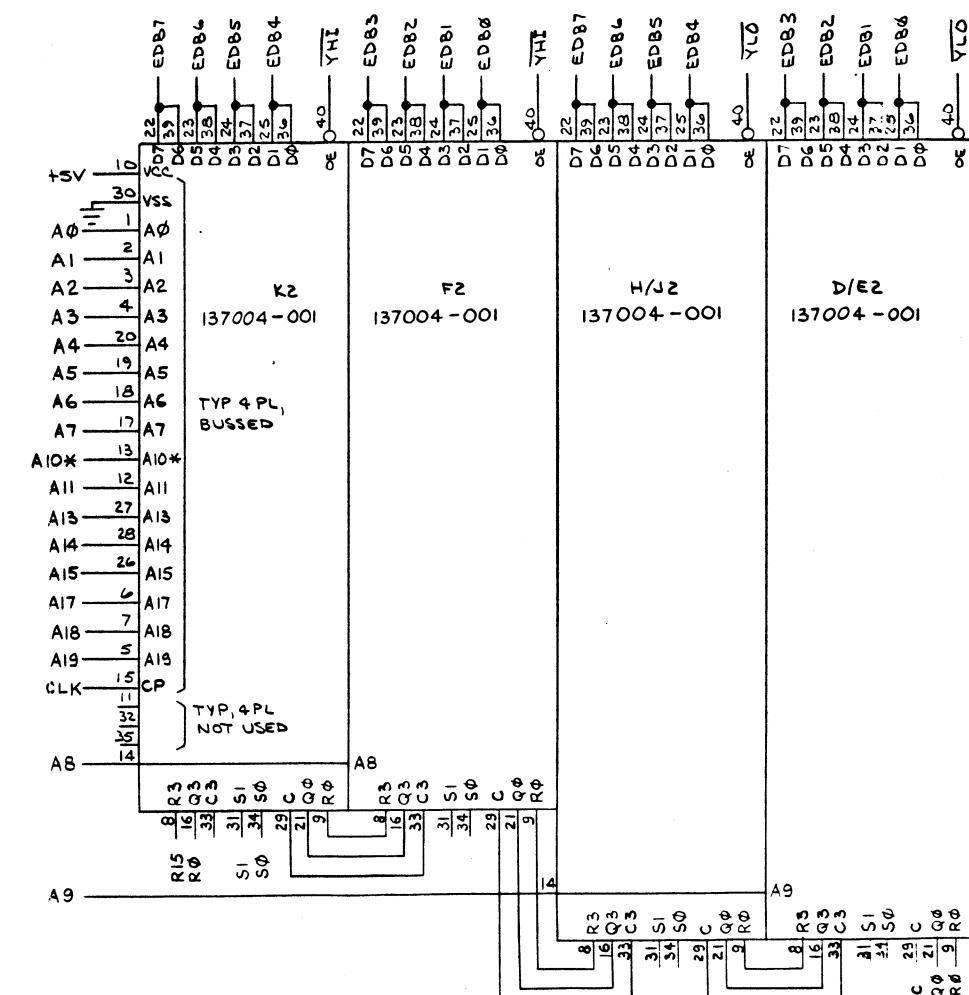
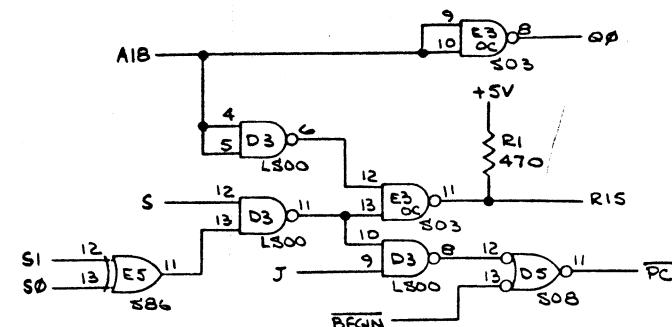
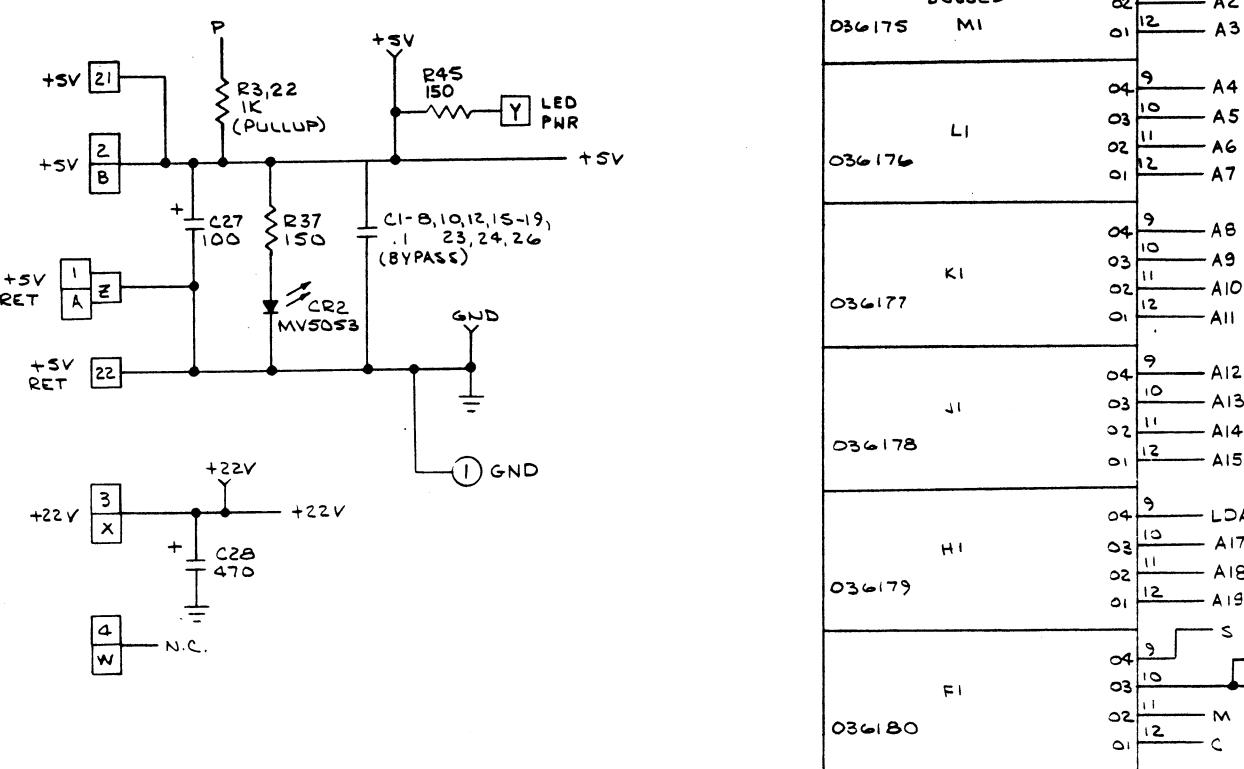
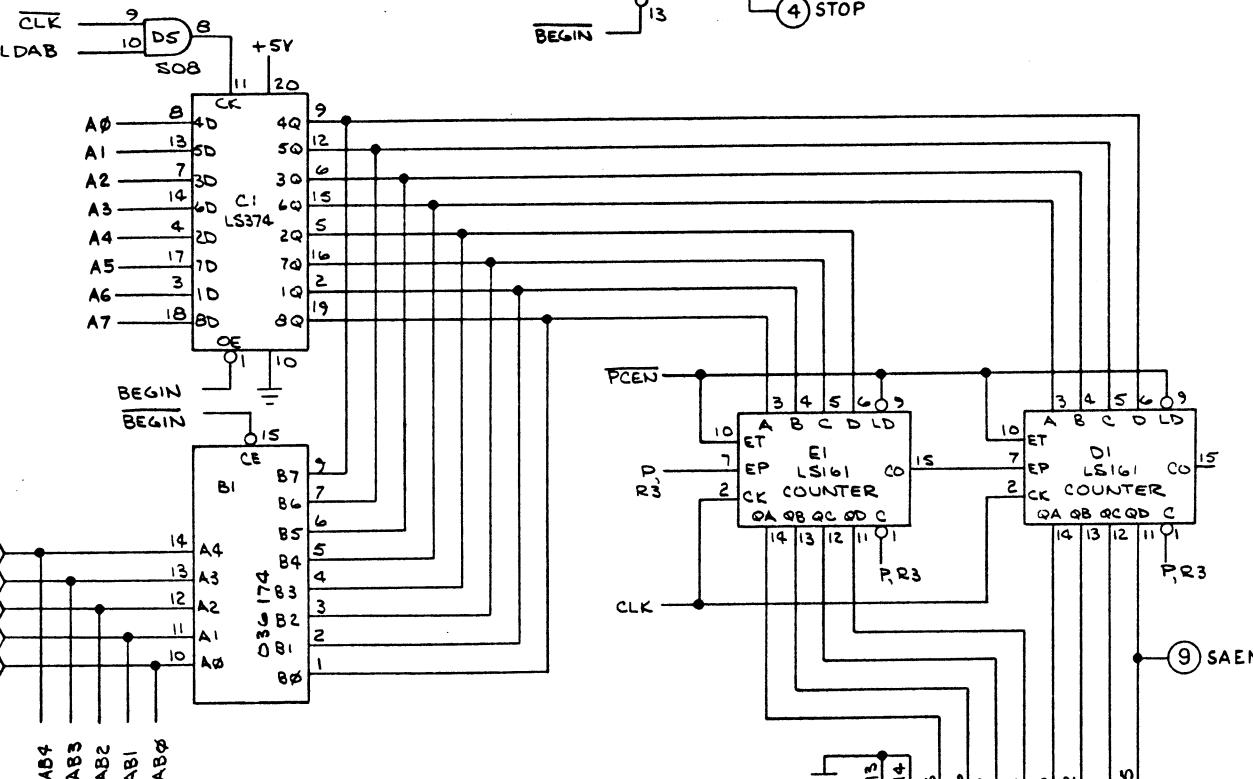
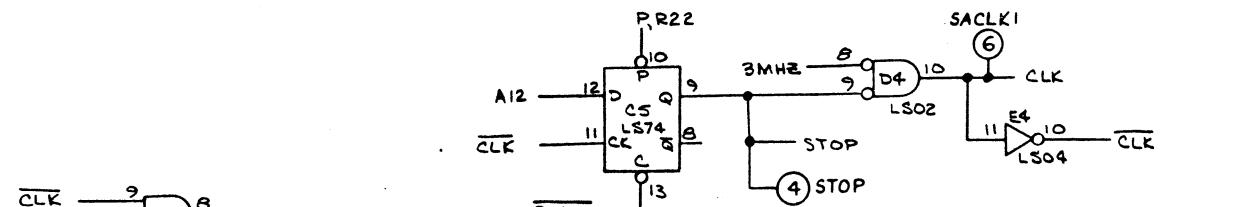






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SYM	DESCRIPTION	DATE	INCORP	CHECK	APPROVED
-	SEE SHEET ONE				



AD35678	BATTLE ZONE	DO NOT SCALE DRAWING	DRAWN BY <i>J. Puth</i>	DATE 10/17/79
NEXT ASSY	FIRST USED ON		CHECKED BY	DATE
OUTSTANDING ECNs		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON: ANGLES ± ¹⁰ ' AXES ± ^{0.05} SURFACE FINISH / ± ^{0.005}	ENGR. MECH	DATE
		MATERIAL:	ENGR. ELECT	DATE
		INDUSTRIAL DESIGN	DATE	
		HARNESS DESIGN	DATE	
		PROJECT ENGR	DATE	
		MFG ENGR	DATE	
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 Atari, Inc. 1265 Borregas Avenue Sunnyvale, Calif. 94086 <small>© A Atari Company 1979</small>				
SCHEMATIC, BATTLE ZONE AUX P.C. BD.				
SIZE D	DRAWING NO. 035678-01	REV A		
		SCALE —	SHEET 2 OF 3	

IC CHART

VOLTAGE / PIN NO.

TYPE	UNUSED GATES						
	GND	+5V	+22V	+12V	+15V	-15V	-5V
74LS00	7	14					
74LS02	7	14					
74S03	7	14					E3
74LS04	7	14					
7407	7	14					
74S08	7	14					
74LS20	7	14					
74LS32	7	14					C4
74LS74	7	14					
74S86	7	14					
74109	8	16					
74LS139	8	16					
74LS161	8	16					
74LS164	7	14					
74LS273	10	20					
74LS374	10	20					
LM324	11	4					MS, M5, M5
555	1	8					
4066	7	14					
CO1294 - 01	1	17					
137004 - 001	30	10					
825123 (PROM)	8	16					
825129 (PROM)	8	16					

MEMORY MAP

ADDRESS	DATA	FUNCTION
1XXXXXX	111111	BOARD NOT ENABLED
0111111	1111111	M BOX STATUS
1111111	1111111	M BOX READ LOW
0111111	1111111	M BOX READ HI
1111111	1111111	SWITCHES
1111111	1111111	MOTOR SOUND
1111111	1111111	START LED
1111111	1111111	SOUND ENABLE
1111111	1111111	ENGINE LOUDNESS
1111111	1111111	SHELL LOUDNESS
1111111	1111111	SHELL SOUND
1111111	1111111	EXPLOSION LOUDNESS
1111111	1111111	EXPLOSION SOUND
0111111	1111111	M BOX START

REVISIONS				
SYM	DESCRIPTION	DATE	INCORP	CHECK
3	PRE - PROD. RELEASE	6/18/80	EM	MM 80
A	PROD REL.	6/18/80	EM	MM 80

REFERENCE DESIG	
HIGHEST	NOT USED
R52 C35 CR2	R4

NOTES :

- 44 PIN EDGE CONNECTOR
- 24 PIN STRIP CONNECTOR
- 12 PIN TEST CONNECTOR

AO35678	BATTLE ZONE	DO NOT SCALE DRAWING	DRAWN BY EM DATE 6/18/80
NEXT ASSY	FIRST USED ON	CHECKED BY DATE	REVISIONS
OUTSTANDING ECNs			
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON:			
ANGLES	$\pm 1^\circ$	$\pm .005$	ENGR. MECH DATE
SURFACE FINISH		$\pm .005$	ENGR. ELECT DATE
MATERIAL:		INDUSTRIAL DESIGN DATE	
HARNESS DESIGN		PROJECT ENGR DATE	
FINISH:		MFG ENGR DATE	
SCHEMATIC, BATTLE ZONE AUX. P.C. BD.		SIZE D DRAWING NO. 035678-01 A	SCALE SHEET 1 OF 3

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