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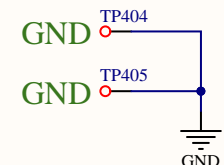
MODIFY:	08/02/2022										
DATE:											
						DESCRIPTION  TELIT EVB 2.0					
FILE NAME      cs2148-A											
Mod. 0366 rev.1 09-2020				ANNOTATION  Refere to 1VV0301732 Rev.2 HUG							FORM A3
PROJECT											
DRAWN				PROJECT			SHEET N. 2	OF SHEETS 14	DRAWING CODE		
VERIFIED											




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MODIFY:	08/02/2022									
DATE:										
						DESCRIPTION  TELIT EVB 2.0				
FILE NAME		cs2148-A								
Mod. 0366 rev.1 09-2020				ANNOTATION  Refere to 1VV0301732 Rev.2 HUG						FORM A3
PROJECT										
DRAWN				PROJECT			SHEET N.	OF SHEETS	DRAWING CODE	
VERIFIED							3	14		



MODIFY:	08/02/2022												
DATE:													
								DESCRIPTION  TELIT EVB 2.0					
FILE NAME cs2148-A													
Mod. 0366 rev.1 09-2020				ANNOTATION  Refere to 1VV0301732 Rev.2 HUG								FORM  A3	
PROJECT													
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VERIFIED													

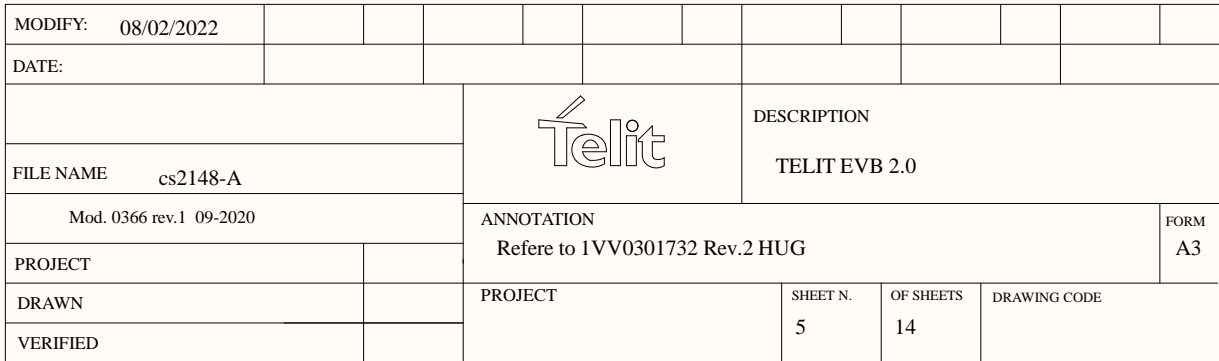
## B



## D




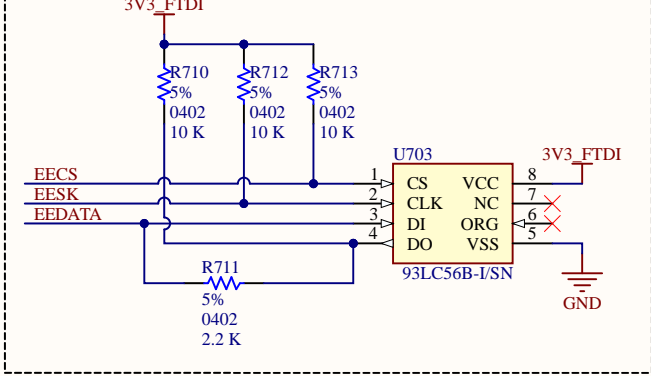
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MODIFY:	08/02/2022										
DATE:											
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FILE NAME      cs2148-A											
Mod. 0366 rev.1 09-2020				ANNOTATION  Refere to 1VV0301732 Rev.2 HUG							FORM A3
PROJECT											
DRAWN				PROJECT			SHEET N. 6	OF SHEETS 14	DRAWING CODE		
VERIFIED											

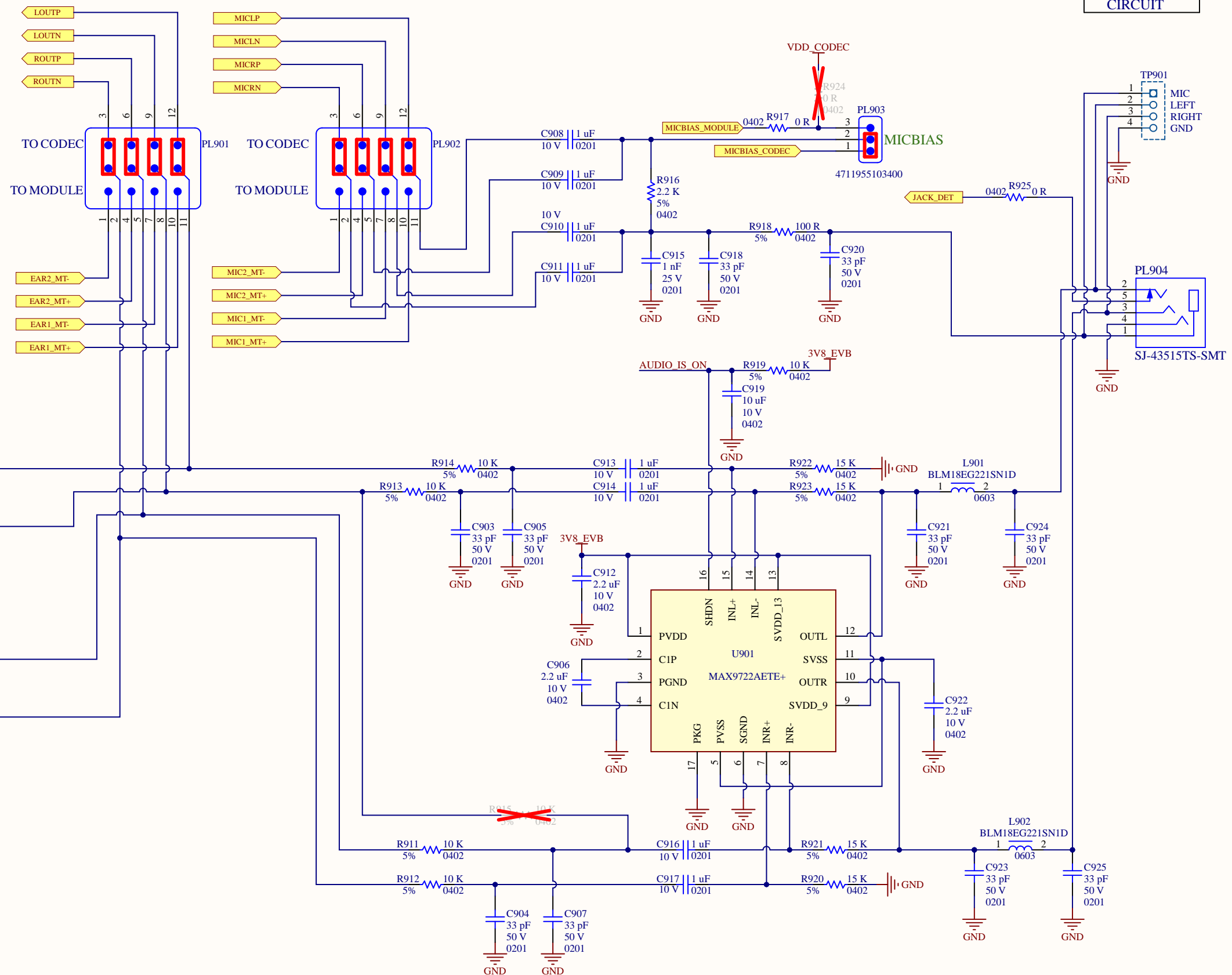
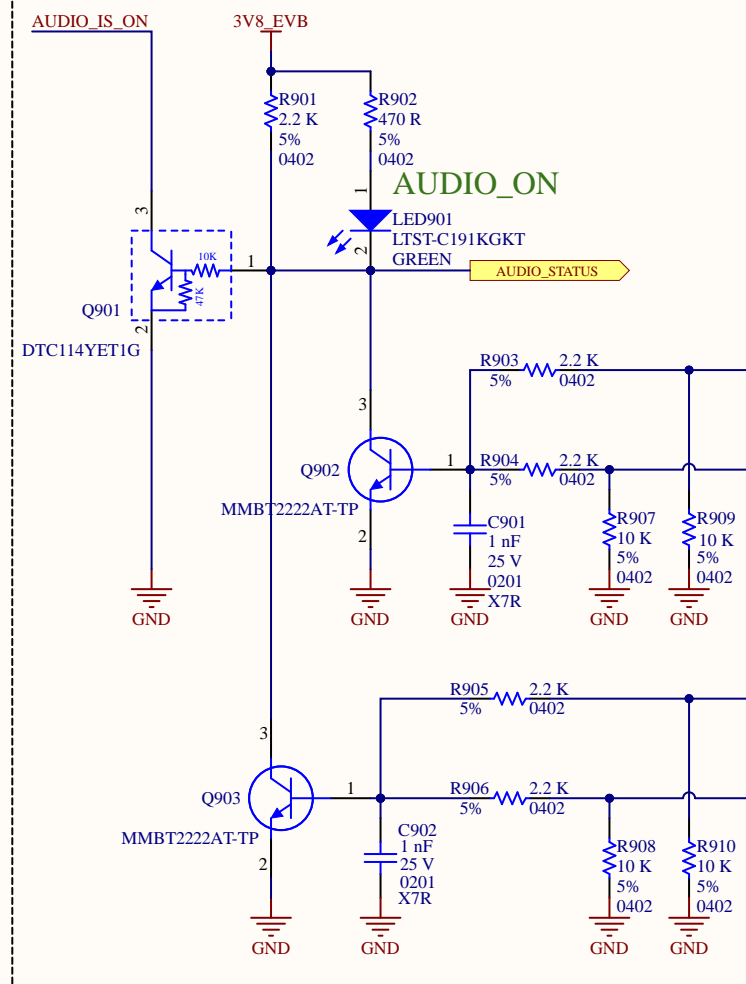


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DATE:											
						DESCRIPTION  TELIT EVB 2.0					
FILE NAME											
Mod. 0366 rev.1 09-2020				ANNOTATION Refere to 1VV0301732 Rev.2 HUG							FORM A3
PROJECT											
DRAWN				PROJECT			SHEET N. 7	OF SHEETS 14	DRAWING CODE		
VERIFIED											

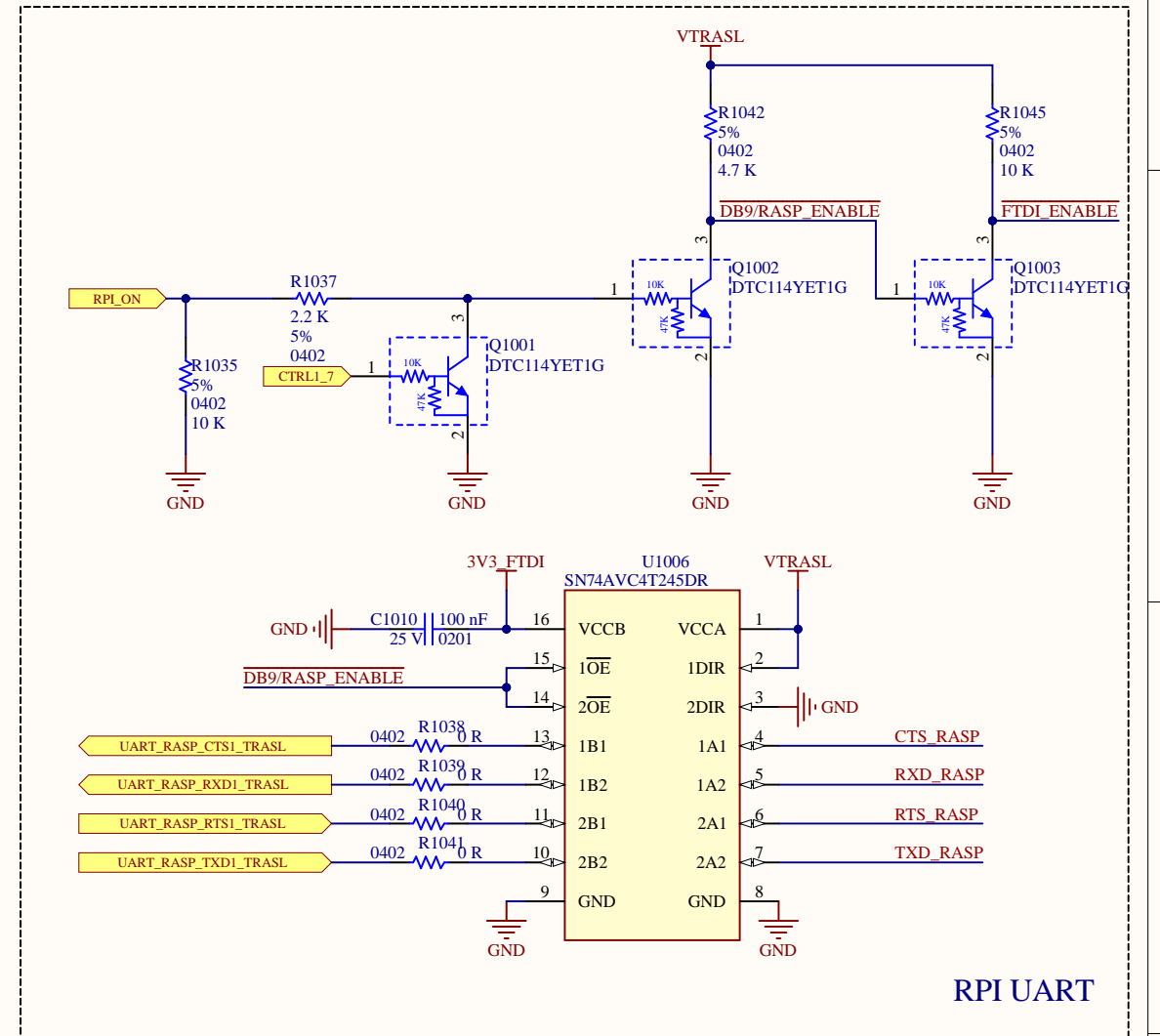




1	2	3	4	5	6	7	8	9	10	11
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FROM/TO FTDI PORTS



Ítelit

DESCRIPTION
TELIT EVB 2.0



Élit

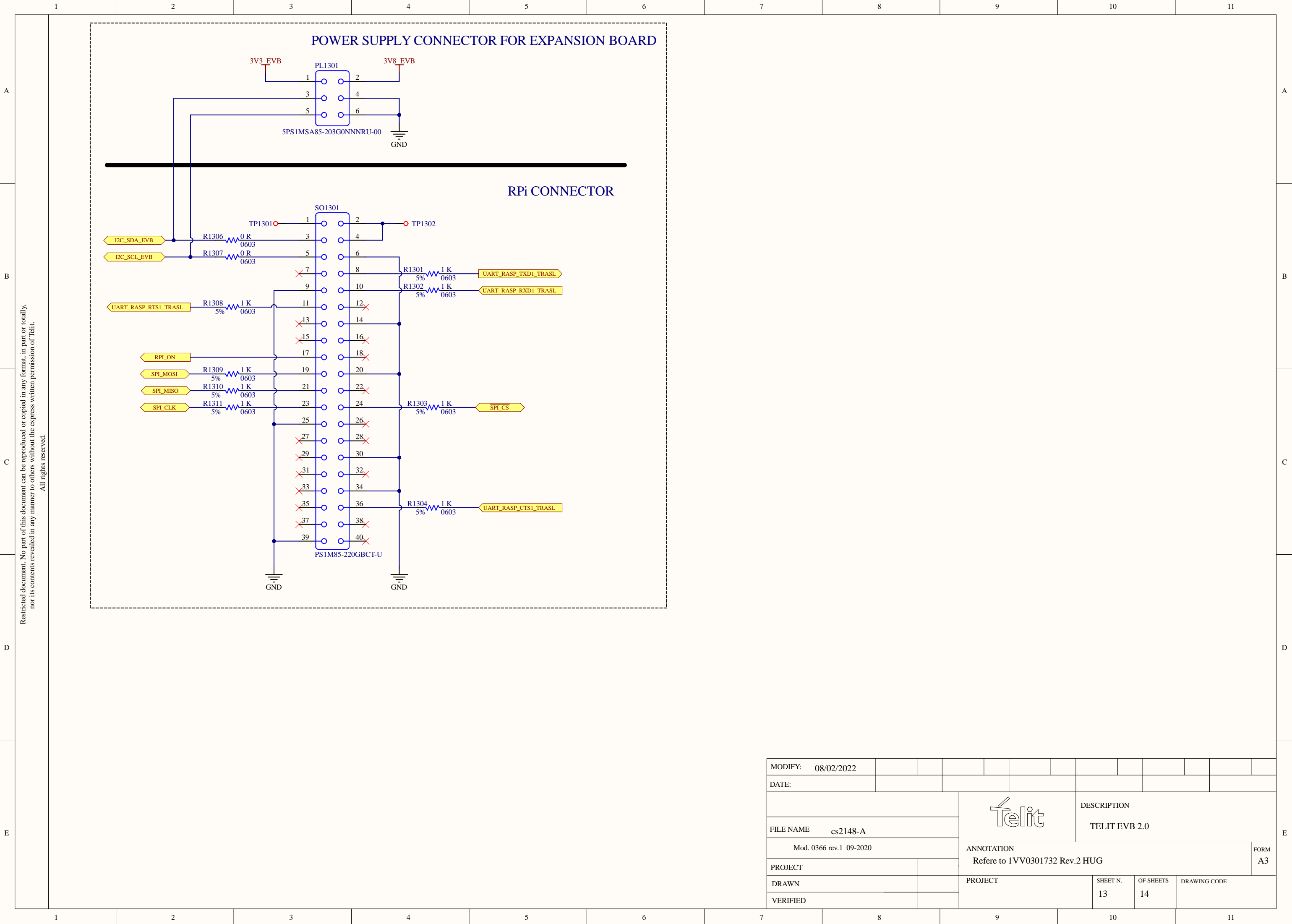


## SIGNAL REMOTE CONTROL

## CURRENT MONITOR

USB DRP I2C

## DAC CONVERTER

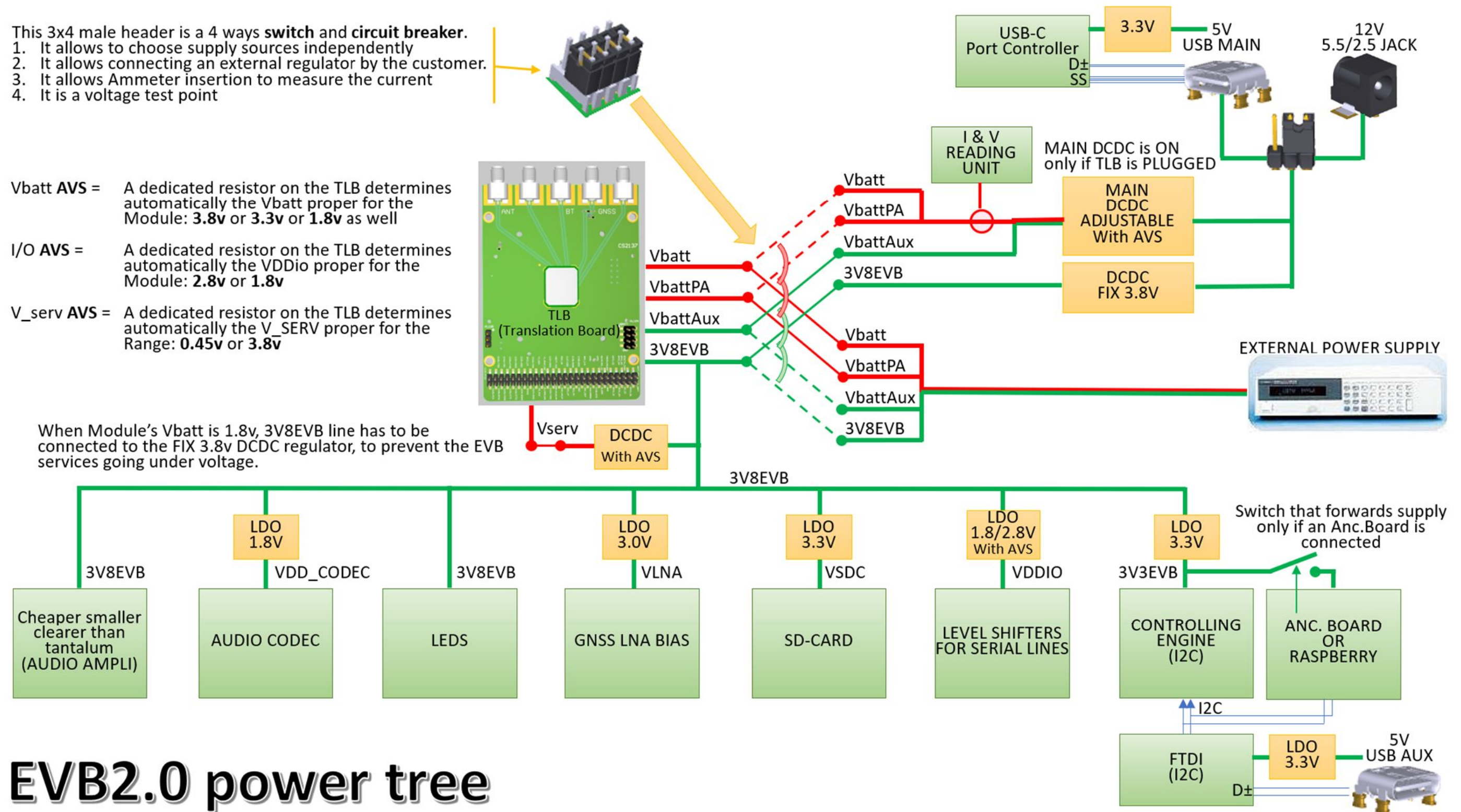


1. It allows to choose supply sources independently
2. It allows connecting an external regulator by the customer.
3. It allows Ammeter insertion to measure the current
4. It is a voltage test point


I/O AVS = A dedicated resistor on the TLB determines automatically the VDDio proper for the Module: **2.8v** or **1.8v**

**V\_serv AVS** = A dedicated resistor on the TLB determines automatically the V\_SERV proper for the Range: **0.45v** or **3.8v**

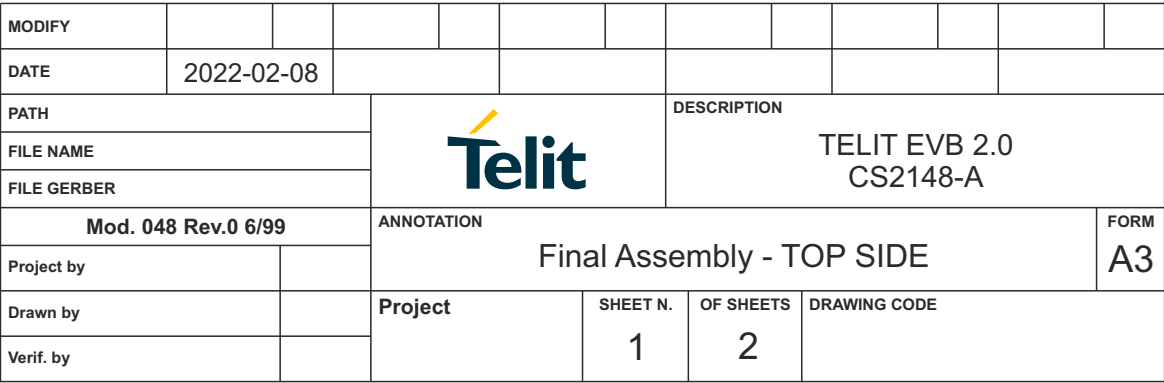
When Module's Vbatt is 1.8v, 3V8EVB line has to be connected to the FIX 3.8v DCDC regulator, to prevent the EVB services going under voltage.



# EVB2.0 power tree

MODIFY:	23/02/2022										
DATE:											
						DESCRIPTION  TELIT EVB 2.0					
FILE NAME cs2148-A											
Mod. 0366 rev.1 09-2020				ANNOTATION  Refere to 1VV0301732 Rev.2 HUG							FORM  A3
PROJECT											
DRAWN				PROJECT			SHEET N.  14	OF SHEETS  14	DRAWING CODE		
VERIFIED											

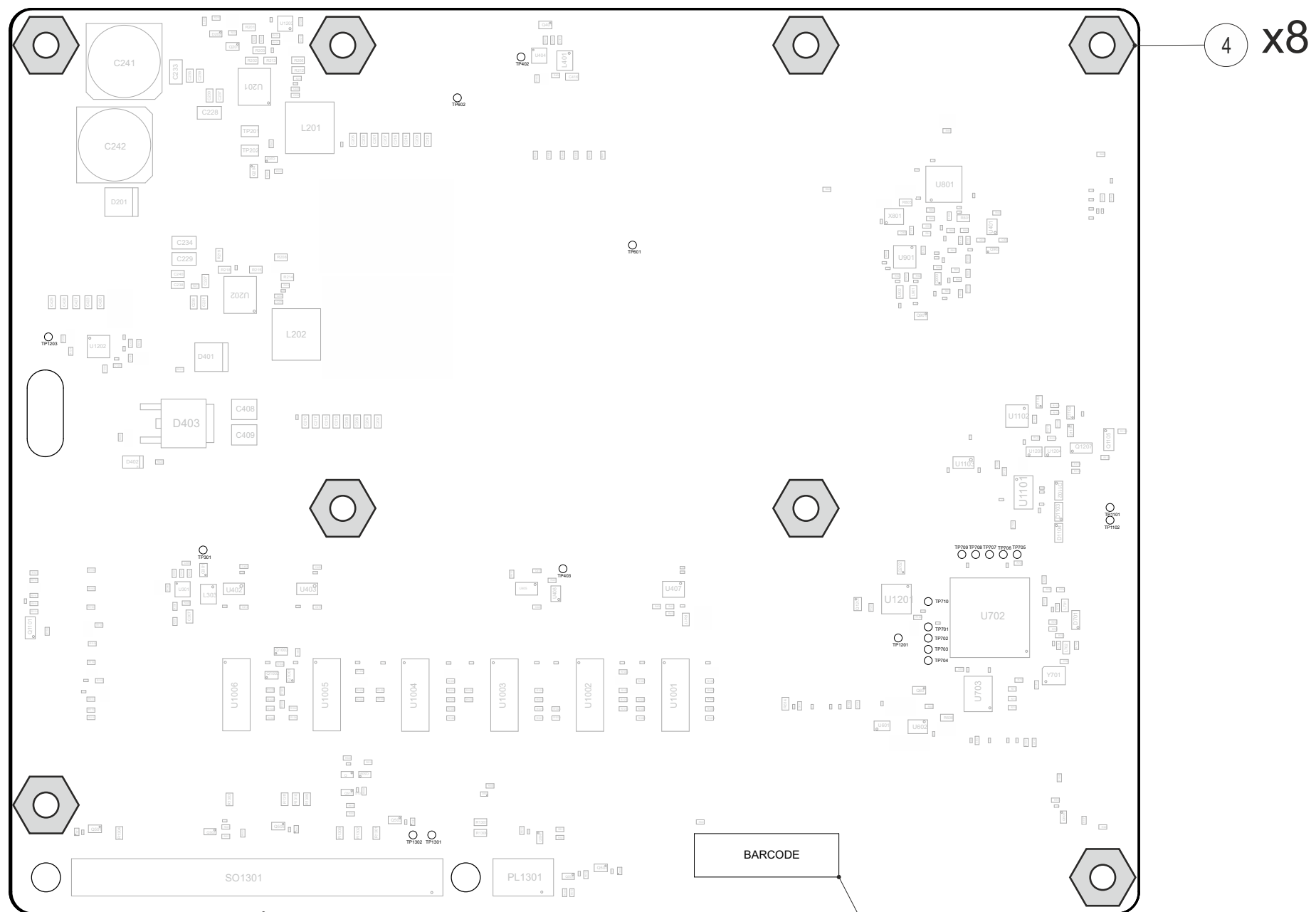
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RIPRODUZIONE E DIVULGAZIONE  
VIETATE**

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1

2

MODIFY										
DATE	2022-02-08									
PATH					DESCRIPTION  TELIT EVB 2.0 CS2148-A					
FILE NAME										
FILE GERBER										
Mod. 048 Rev.0 6/99			ANNOTATION  Final Assembly - BOTTOM SIDE							FORM
Project by										A3
Drawn by			Project	SHEET N.	OF SHEETS	DRAWING CODE				
Verif. by				2	2					


**TUTTI I DIRITTI RISERVATI  
RIPRODUZIONE E DIVULGAZIONE  
VIETATE**



MODIFY									
DATE	2022-02-08								
PATH					DESCRIPTION  TELIT EVB 2.0 CS2148-A				
FILE NAME									
FILE GERBER									
Mod. 048 Rev.0 6/99			ANNOTATION  Primary Layer SMD						FORM
Project by									A3
Drawn by			Project	SHEET N.	OF SHEETS	DRAWING CODE			
Verif. by				1	1				

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MODIFY											
DATE	2022-02-08										
PATH						DESCRIPTION TELIT EVB 2.0 CS2148-A					
FILE NAME											
FILE GERBER											
Mod. 048 Rev.0 6/99			ANNOTATION Secondary Layer SMD								FORM A3
Project by											
Drawn by											
Verif. by			Project		SHEET N.	OF SHEETS	DRAWING CODE				
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