|  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- |
| Operation | MemRead | MemWrite | RegWrite | ALUControl | MemToReg | RegDst | ALUSRC |
| lw | 1 | 0 | 1 | 1 | 1 | 0 | 1 |
| sw | 0 | 1 | 0 | 1 | 0 | 0 | 1 |
| add | 0 | 0 | 1 | 1 | 0 | 1 | 0 |
| addi | 0 | 0 | 1 | 1 | 0 | 0 | 1 |