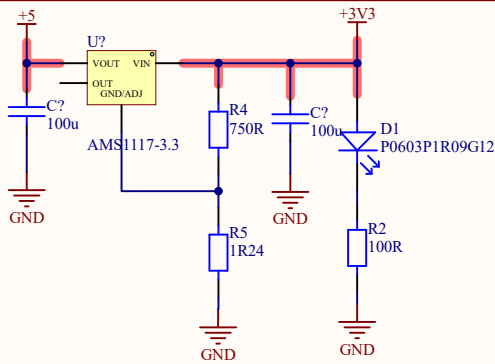
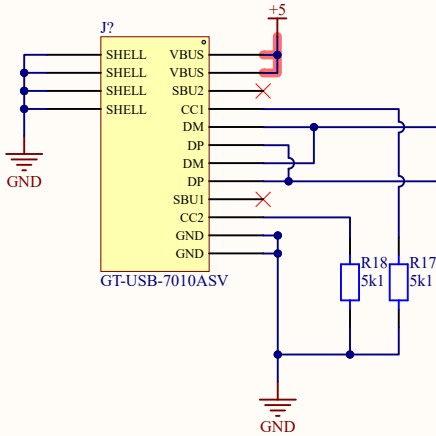
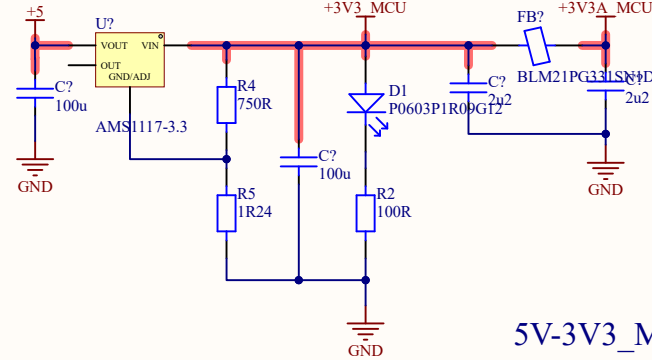


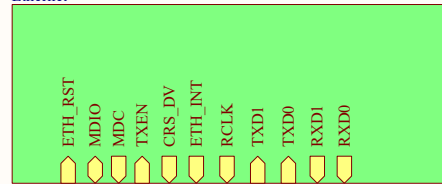
5V-3V3



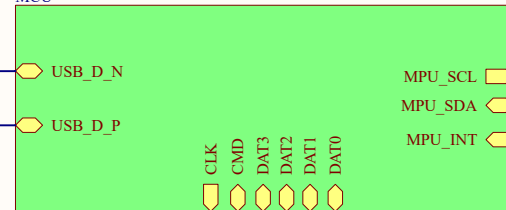
5V-3V3_MCU



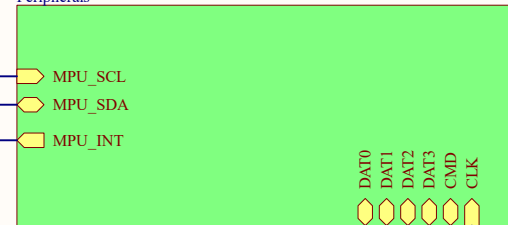
S3
Ethernet



S1
MCU



S2
Peripherals



| | | |
|---------------------------|--|-----------------|
| Title PARERO DSP BOARD | | |
| Size A4 | Number | Revision 0.1 |
| Date: | 12/01/2025 | Sheet of 1 |
| File: | D:\HardWare Repos\parero board schematics\Doc conics | |

