

Scanning Tunnelling Microscope - Milestone # 93: Layout of bias voltage controller PCB

Status:	Resolved	Priority:	Normal
Author:	Andrew Starr	Category:	
Created:	11/19/2012	Assignee:	
Updated:	12/02/2012	Due date:	
Subject:	Layout of bias voltage controller PCB		
Description			

History

11/19/2012 04:07 am - Andrew Starr

- Status changed from New to In Progress

12/02/2012 02:45 am - Andrew Starr

- Status changed from In Progress to Resolved