AttenuatorX - Milestone # 100: Design PCB for XTest1

Status:	Closed	**Priority:**	Normal
Author:	Jack Linke	**Category:**	
Created:	12/21/2012	**Assignee:**	Jack Linke
Updated:	12/30/2012	**Due date:**	12/30/2012
Subject:	Design PCB for XTest1		

Description

Design the PCB for XTest1, ensuring correct layout procedures are followed for RF designs up to 3GHz.

History

12/26/2012 03:08 pm - Jack Linke

- Due date changed from 12/27/2012 to 12/29/2012

- Status changed from New to In Progress

- Start date changed from 12/24/2012 to 12/26/2012

- % Done changed from 0 to 10

- Estimated time changed from 8.00 to 12.00

Began associating components with their packages and very basic organizing of the PCB.

12/27/2012 01:18 am - Jack Linke

- % Done changed from 10 to 30

- Estimated time changed from 12.00 to 30.00

Majority of parts are now placed.

The major problem at this point is finding a decent enclosure for this project. I want to keep the RF path as short as possible, but finding just the right box/case/enclosure is turning out to be tricky. I may just have to settle on something sub-par, which wouldn't be too terrible, since this is only a test project for R&D toward the final product.

12/30/2012 10:32 pm - Jack Linke

- Due date changed from 12/29/2012 to 12/30/2012

- Status changed from In Progress to Closed

- % Done changed from 30 to 100

- Estimated time changed from 30.00 to 18.00

Completed PCB for XTest1