

O3_O2_O1_O0	Instruction	Rd_src_reg	Reg_write	Mem_write	Mem_read	WD_src_1	WD_src_0	EN_Jump	ALU_2_src	ALU_2_imm	ALU_sel_2	ALU_sel_1	ALU_sel_0
0	ADD	0	1	0	0	0	0	0	0	x	0	0	0
1	SUB	0	1	0	0	0	0	0	0	x	0	0	1
2	AND	0	1	0	0	0	0	0	0	x	0	1	0
3	OR	0	1	0	0	0	0	0	0	x	0	1	1
4	XOR	0	1	0	0	0	0	0	0	x	1	0	0
5	NOT	0	1	0	0	0	0	0	x	x	1	0	1
6	ADDI	0	1	0	0	0	0	0	1	1	0	0	0
7	SUBI	0	1	0	0	0	0	0	1	1	0	0	1
8	INCR	0	1	0	0	0	0	0	1	0	0	0	0
9	DECR	0	1	0	0	0	0	0	1	0	0	0	1
10	LOAD	0	1	0	1	0	1	0	x	x	x	x	x
11	STORE	1	0	1	0	x	x	0	x	x	x	x	x
12	MOV	0	1	0	0	1	0	0	x	x	x	x	x
13	JUMP	x	0	0	0	x	x	1	x	x	x	x	x