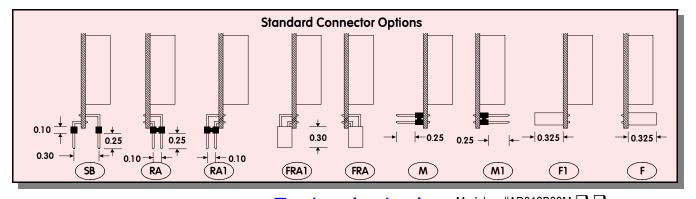


ADAPT912B32™ CONNECTOR PINOUTS

H1				H2			
PIN	SIGNAL NAME	PIN	SIGNAL NAME	PIN	SIGNAL NAME	PIN	SIGNAL NAME
1	PS4/SDI/MISO	50	GROUND	1	MUX15/PA7	50	VCC (+5VDC)
2	PS5/SDO/MOSI	49	GROUND	2	MUX14/PA6	49	GROUND
3	PS6/SCK	48	PSO/RXD0	3	MUX13/PA5	48	DBE*
4	PS7/SS*	47	+5VDC	4	MUX12/PA4	47	GROUND
5	PS1/TXD0	46	IRQ*	5	MUX11/PA3	46	GROUND
6	PT7/IOC7/PAI	45	XIRQ*	6	MUX10/PA2	45	GROUND
7	PT6/IOC6	44	RESET*	7	MUX9/PA1	44	GROUND
8	PT5/IOC5	43	PE7/DBE*	8	MUX8/PA0	43	VIN
9	PT4/IOC4	42	PDLC0	9	MUX7/PB7	42	VIN
10	PT3/IOC3	41	PDLC1	10	MUX6/PB6	41	VIN
11	PT2/IOC2	40	PDLC2	11	MUX5/PB5	40	VIN
12	PT1/IOC1	39	PDLC3	12	MUX4/PB4	39	RESERVED
13	PT0/IOC0	38	PDLC4	13	MUX3/PB3	38	RESERVED
14	PJ7/PP7	37	PDLC5	14	MUX2/PB2	37	RESERVED
15	PJ6/PP6	36	PDLC6	15	MUX1/PB1	36	RESERVED
16	PJ5/PP5	35	RESERVED	16	MUX0/PB0	35	RESERVED
17	PJ4/PP4	34	PS2	17	R/W* / PE2	34	RESERVED
18	PJ3/PP3/PW3	33	PE4/ECLK	18	ECLK/PE4	33	RESERVED
19	PJ2/PP2/PW2	32	PS3	19	LSTRB*/PE3	32	RESERVED
20	PJ1/PP1/PW1	31	VRL	20	RESERVED	31	RESERVED
21	PJ0/PP0/PW0	30	VRH	21	RESET*	30	RESERVED
22	PAD0/AN0	29	PAD4/AN4	22	MISO	29	RESERVED
23	PAD1/AN1	28	PAD5/AN5	23	MOSI	28	RESERVED
24	PAD2/AN2	27	PAD6/AN6	24	SCK	27	RESERVED
25	PAD3/AN3	26	PAD7/AN7	25	SS*	26	RESERVED

NOTE: * indicates active low signal



Technological Arts Module: #AD912B32M-□-□
Starter Package: #AD912B32SP(-□-□)
(supplied with RA-M connectors unless otherwise specified)