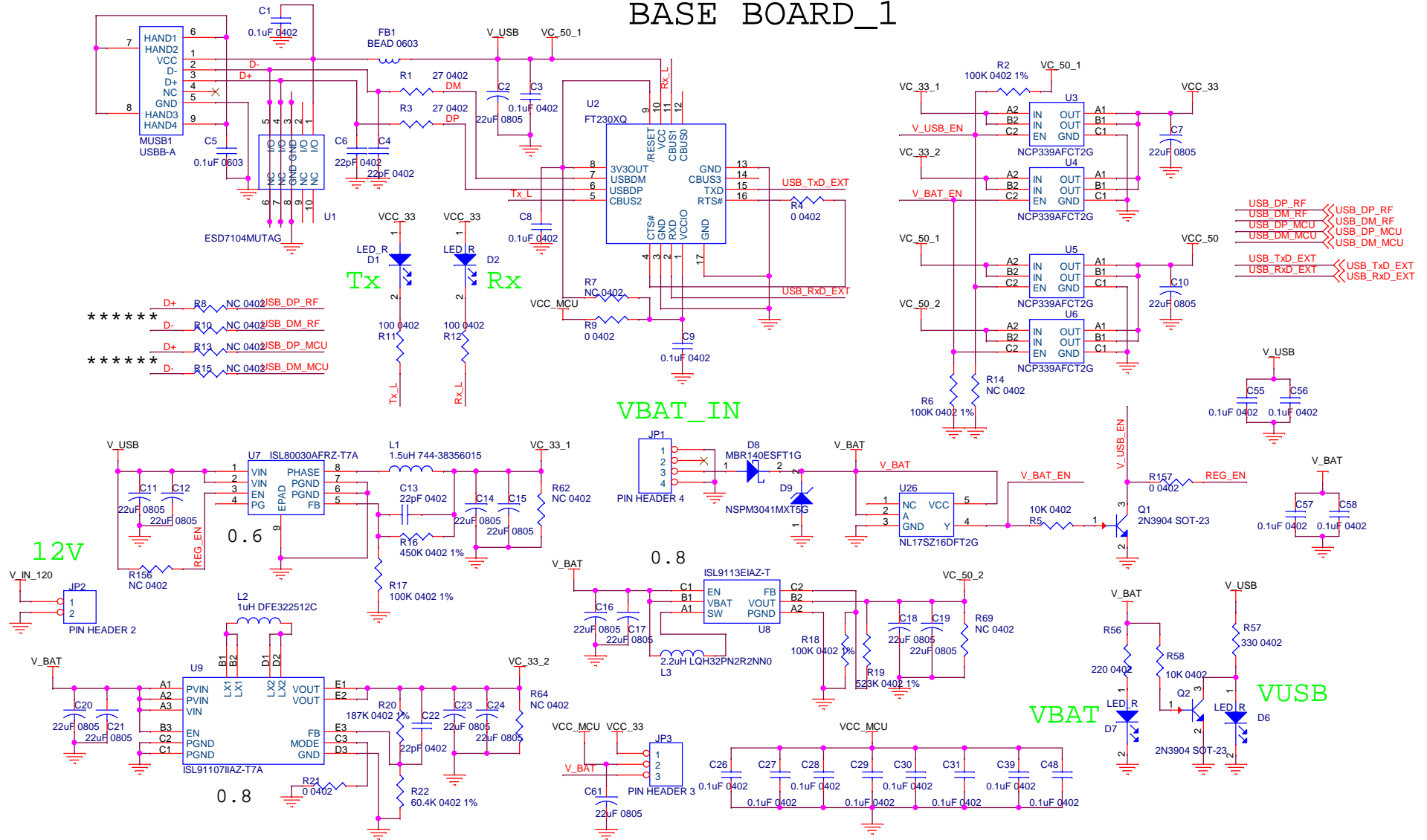


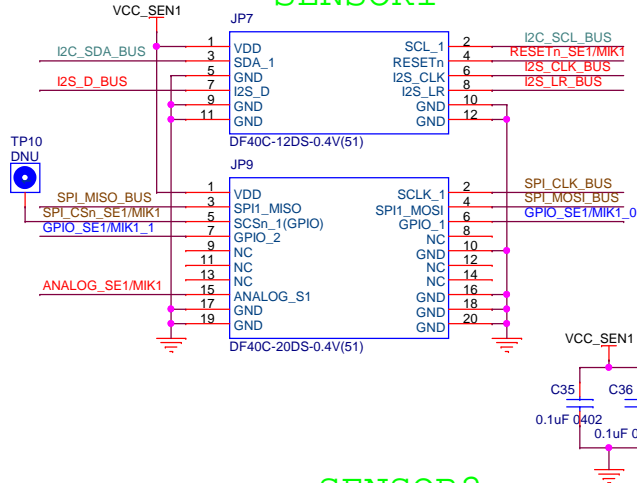
BASE BOARD_1



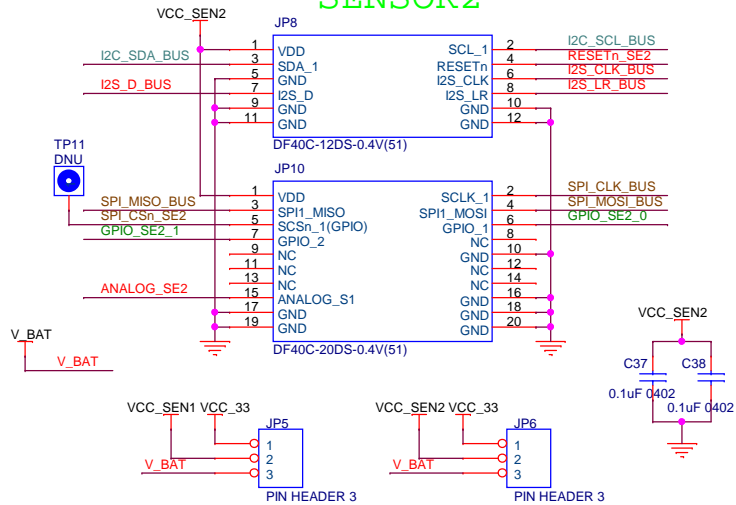
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BASE BOARD_5

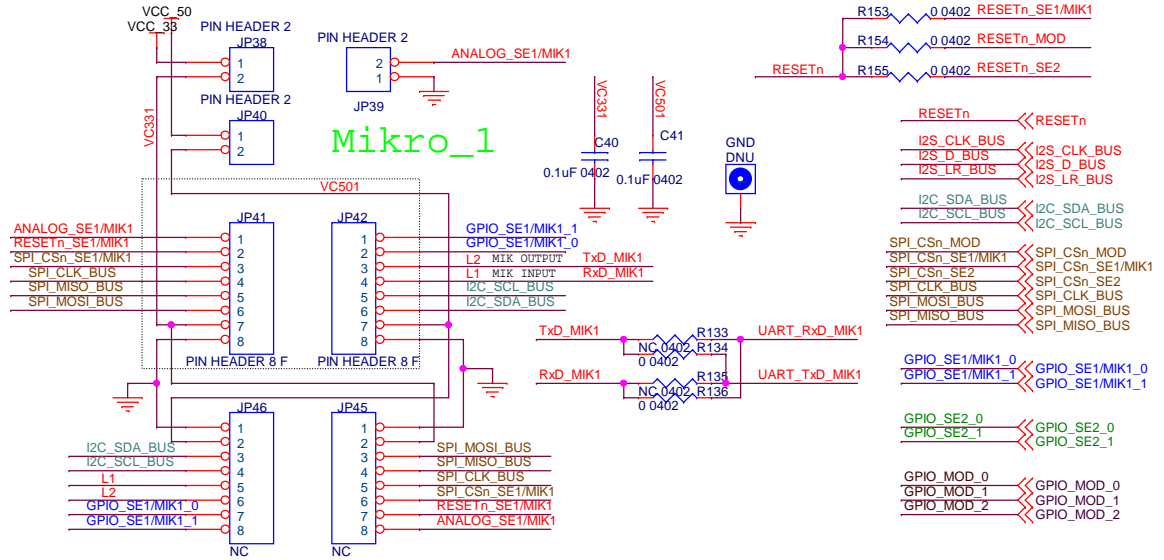
SENSOR1



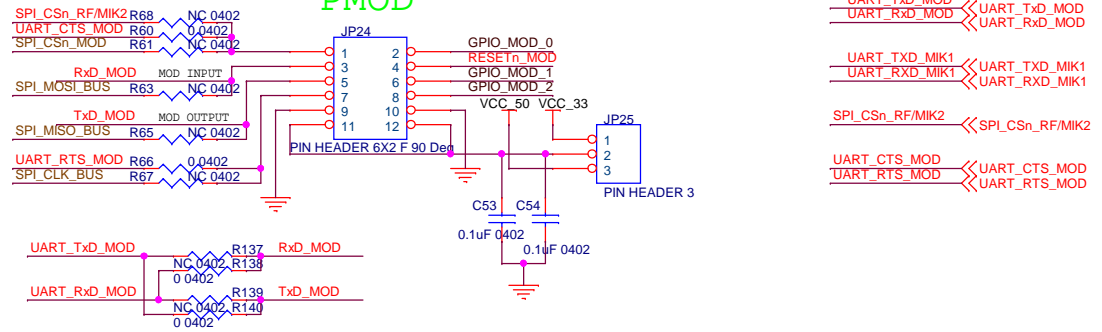
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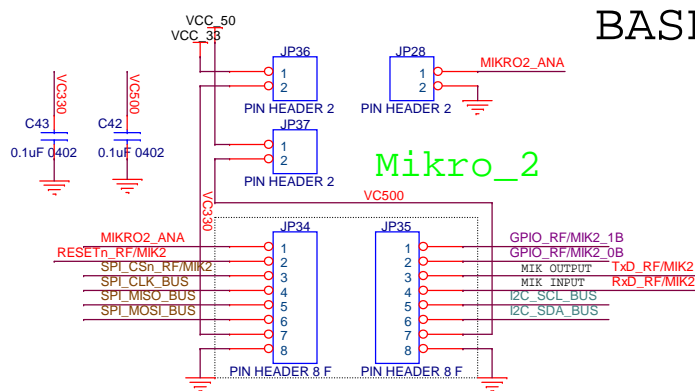
Mikro_1



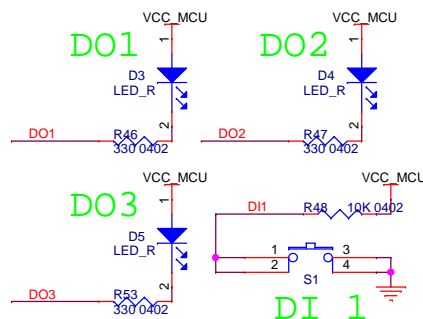
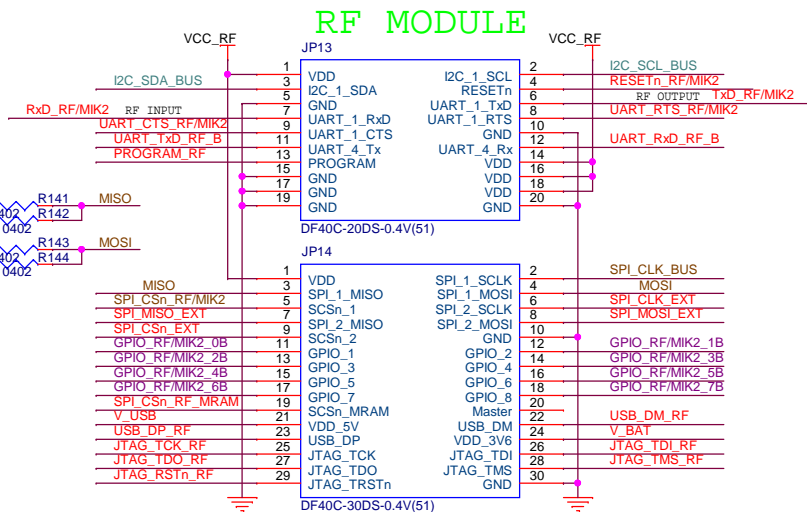
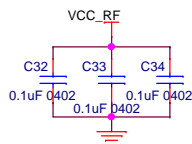
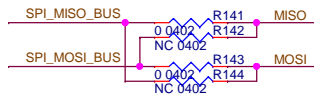
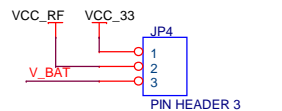
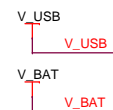
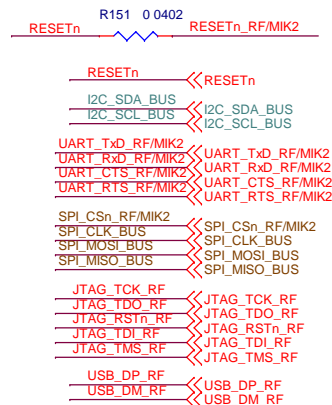
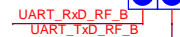
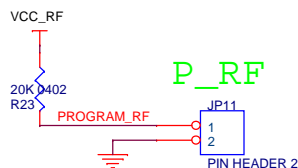
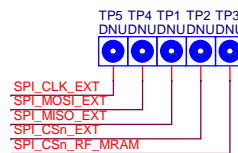
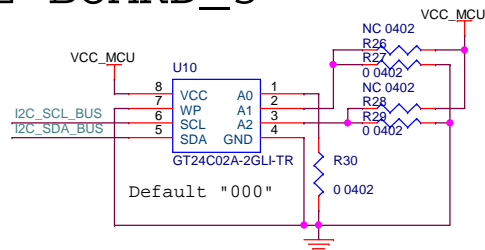
PMOD



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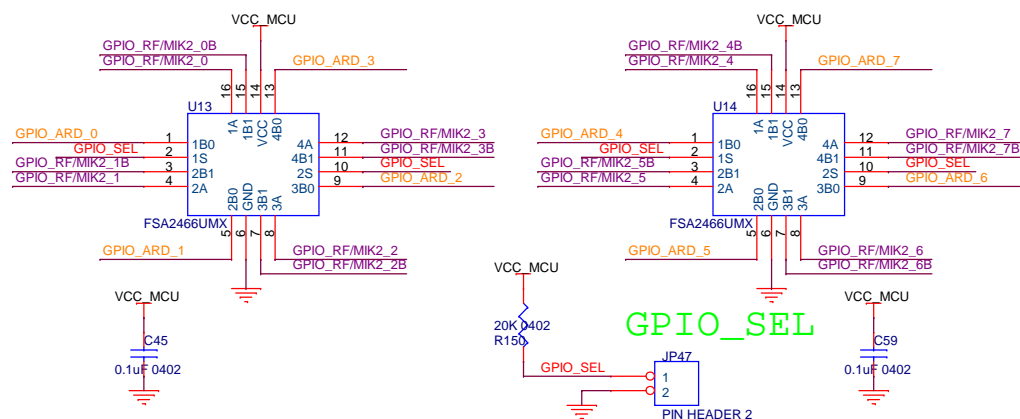


BASE BOARD_3

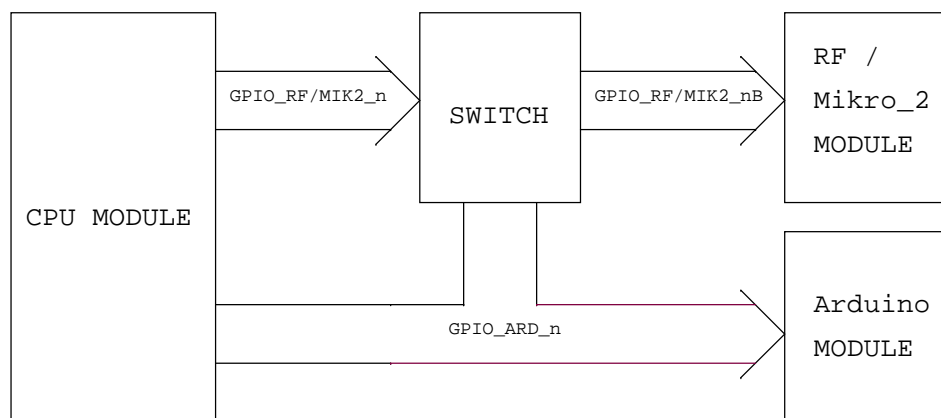


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BASE BOARD_6



OFF : GPIO_RF/MIK2_n >> GPIO_RF/MIK2_nB
ON : GPIO_RF/MIK2_n >> GPIO_ARD_n



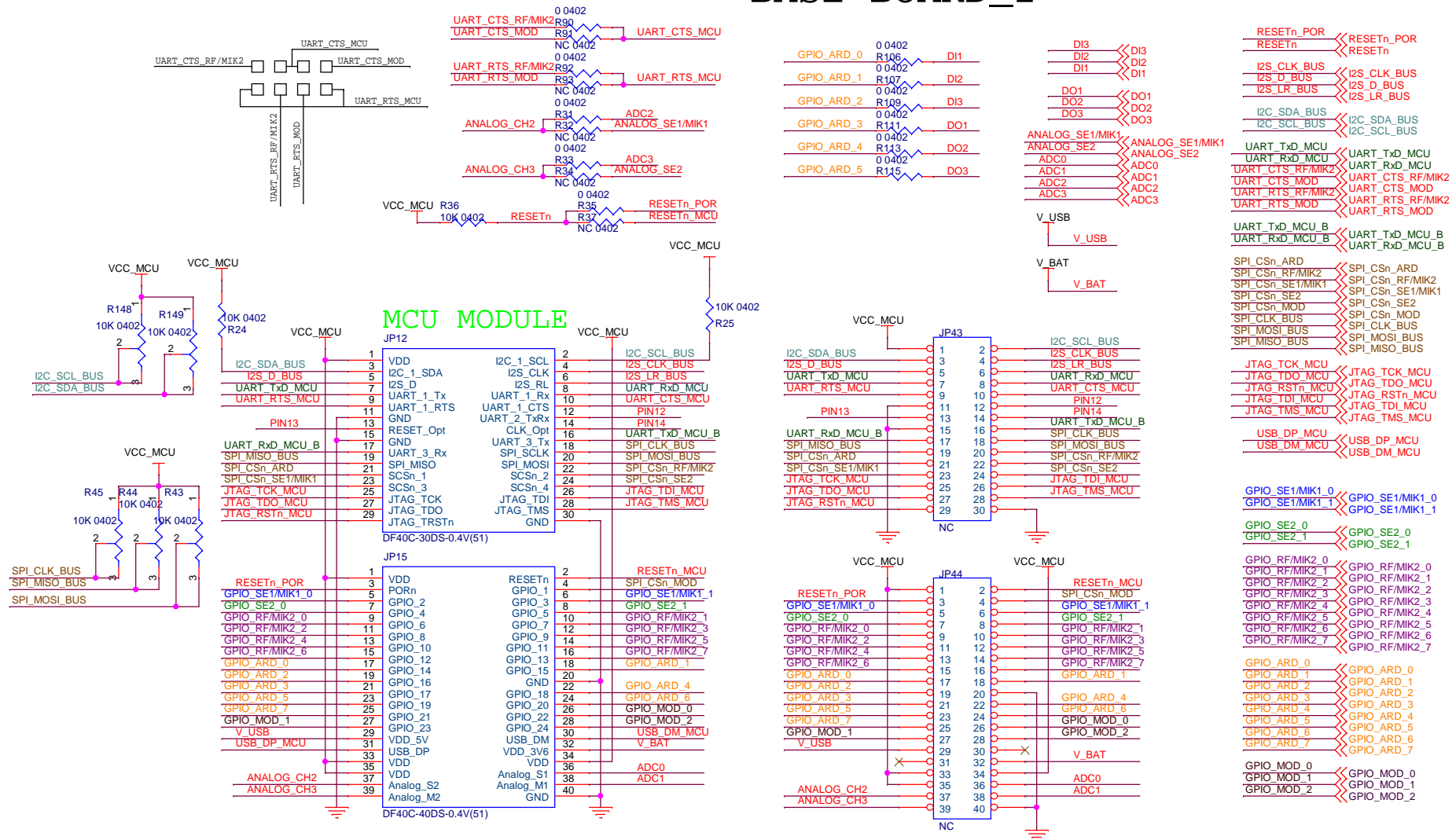
GPIO_RF/MIK2_0 >> GPIO_RF/MIK2_0
GPIO_RF/MIK2_1 >> GPIO_RF/MIK2_1
GPIO_RF/MIK2_2 >> GPIO_RF/MIK2_2
GPIO_RF/MIK2_3 >> GPIO_RF/MIK2_3
GPIO_RF/MIK2_4 >> GPIO_RF/MIK2_4
GPIO_RF/MIK2_5 >> GPIO_RF/MIK2_5
GPIO_RF/MIK2_6 >> GPIO_RF/MIK2_6
GPIO_RF/MIK2_7 >> GPIO_RF/MIK2_7

GPIO_ARD_0 >> GPIO_ARD_0
GPIO_ARD_1 >> GPIO_ARD_1
GPIO_ARD_2 >> GPIO_ARD_2
GPIO_ARD_3 >> GPIO_ARD_3
GPIO_ARD_4 >> GPIO_ARD_4
GPIO_ARD_5 >> GPIO_ARD_5
GPIO_ARD_6 >> GPIO_ARD_6
GPIO_ARD_7 >> GPIO_ARD_7

GPIO_RF/MIK2_0B >> GPIO_RF/MIK2_0B
GPIO_RF/MIK2_1B >> GPIO_RF/MIK2_1B
GPIO_RF/MIK2_2B >> GPIO_RF/MIK2_2B
GPIO_RF/MIK2_3B >> GPIO_RF/MIK2_3B
GPIO_RF/MIK2_4B >> GPIO_RF/MIK2_4B
GPIO_RF/MIK2_5B >> GPIO_RF/MIK2_5B
GPIO_RF/MIK2_6B >> GPIO_RF/MIK2_6B
GPIO_RF/MIK2_7B >> GPIO_RF/MIK2_7B

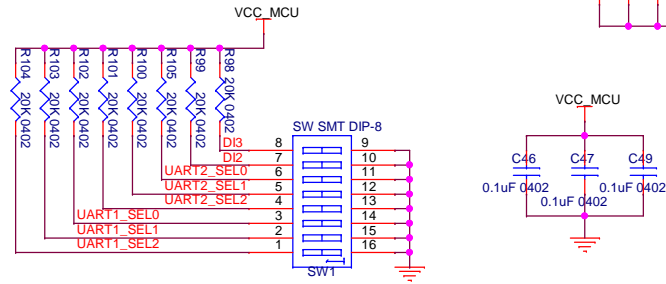
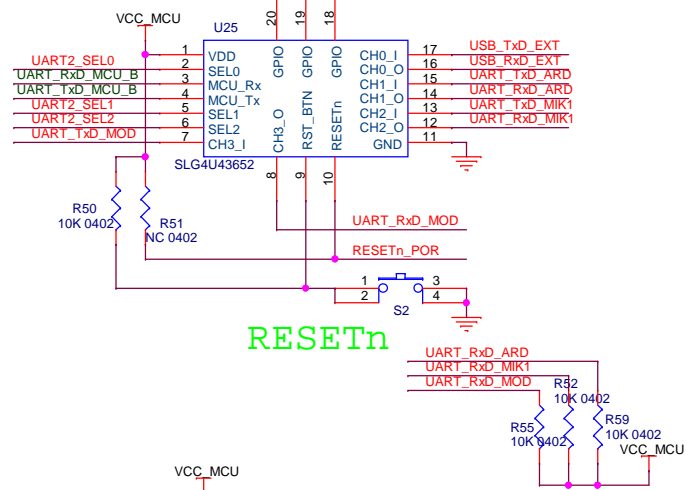
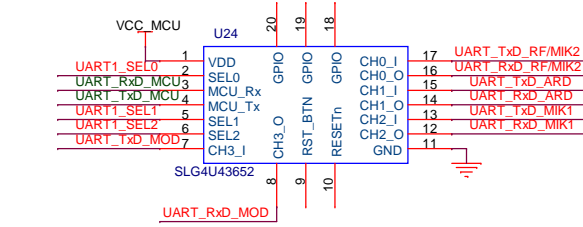
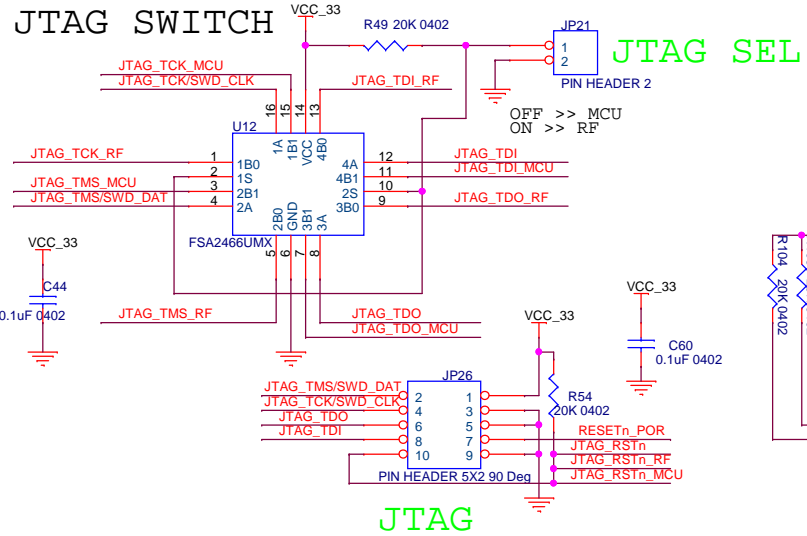
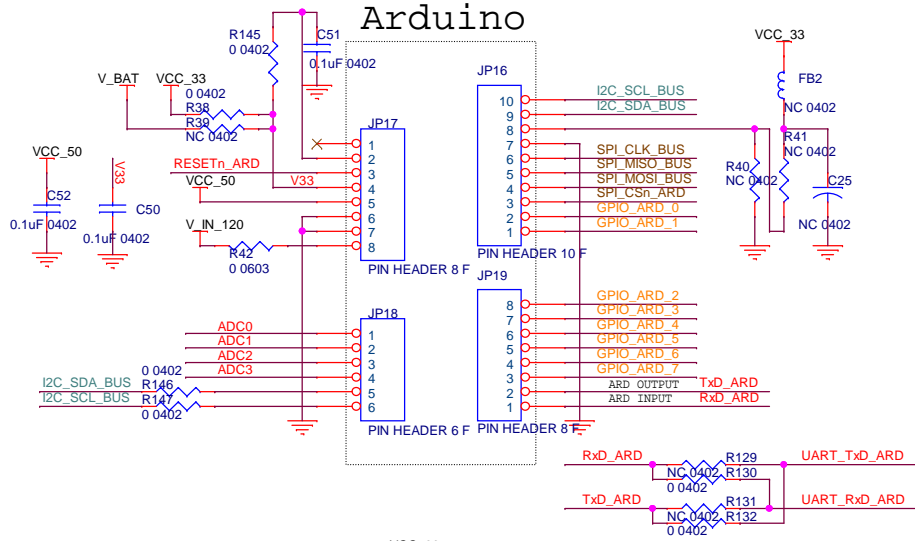
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BASE BOARD_2



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BASE BOARD_4



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