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=== STATS ===

= CPU =

bits: 8

Registers: 6

Instructions: 16

Clock Speed: 256 khz.

= RAMemory =

Addresses: 128

Address Width: 8

Data Width: 8

= Program Memory =

Adresses: 4095

Address With: 12

Data Width: 16

=== INSTRUCTIONS ===

0: HALT -> Stops the CPU

1: ADD -> Adds 2 Registers

2: ADD^ -> Adds a Register by a value

3: DEC -> Decreaments 2 Registers

4: DEC^ -> Decreaments Register by value

5: JMP -> Jumps to a part of the program

6: CMP -> Compares 2 Registers

7: CMP^ -> Compares Register by a value

8: JE -> Jumps if CMP is succesful

9: SET -> Sets Register to a value

A: OUTB -> Outputs Registers Value

B: INB -> Stores Input in a Register

C: MEM -> Stores Register in MEM Address

D: MEM^ -> MEM but Address from Register

E: LMEM -> Load MEM Address to Register

F: LMEM^ -> LMEM but Address from Register