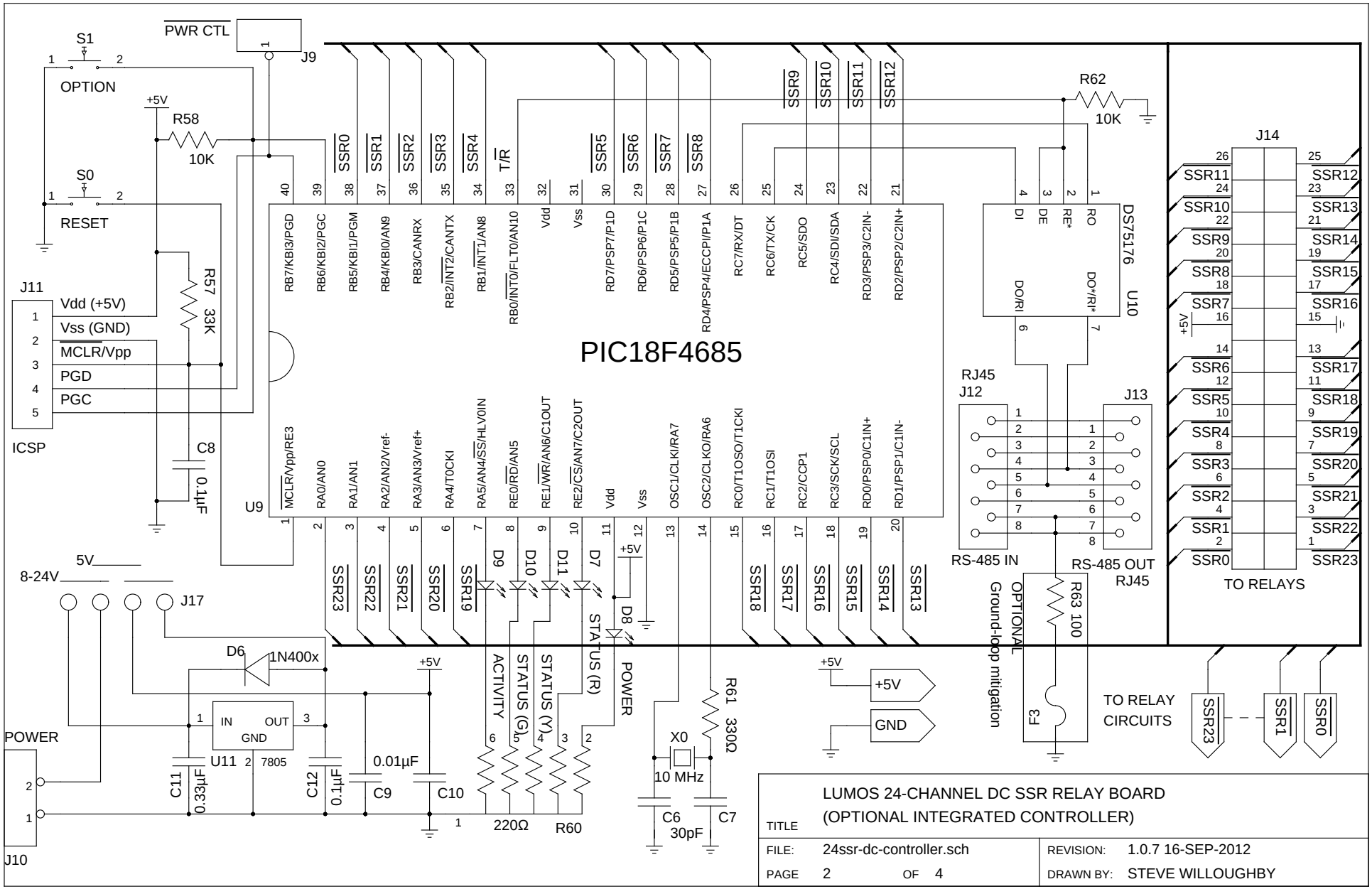
PIC18F4685
U9
DS75176
U10
J9
PWR CTL
S1
OPTION
S0
RESET
J11
ICSP
Vdd (+5V)
Vss (GND)
MCLR/Vpp
PGD
PGC
R57 33K
C8 0.1µF
R58 10K
R62 10K
J14
TO RELAYS
RJ45
J12
RS-485 IN
J13
RS-485 OUT
RJ45
OPTIONAL
Ground-loop mitigation
R63 100
F3
TO RELAY CIRCUITS
SSR23
SSR1
SSR0
8-24V
5V
J17
D6 1N400x
U11 7805
C11 0.33µF
C12 0.1µF
C9 0.01µF
C10
POWER
J10
ACTIVITY
STATUS (G)
STATUS (Y)
STATUS (R)
POWER
220Ω
R60
R61 330Ω
X0
10 MHz
C6 30pF
C7
+5V
GND
LUMOS 24-CHANNEL DC SSR RELAY BOARD
(OPTIONAL INTEGRATED CONTROLLER)
TITLE
FILE: 24ssr-dc-controller.sch
REVISION: 1.0.7 16-SEP-2012
PAGE 2 OF 4
DRAWN BY: STEVE WILLOUGHBY