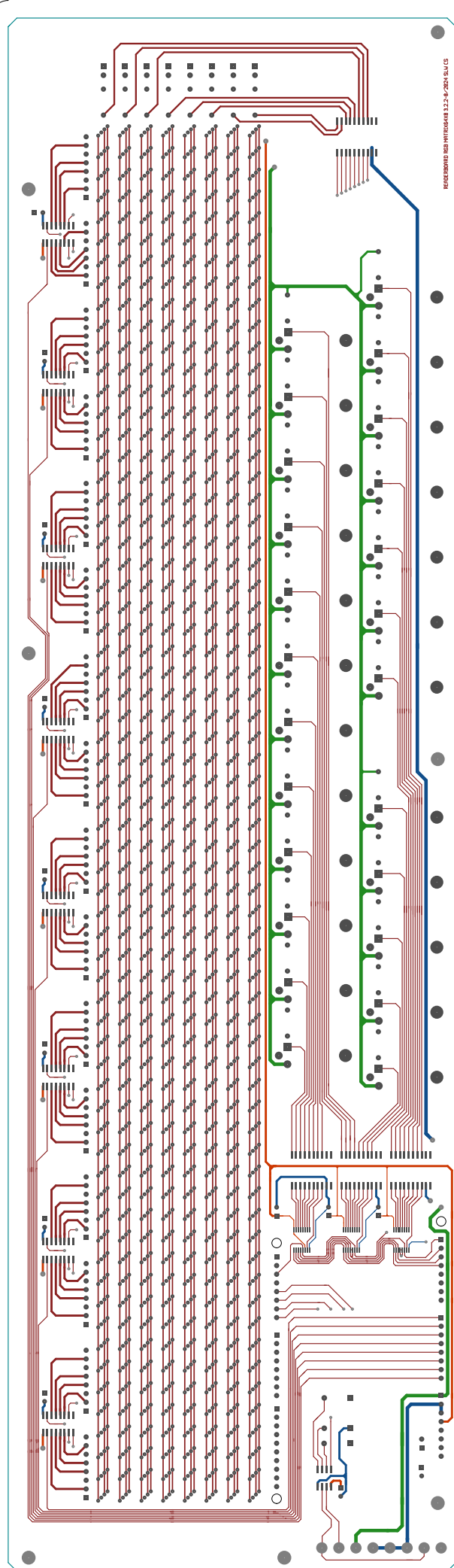


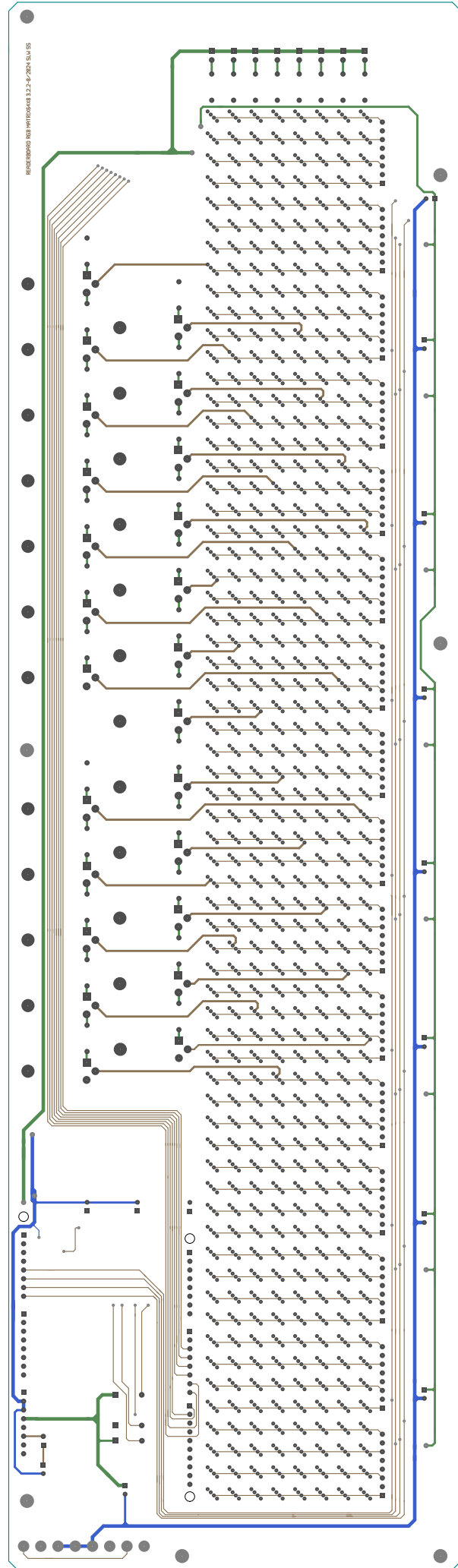
1. Table of Contents (This Page)

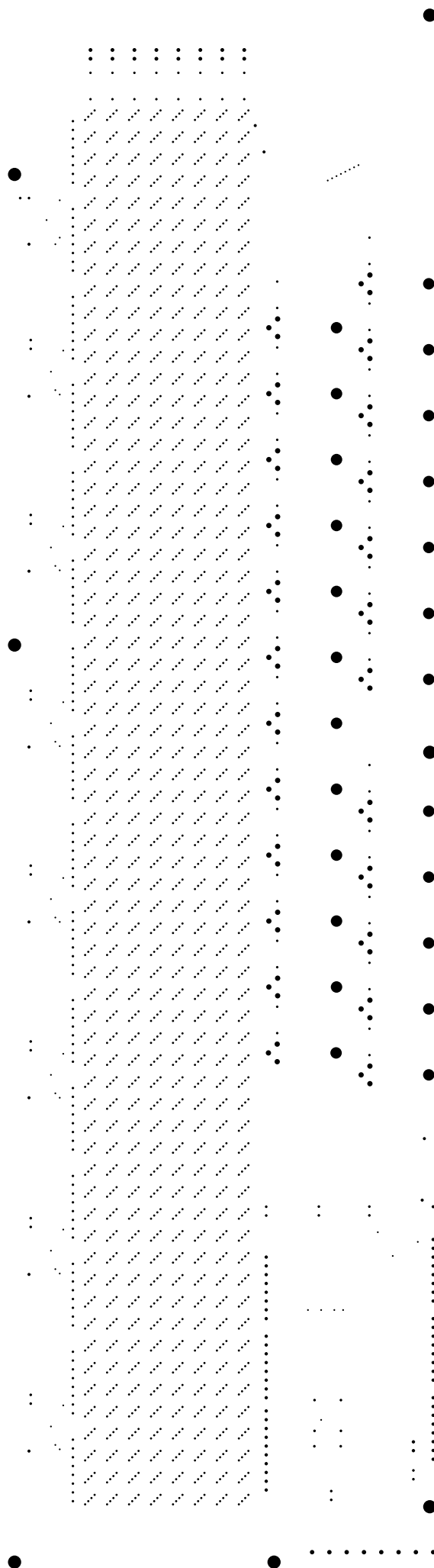
Created on Tue Sep 10 18:05:57 2024

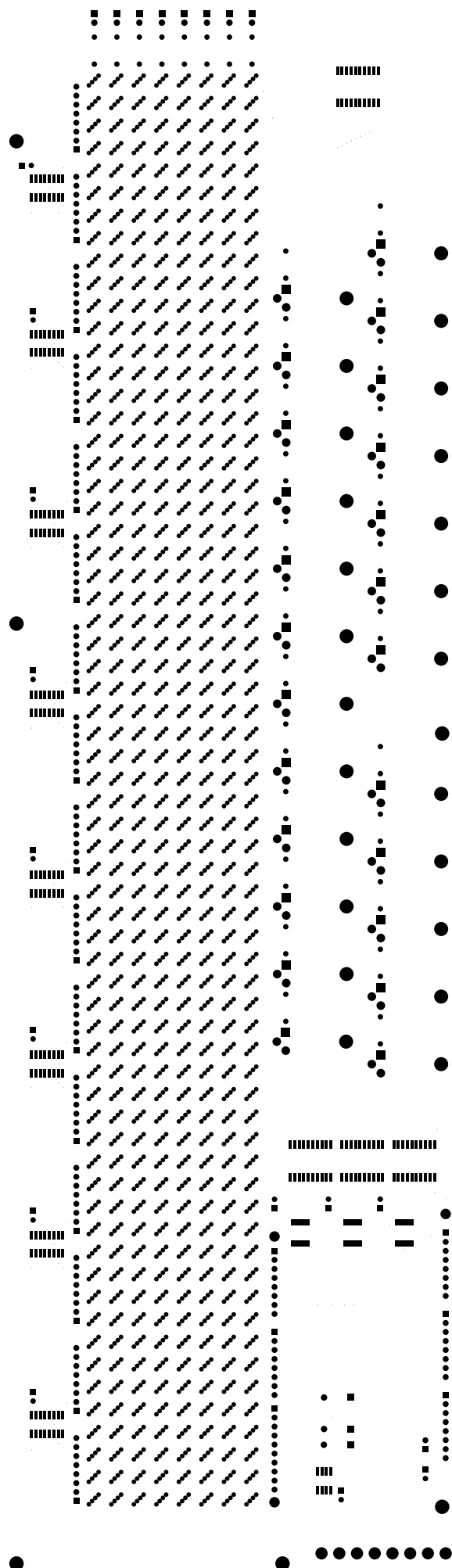
2. top
3. ground
4. outline
5. plated-drill
6. unplated-drill
7. componentmask
8. soldermask
9. topsilk
10. bottomsilk
11. toppaste
12. topassembly
13. bottomassembly
14. fab

```
top, scale = 1:0.600
matrix64x8_rgb_300.pcb
```

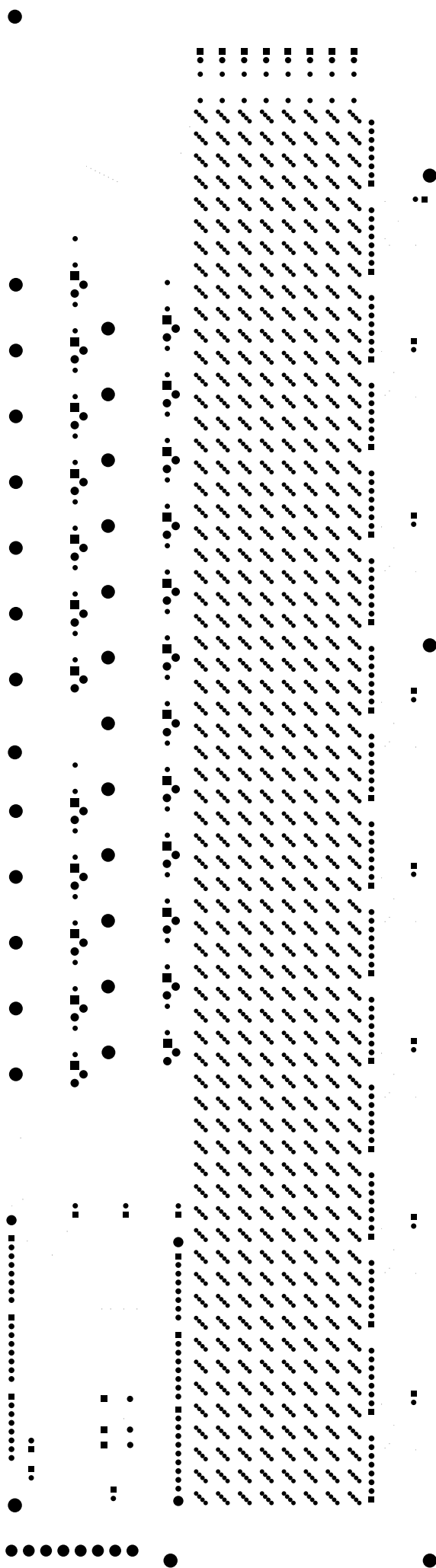




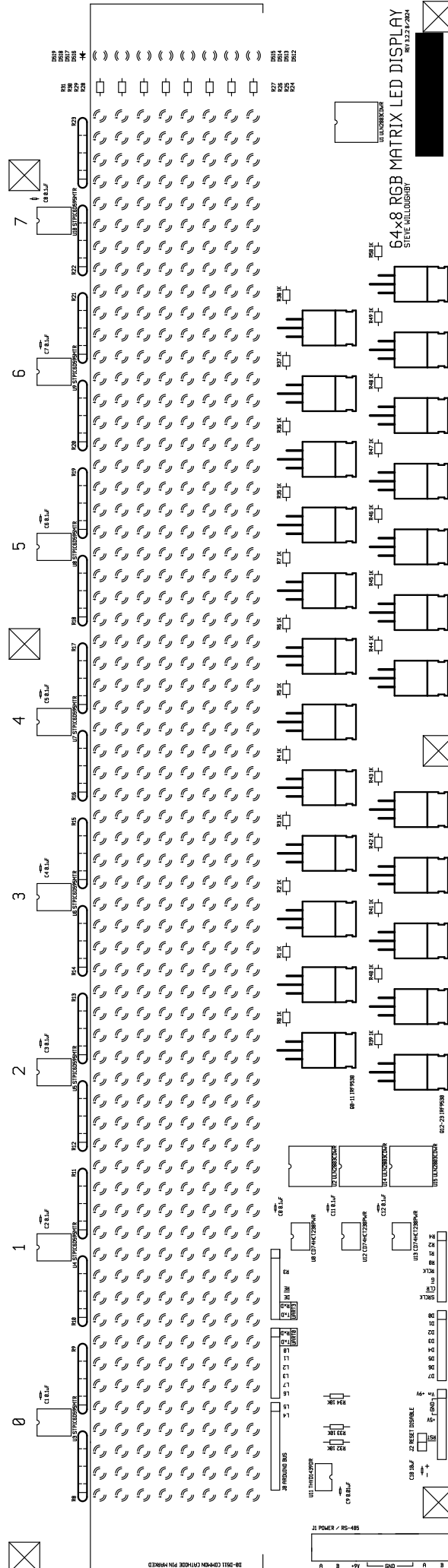




bottommask (mirrored), scale = 1:0.6
matrix64x8_rgb_300.pcb



topsilk, scale = 1:0.600
matrix64x8_rgb_300.pcb



bottomsilk (mirrored), scale = 1:0.600
matrix64x8_rgb_300.pcb



0
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3
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6
7



0

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2

3

4



5

6

7



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L7 GRN
L6 YEL
L5 YEL
L4 RED
L3 RED
L2 BLU
L1 BLU
L0 WHI

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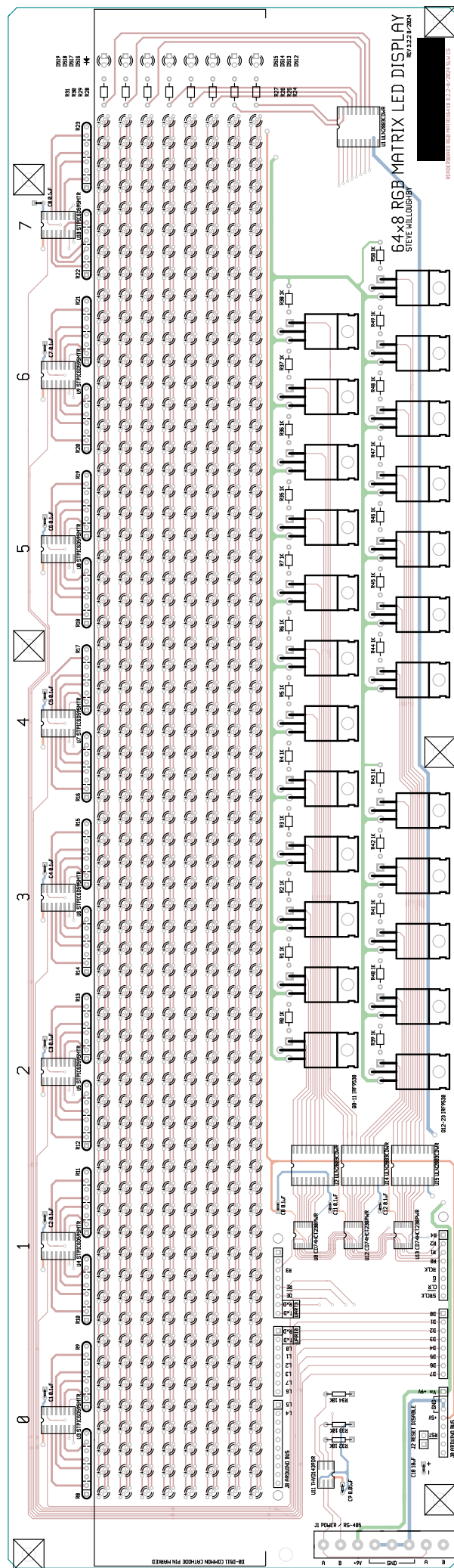
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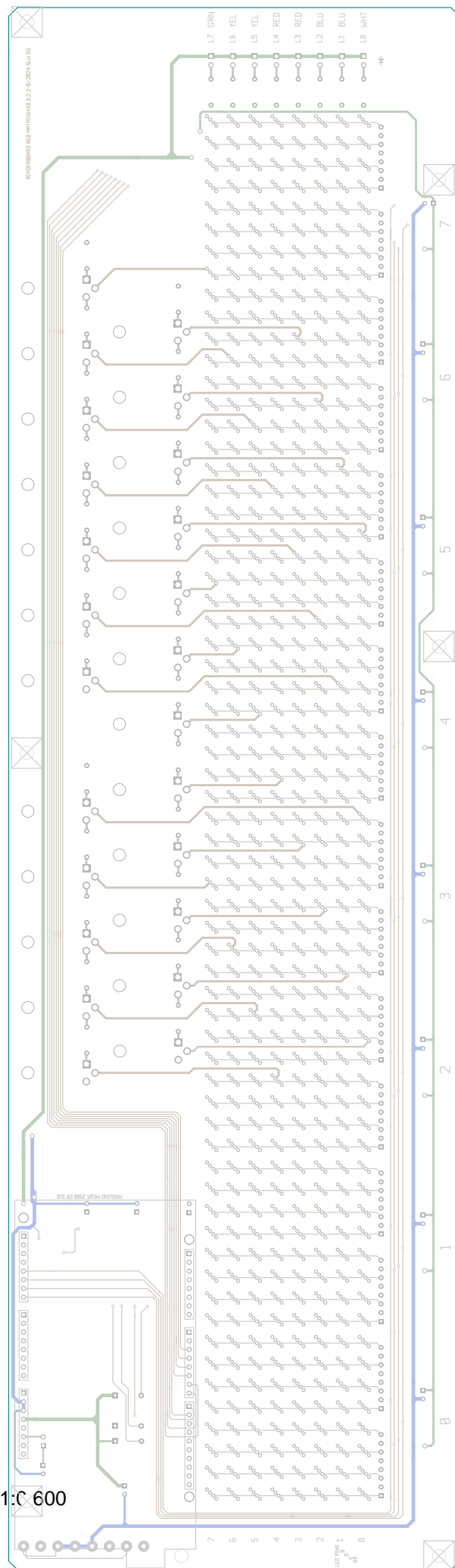
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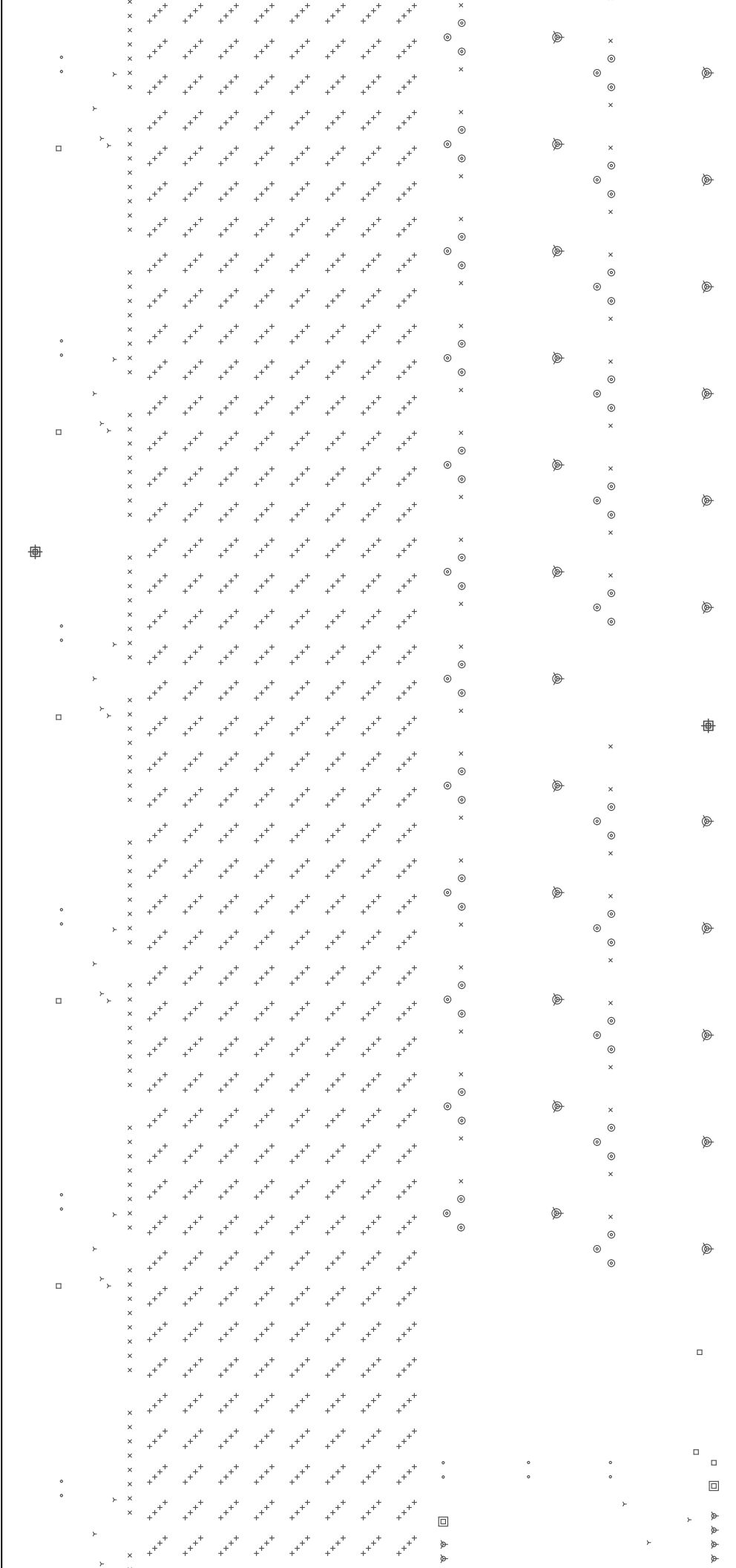
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topassembly, scale = 1:0.600
matrix64x8_rgb_300.pcb



bottomassembly (mirrored), scale = 1:C 600
matrix64x8_rgb_300.pcb





Board outline is the centerline of this path