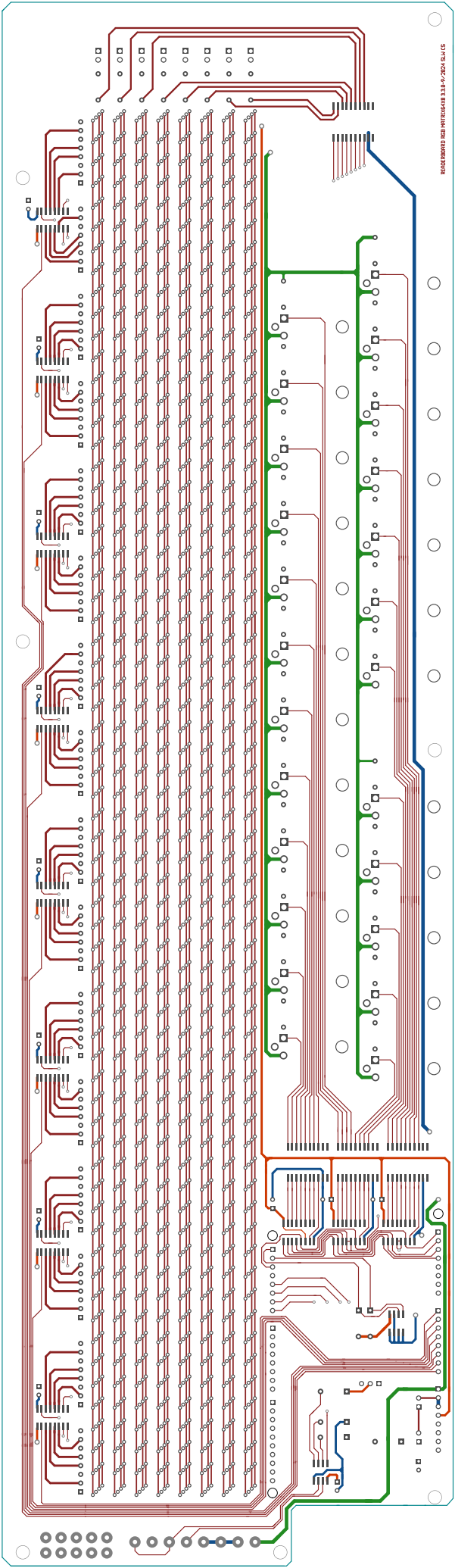


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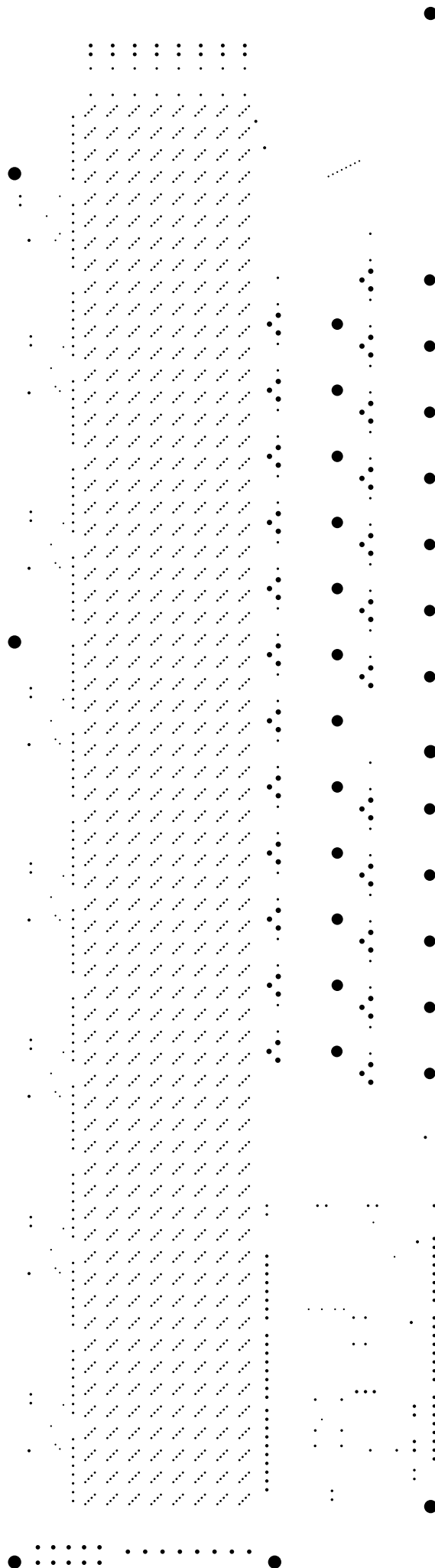
top, scale = 1:0.600
readerboard.pcb

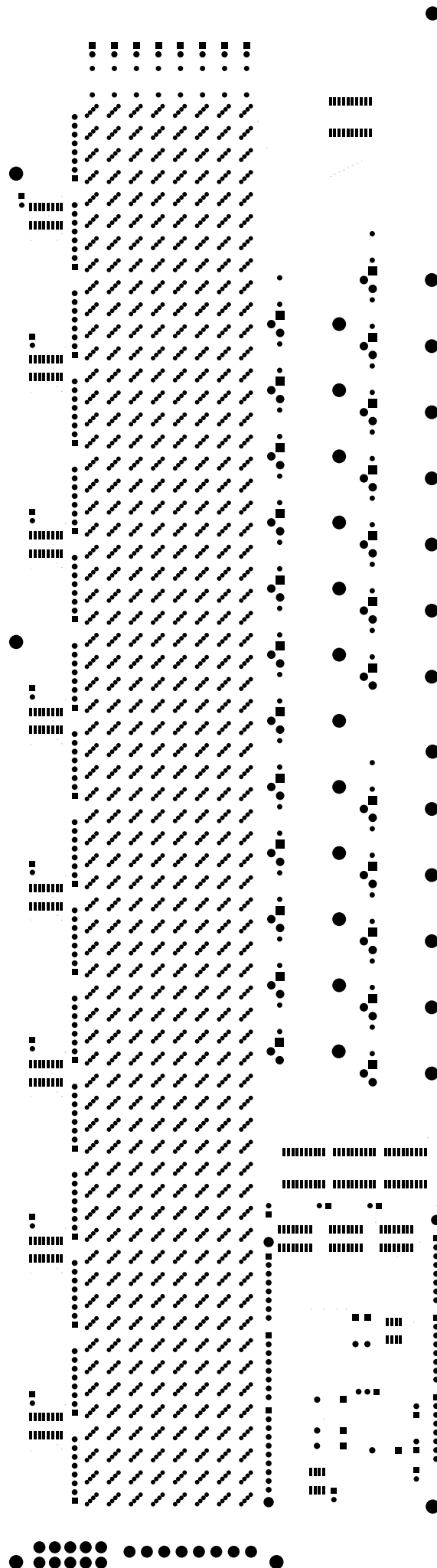


5/17/2019 10:24:10 AM
C:\Users\user\Documents\readerboard.pcb

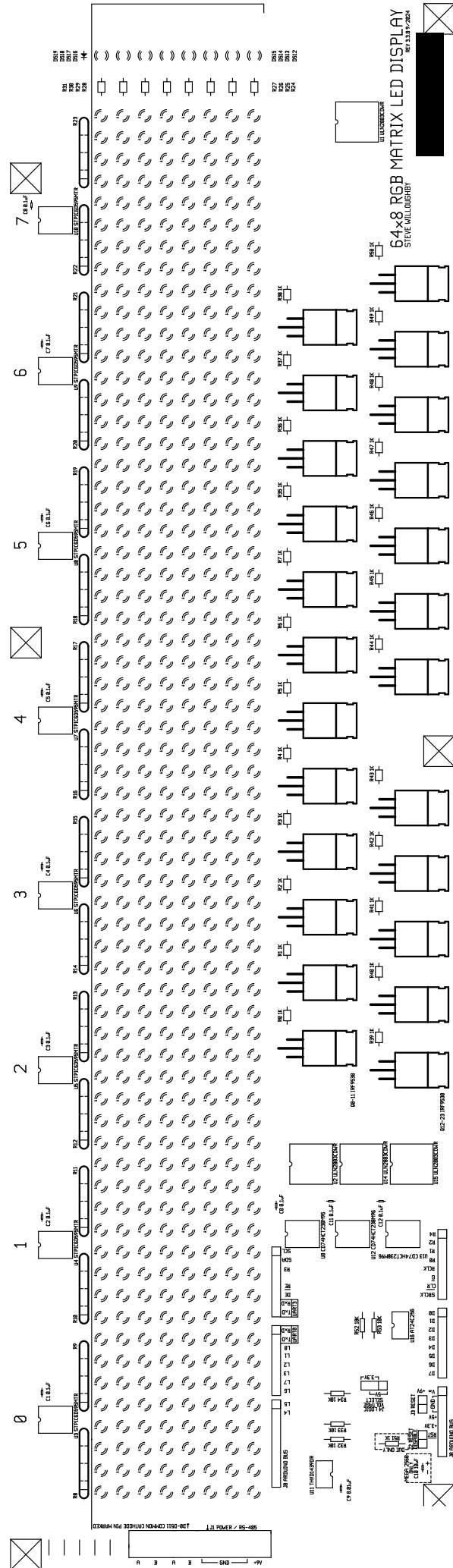


bottom (mirrored), scale = 1:0.600
readerboard.pcb






```
topsilk, scale = 1:0.600
readerboard.pcb
```



bottomsilk (mirrored), scale = 1:0.600
readerboard.pcb



ROUND PERIOD 2500 OR 10E

14 13 12 11 10 9 8 7 6 5 4 3 2 1 0

LEDBLANK
A₀ B₀ C₀ D₀ E₀ F₀ G₀ H₀



0

1

2

3

4



5

6

7



*

L7 WHI

L6 BLU

L5 RED

L4 RED

L3 YEL

L2 YEL

L1 GRN

L0 GRN

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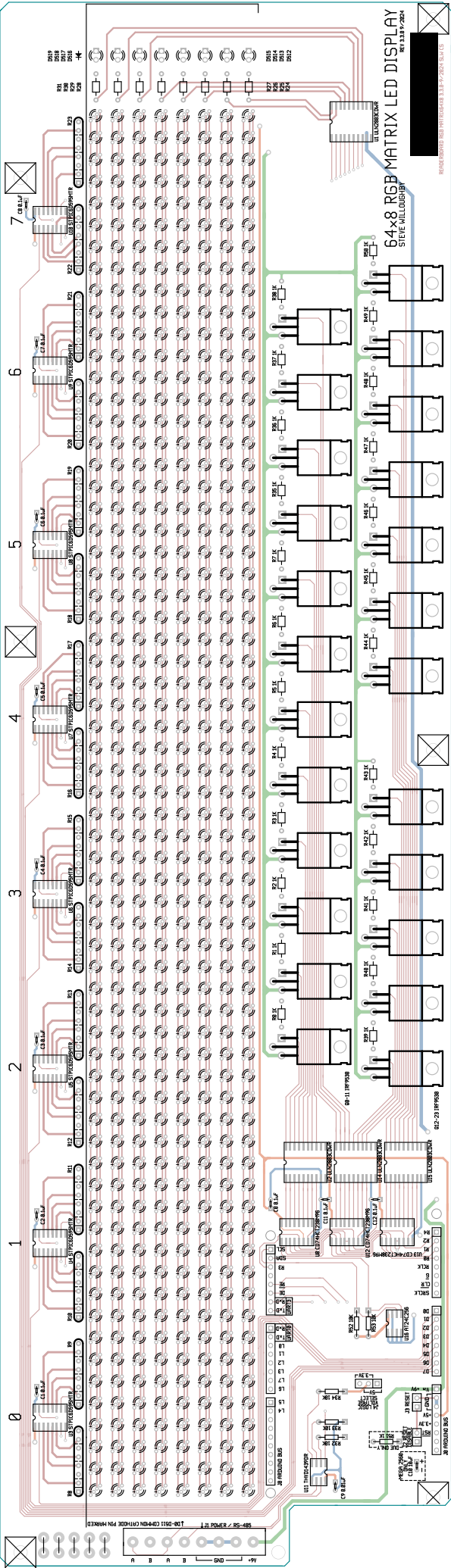
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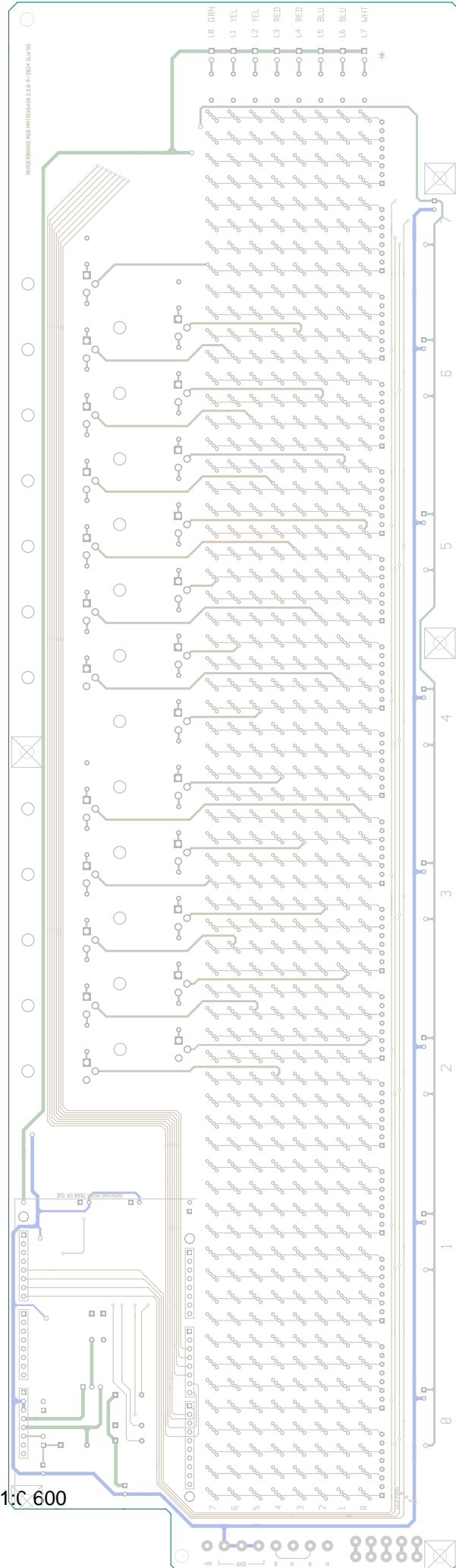
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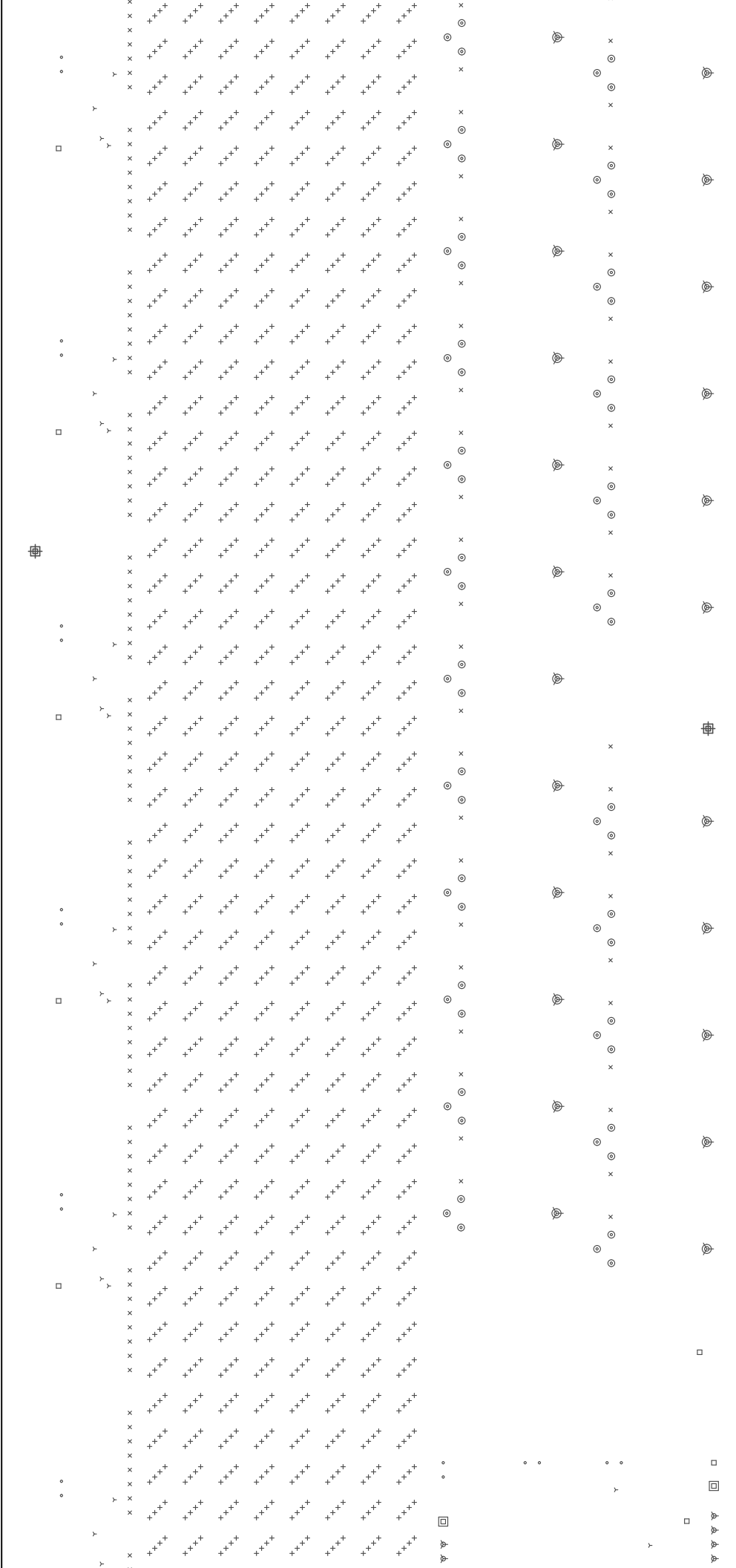
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topassembly, scale = 1:0.600
readerboard.pcb



bottomassembly (mirrored), scale = 1: C 600
readerboard.pcb





Board outline is the centerline of this path