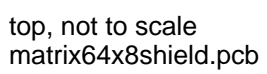


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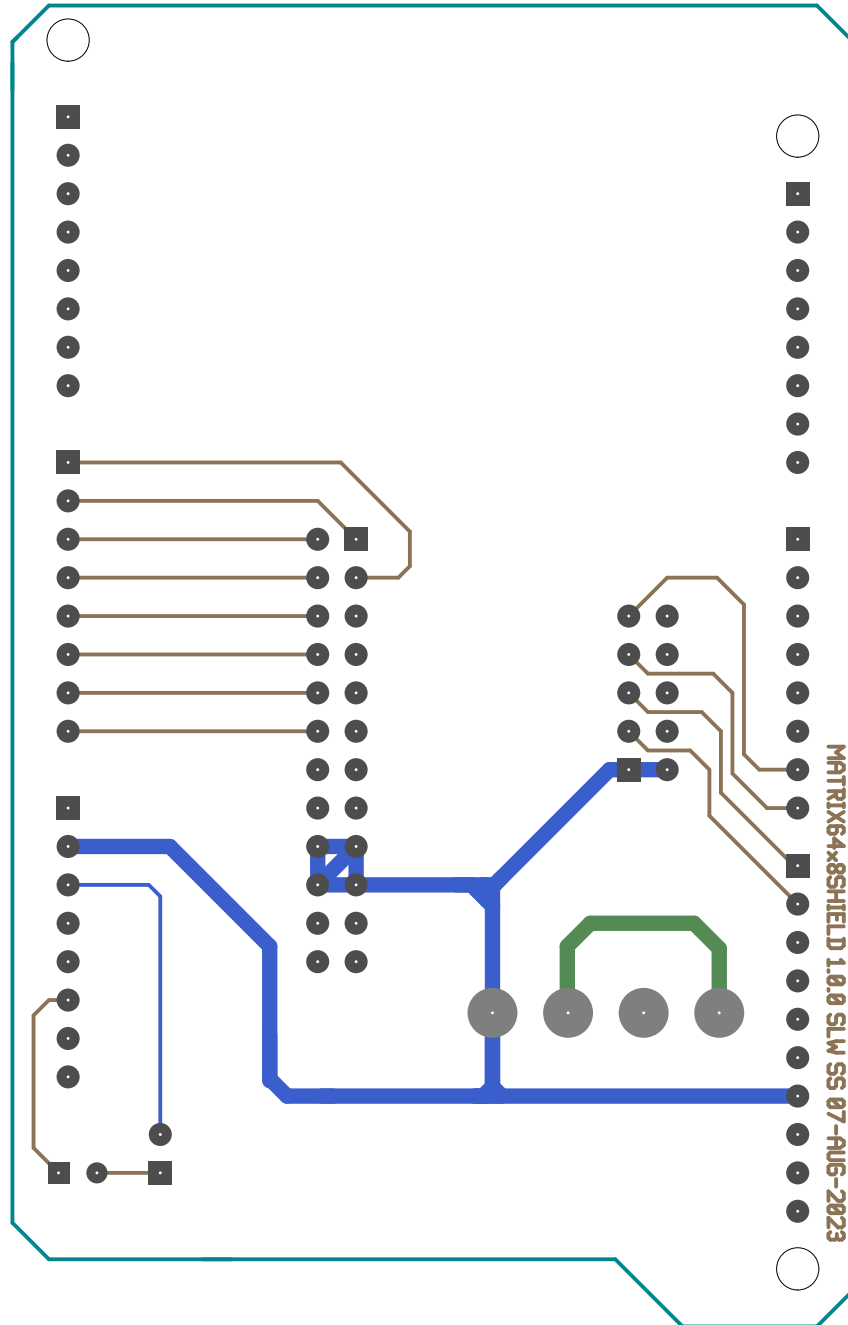
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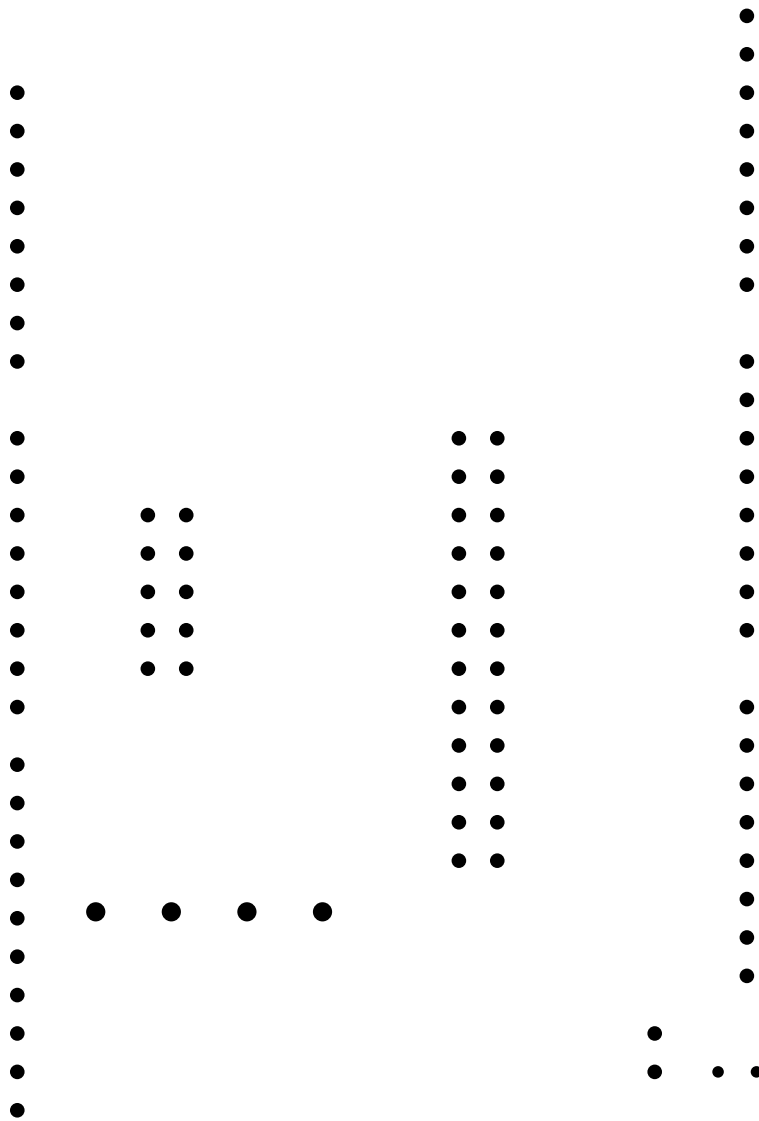
2. top
3. outline
4. ground
5. plated-drill
6. unplated-drill
7. componentmask
8. soldermask
9. topsilk
10. bottomsilk
11. topassembly
12. bottomassembly
13. fab

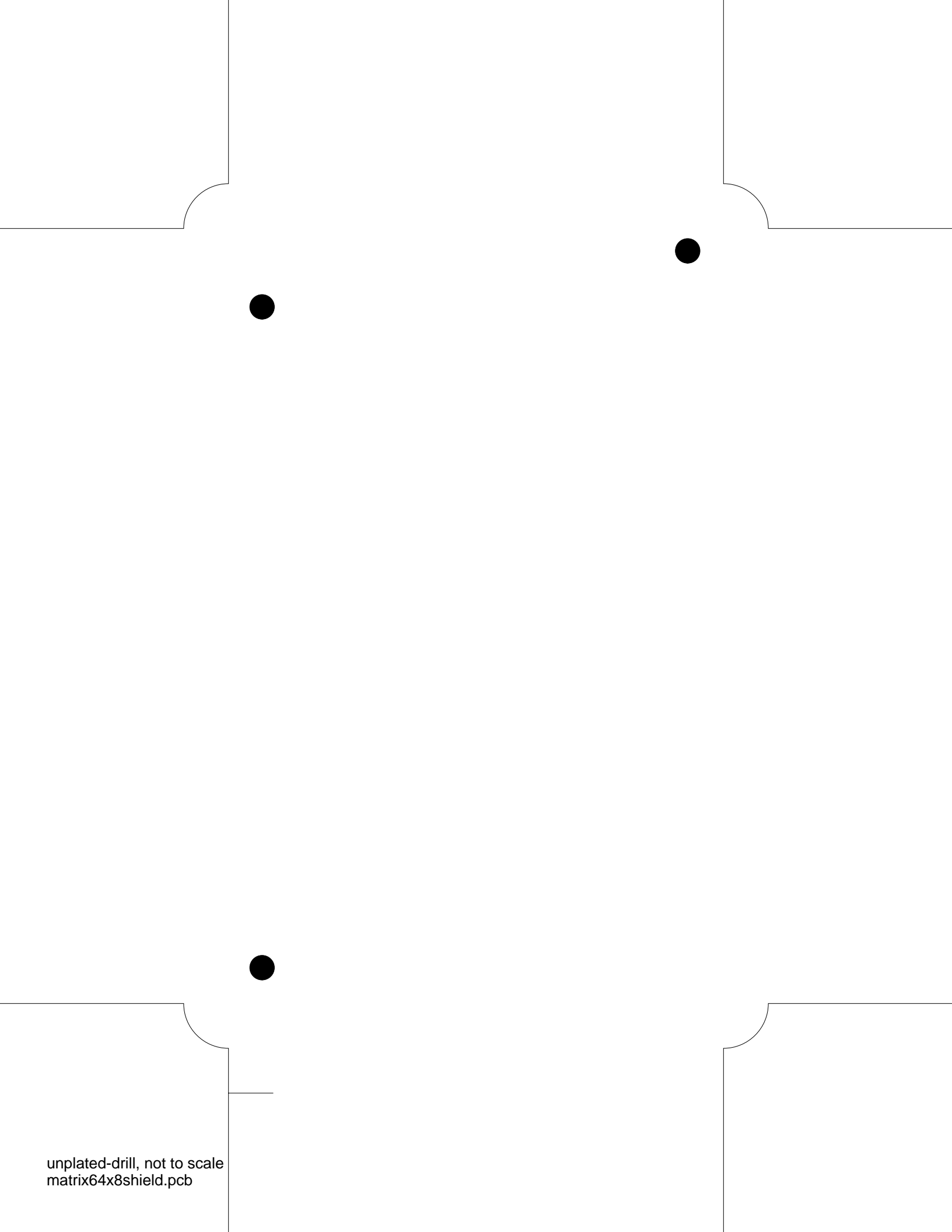




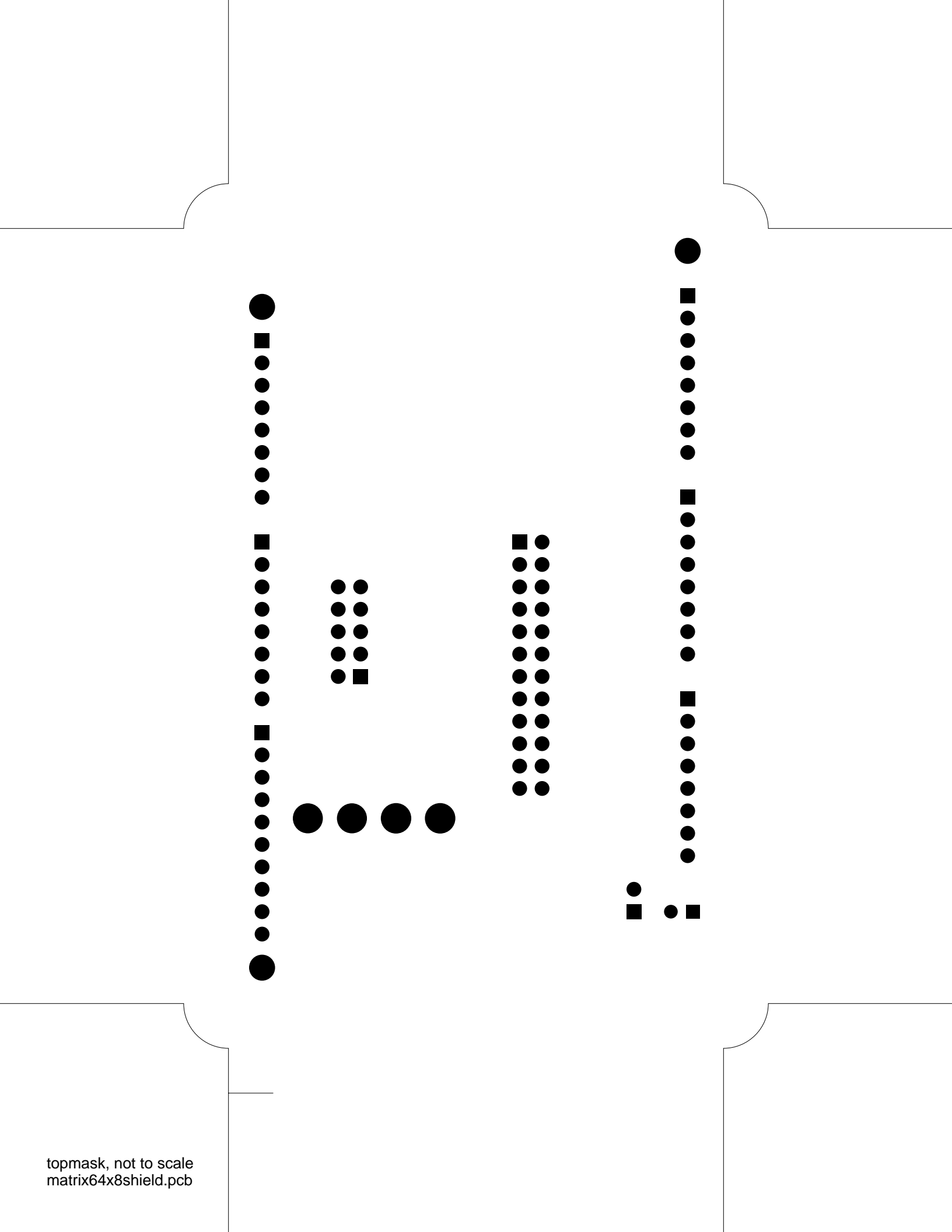
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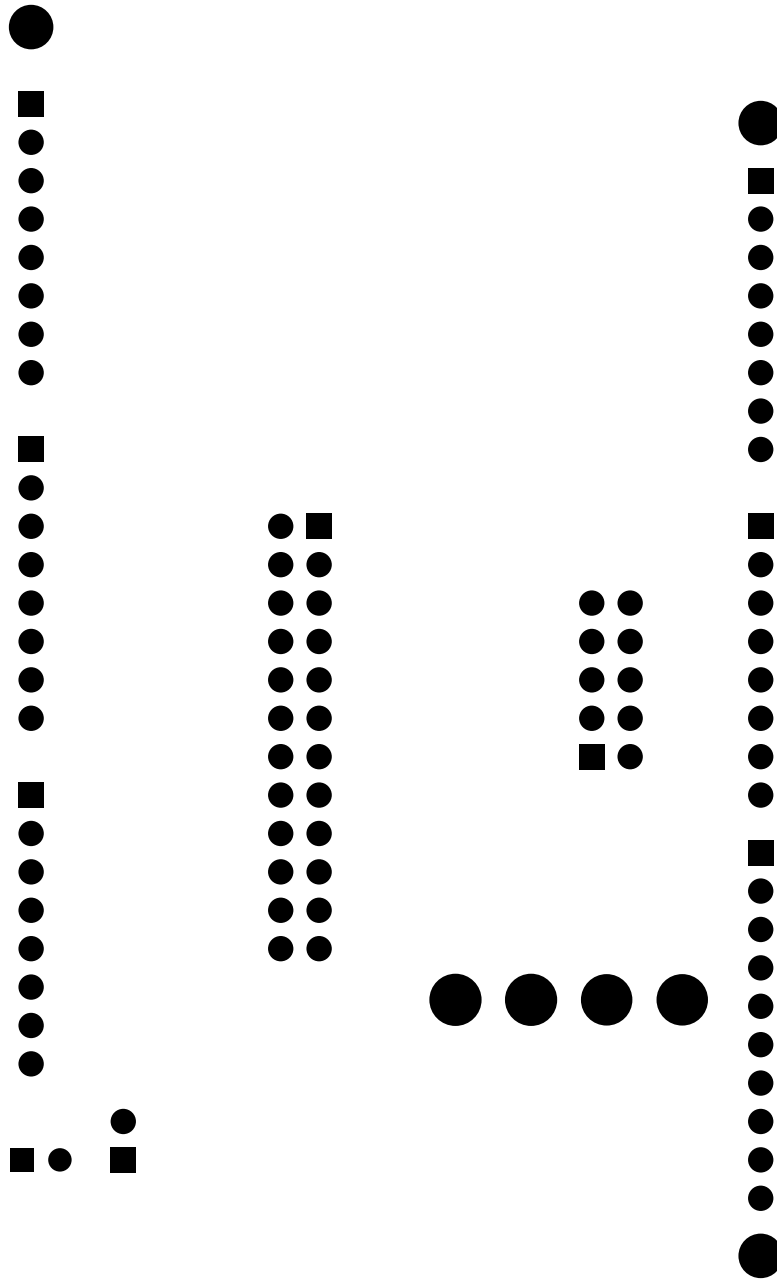




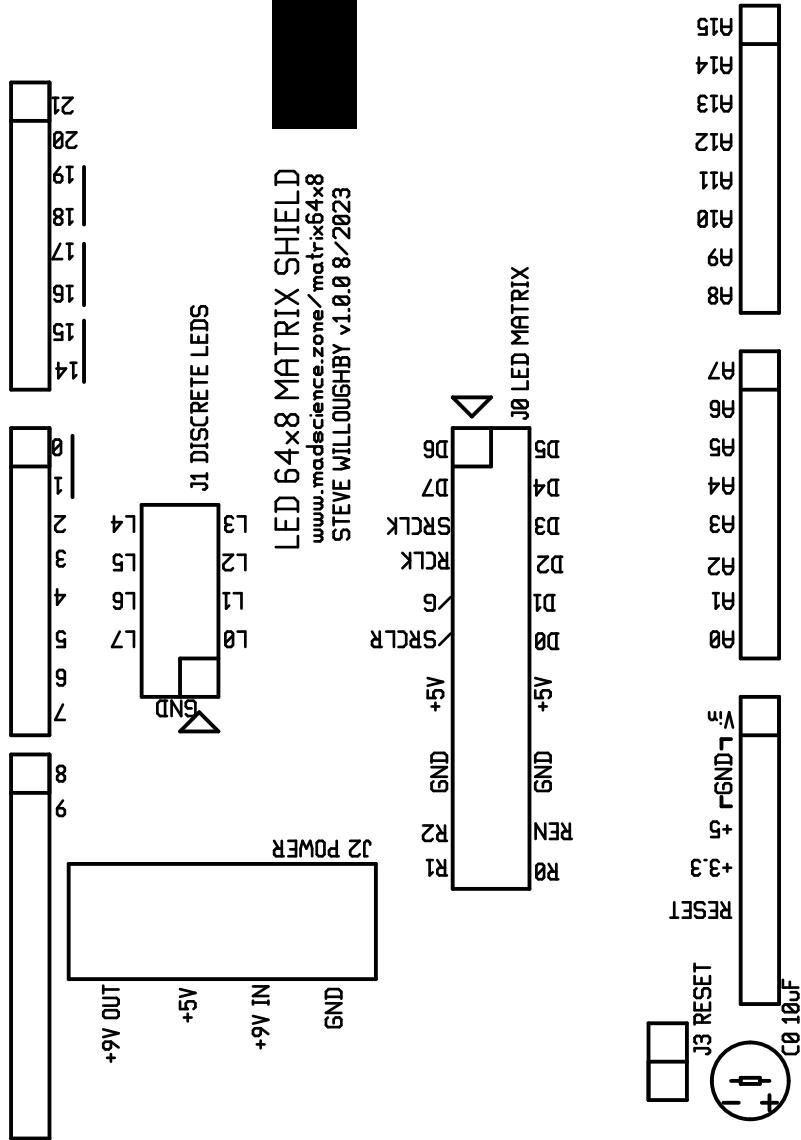


unplated-drill, not to scale  
matrix64x8shield.pcb

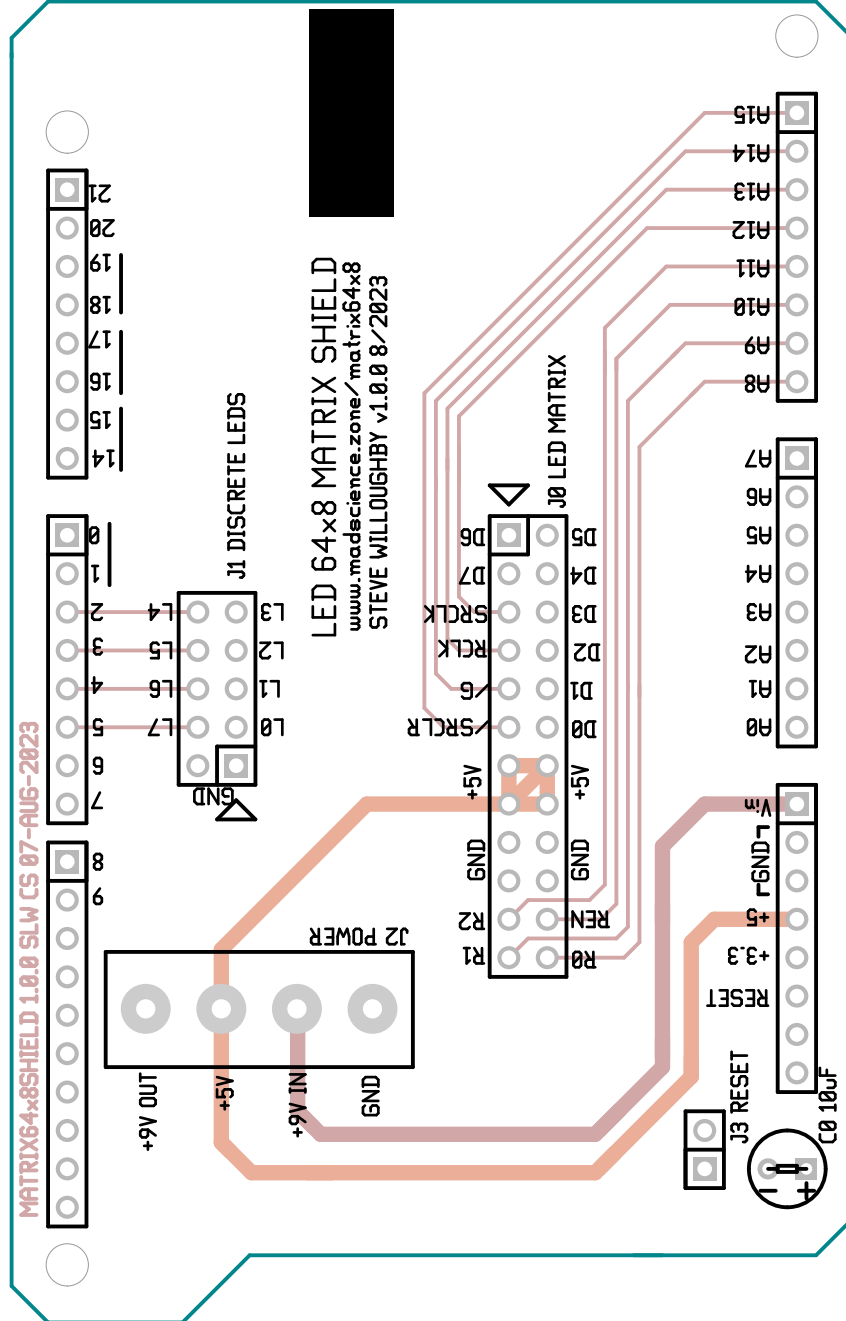


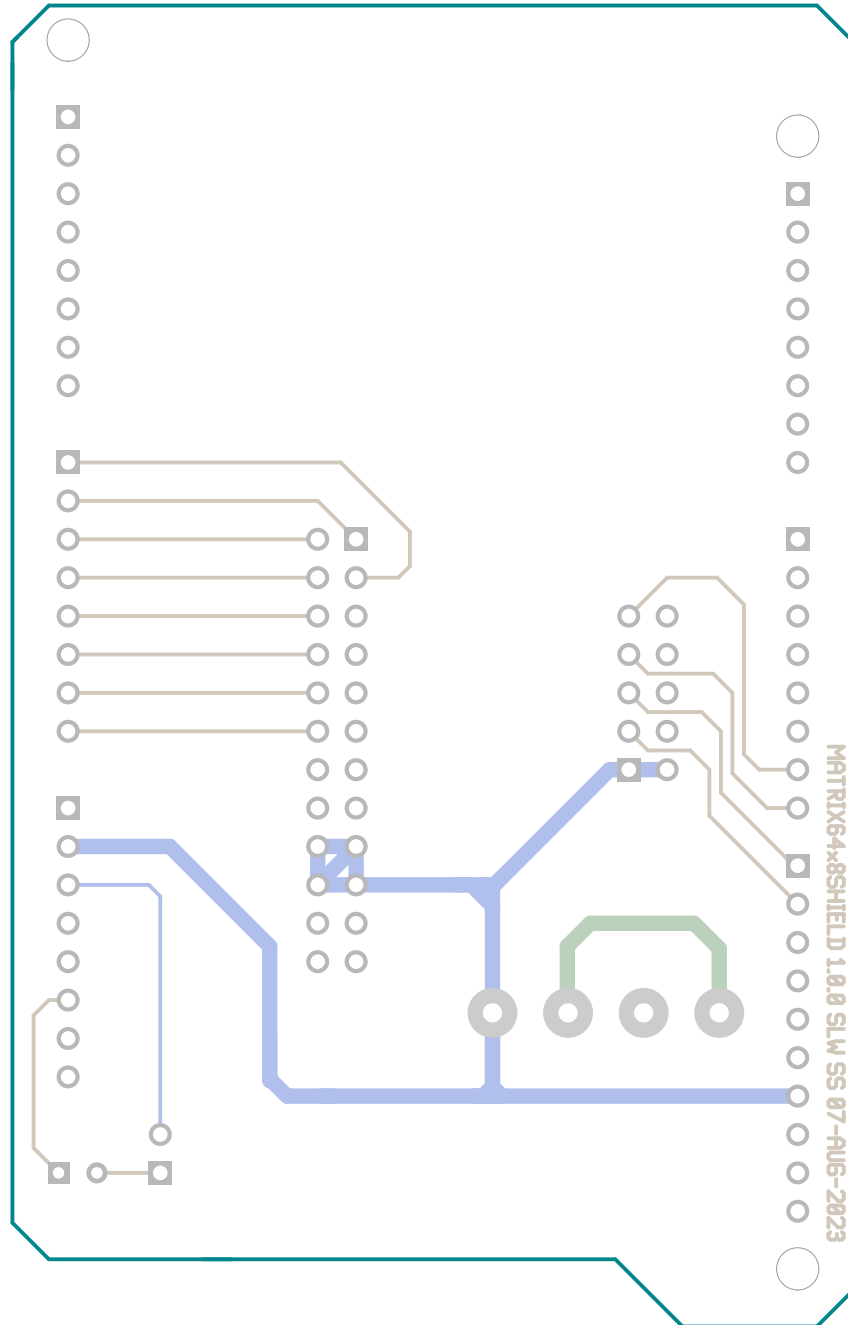










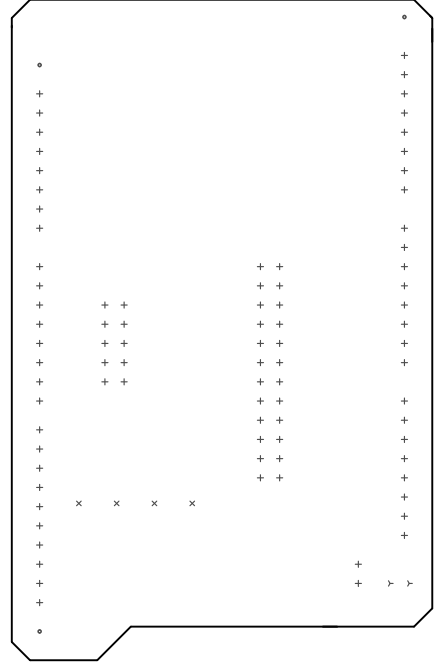


Symbol	Diam. (Inch)	Count	Plated?
γ	0.030	2	YES
+	0.038	86	YES
×	0.050	4	YES

Title: (unknown) - Fabrication Drawing  
 Author: Steve Willoughby  
 Date: Mon Aug 14 02:32:52 2023 UTC

Symbol	Diam. (Inch)	Count	Plated?
γ	0.030	2	YES
+	0.038	86	YES
x	0.050	4	YES
•	0.110	3	NO

Title: (unknown) - Fabrication Drawing  
Author: Steve Willoughby  
Date: Mon Aug 14 02:32:52 2023 UTC



Board outline is the centerline of this path