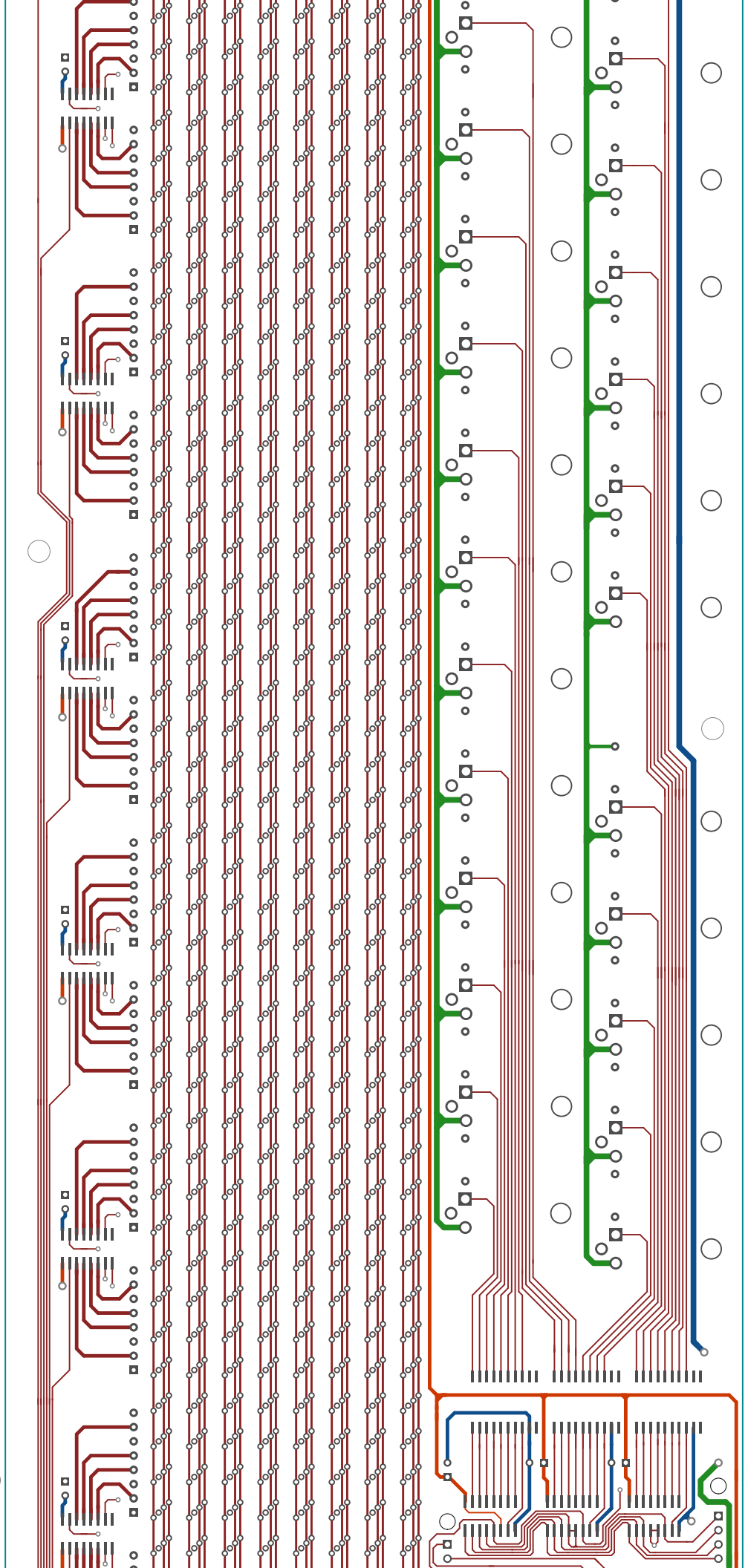


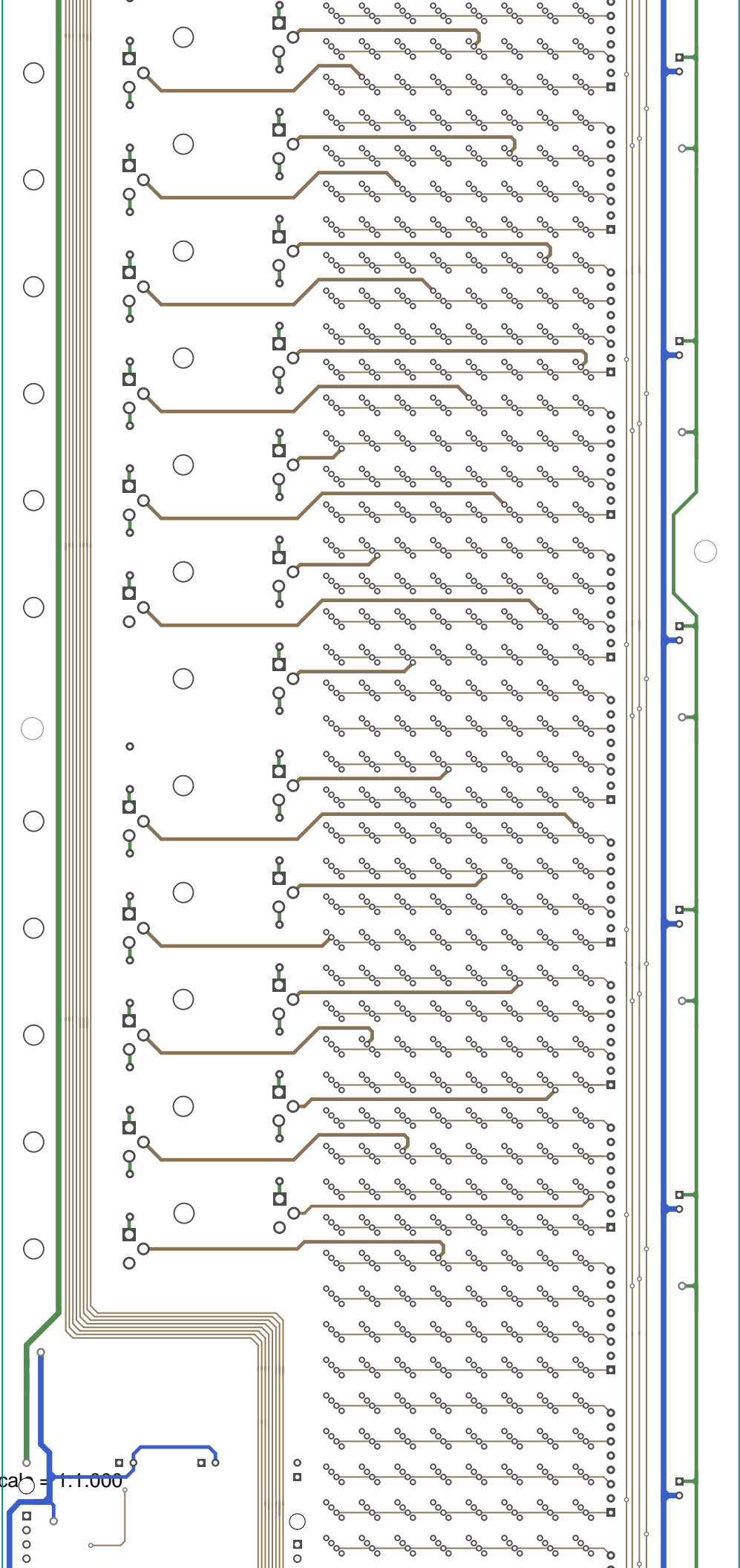
1. Table of Contents (This Page)

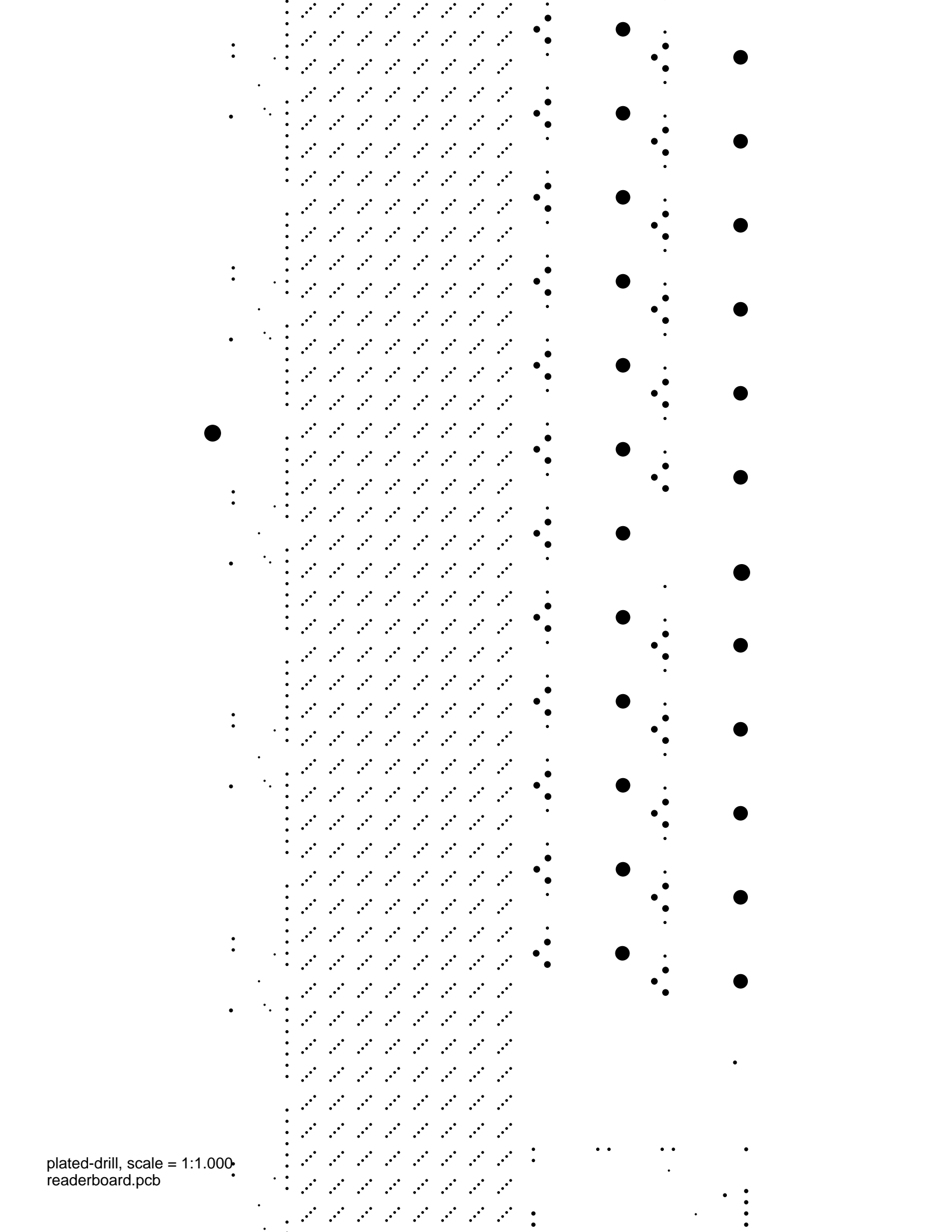
Created on Mon Sep 30 01:03:09 2024

2. top
3. ground
4. outline
5. plated-drill
6. unplated-drill
7. componentmask
8. soldermask
9. topsilk
10. bottomsilk
11. toppaste
12. topassembly
13. bottomassembly
14. fab



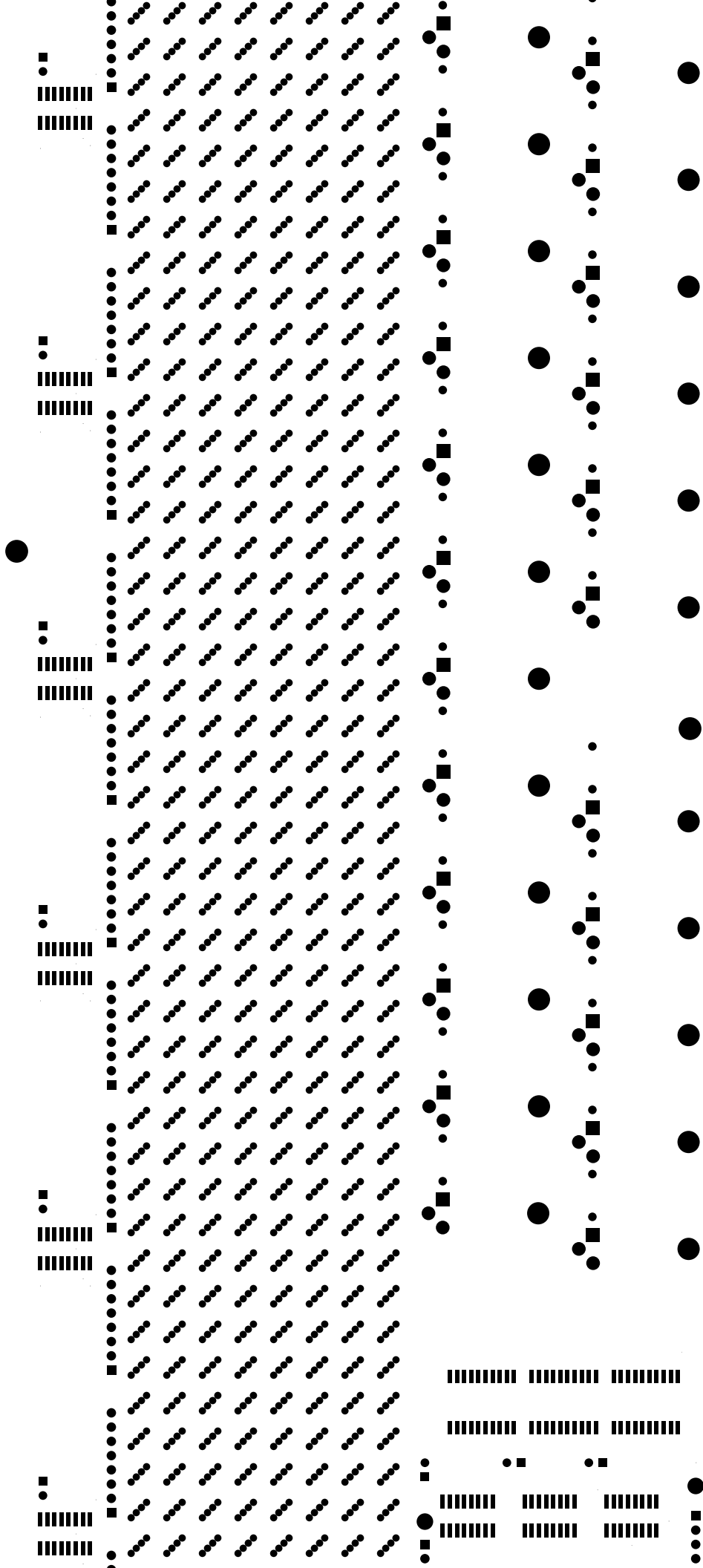
bottom (mirrored), scale = 1:1.000
readerboard.pcb



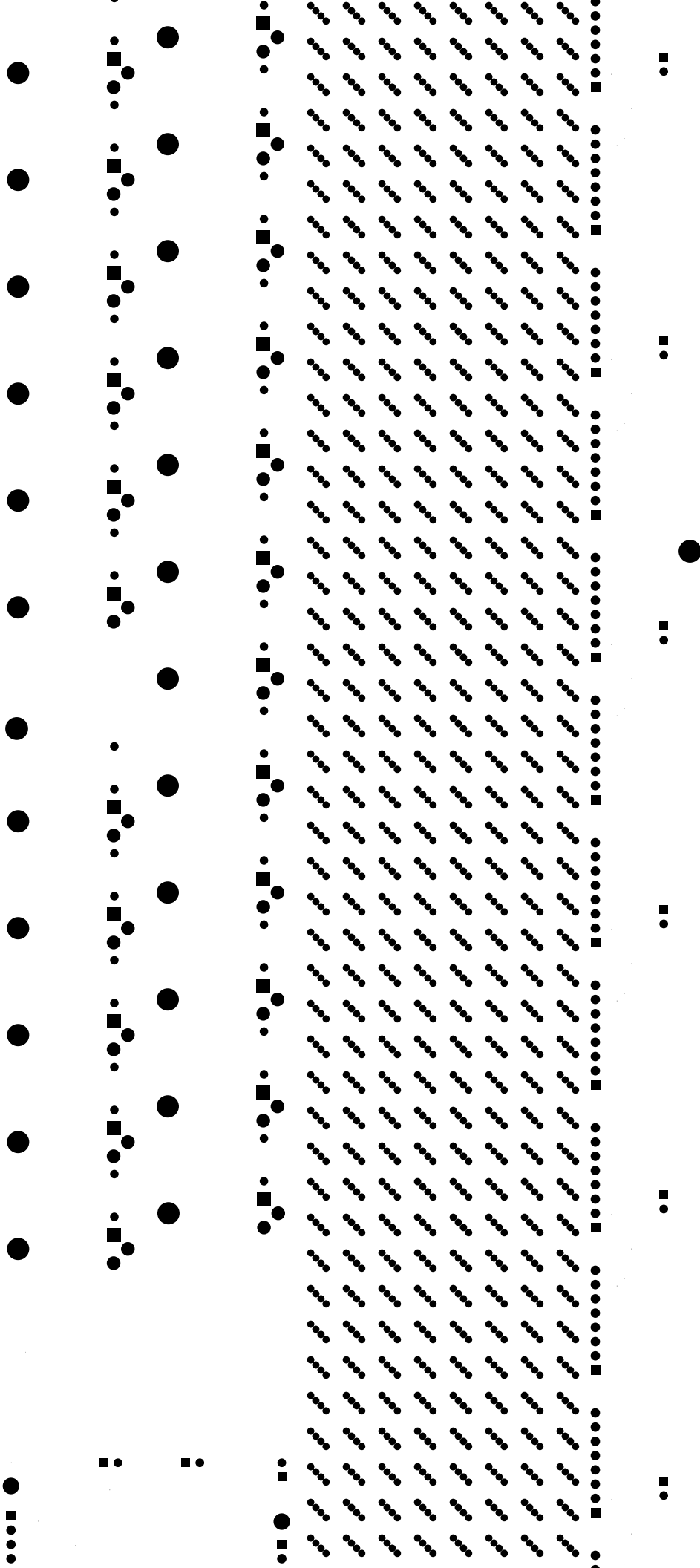


plated-drill, scale = 1:1.000
readerboard.pcb

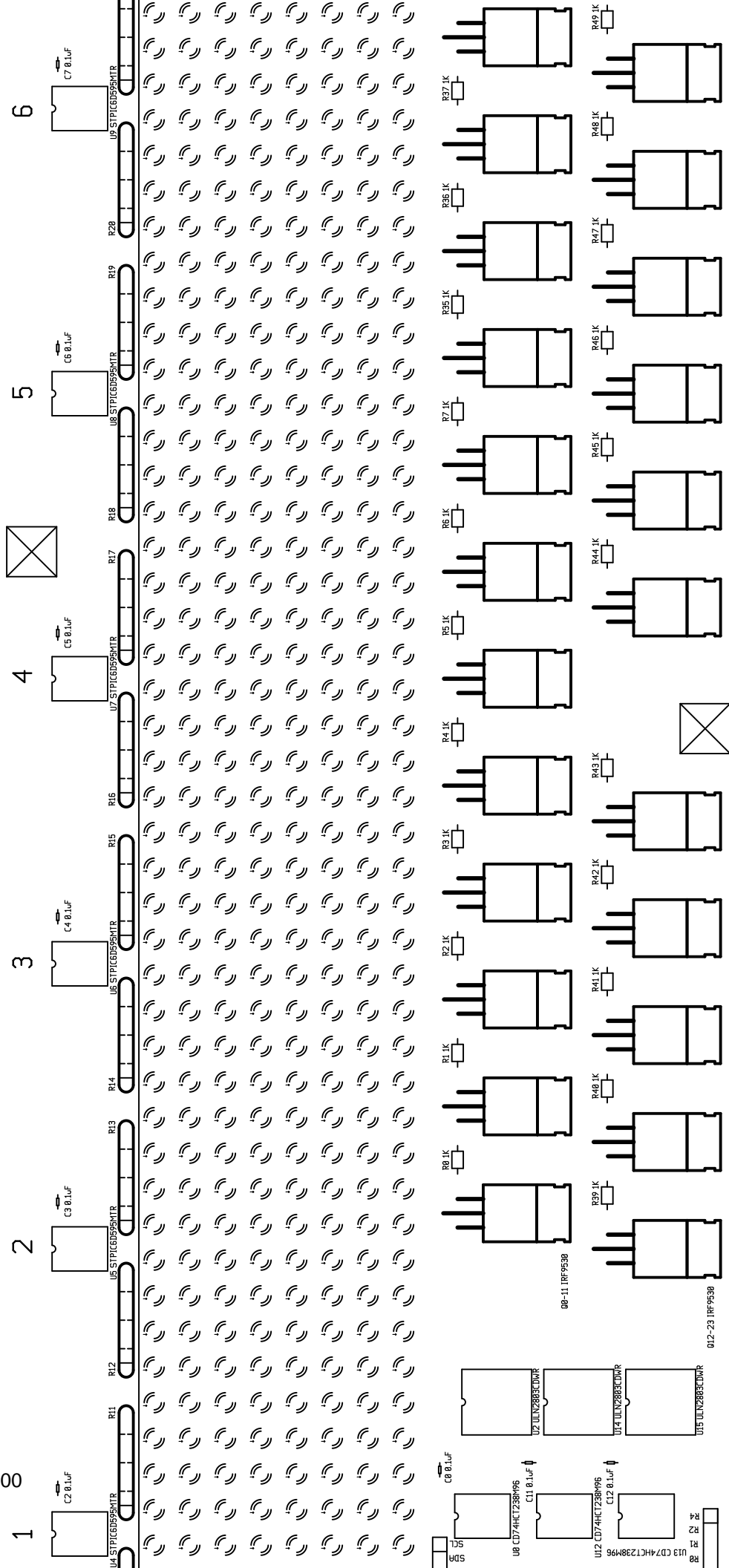




topmask, scale = 1:1
readerboard.pcb




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topsilk, scale = 1:1.000
readerboard.pcb
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2

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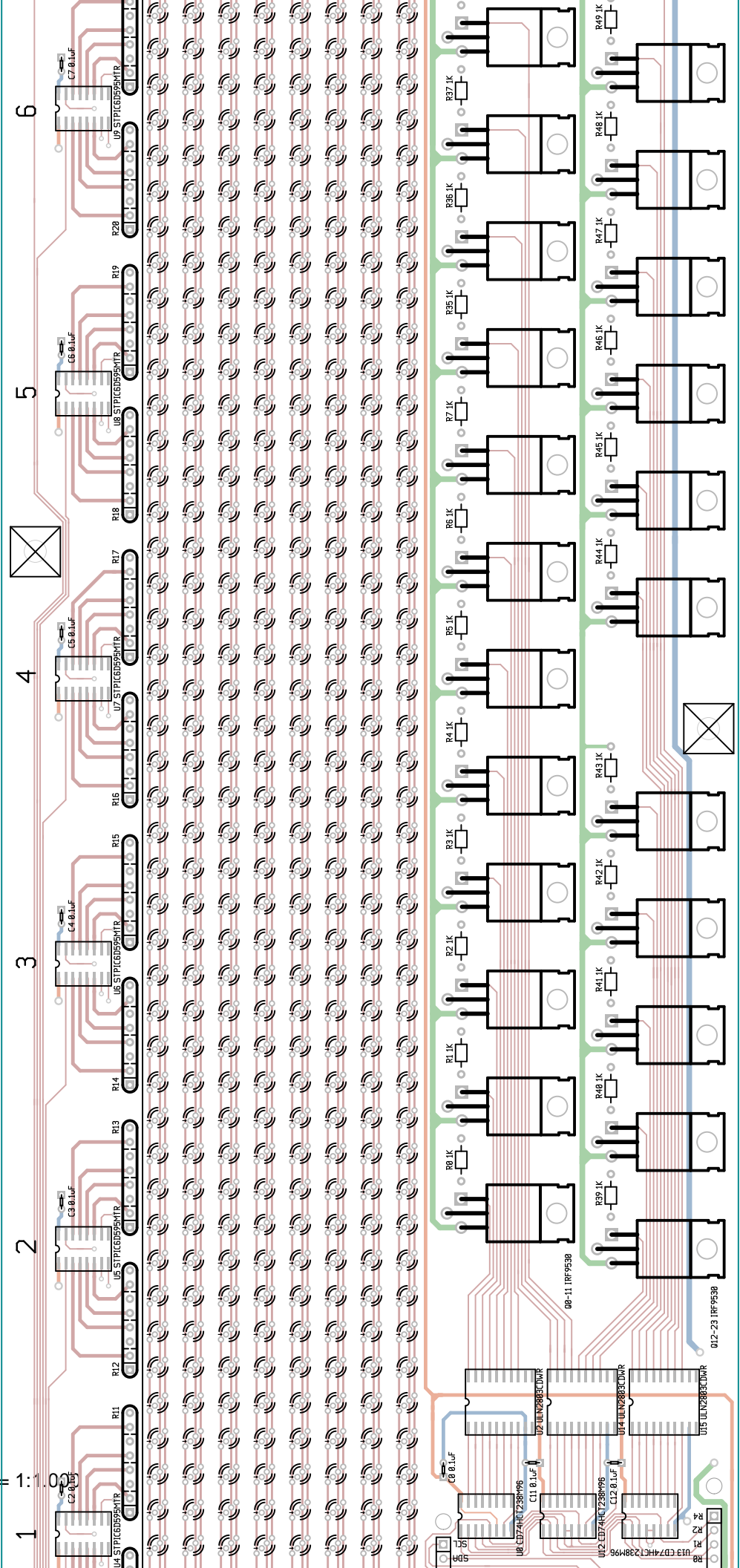
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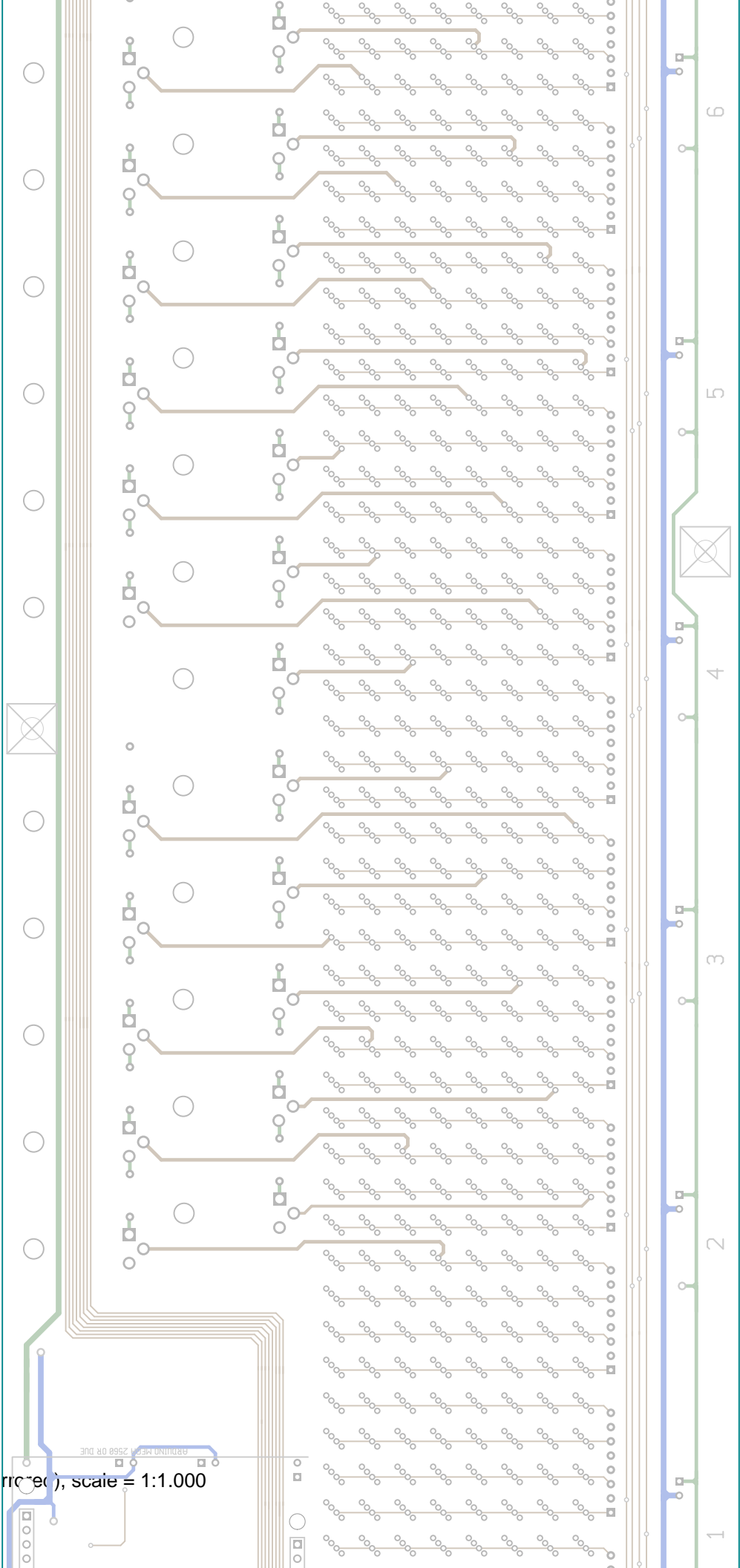
||||||| ||| |||

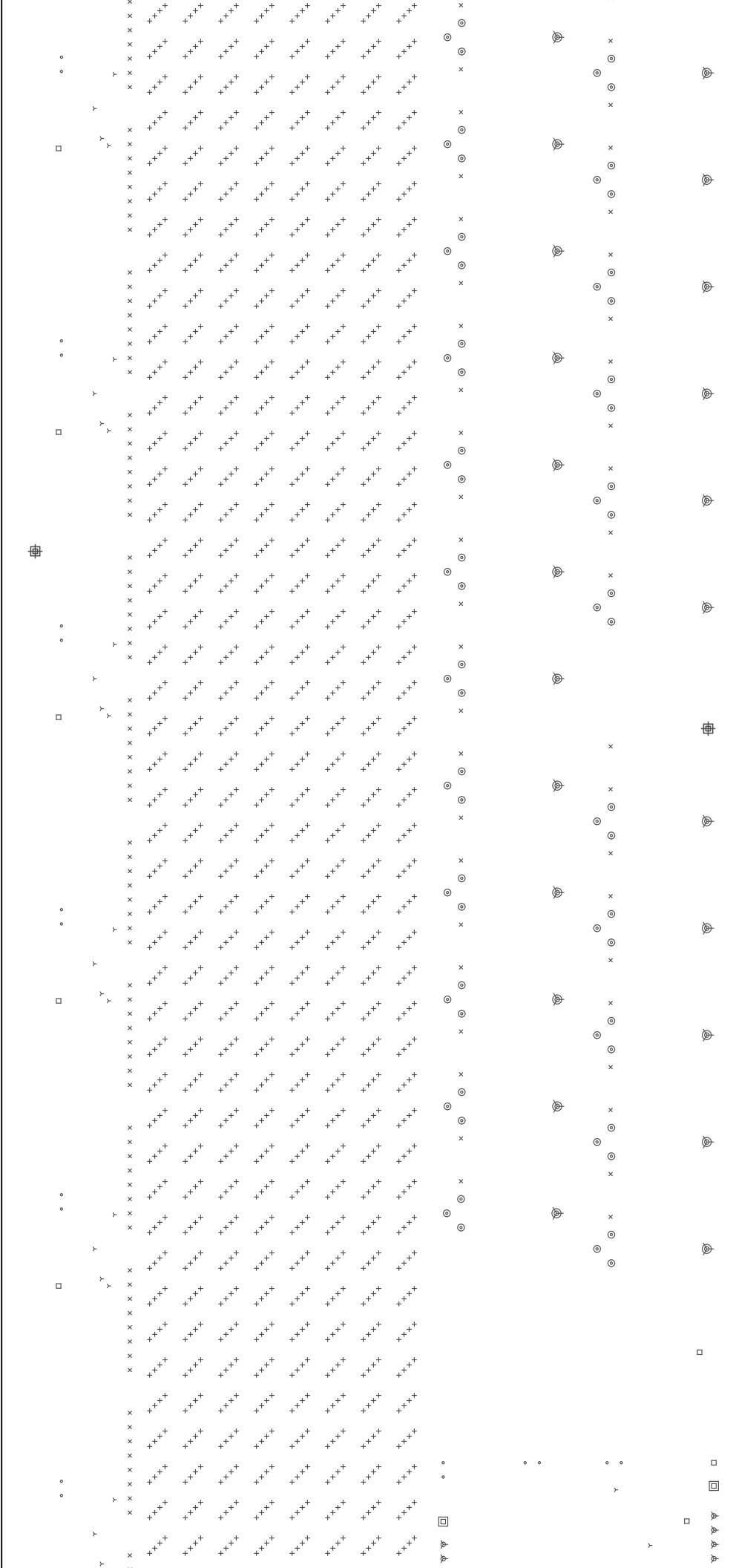
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bottomassembly (mirrored), scale = 1:1.000
readerboard.pcb





Board outline is the centerline of this path