# Rohith R

Nanyang Technological University School of Computer Science Block N4, 50 Nanyang Avenue

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### Education BITS Pilani, India

Bachelor of Engineering (Hons.), Computer Science, 2017.

Cumulative Grade Point Average (CGPA): 7.145

Fields of interest: Operating Systems, Computer Architecture, High Performance and Parallel Computing

# Bachelor Thesis Simulation and Scheduling of Many-Core Architecture

Supervisor: Dr. Lam Siew Kei, Nanyang Technological University Research involves finding efficient scheduling algorithms taking into consideration the effects of difference in the performance of the cores and their reliability.

rohithr31@gmail.com

https://rohithr31.github.io

### Work Experience

#### Intuit IDC, Bangalore

Software Developer Intern

Project: Enabling Drag-and-Drop in Multi-Window mode, android-N Collaborated with Intuit and Google engineers to get review of work

## State e-gov agency, Chennai

Software Developer Intern

Project: Track the bus timings, frequency and routes in real time and update the user using an app

### **Projects**

#### **BITS-OS**

Basic OS from scratch, for learning low level operations of OS Implemented VGA, keyboard, hardware clock drivers along with mini shell

#### **MIT-JOS**

MIT-6.828 Postgraduate OS Labs

Implemented Paging, Memory and Process Management

#### Improve efficiency of C++ Thrust Library

Mentor: Dr. Santonu Sarkar — Collaborators: Siemens Research Lab Introduced the concept of shared memory as software controlled cache Improved efficiency and decreased LOC

#### MIPS Architecture and Cache design

Mentor: Dr. Biju K.R.

Implemented VLIW architecture with 5 stage pipeline

Studied and implemented advanced caches like Phase, Way-Prediction

and Way-Halting caches

Teaching/ Department of Computer Science, BITS Pilani

Mentoring Teaching Assistant, Data Structures and Algorithms, Spring, 2016

Technology Incubator Programme, BITS Pilani

Project Mentor, Operating System from Scratch, Fall, 2015

Project Mentor, Intro. to Competitive Coding, Fall, 2014 and Spring, 2015

Skills C++/C, Java (Android), Python, Cuda, Verilog, PHP & MySQL

Co-curricular Vice President, ACM Chapter, BITS Pilani Goa Campus

Developer @ Mobile App Club, BITS Pilani Goa Campus

References Dr. Lam Siew Kei

School of Computer Science

Nanyang Technological University

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Dr. Biju K.R.

Dept. of Computer Science and Information Systems

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Dr. Santonu Sarkar

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