Schematic Design Of Transistor Level XOR & XNOR Gate.

**DESCRIPTION**

XOR(exclusive OR)

For a 2 input XOR,the output of the gate is low when both the inputs are same(either both low or both high).The output is high if one and only one of the inputs is high.The function is addition modulo 2 and hence the gate is used in half adder

XNOR(exclusive OR)

For a 2 input XNOR,the output of the gate is high when both the inputs are same(either both low or both high).The output is low if one and only one of the inputs is high.

**PRERIQUISITIES**

This experiment is mainly written in JS, HTML and CSS.

The foreground of this experiment is that i took a lot of effort and found all types of codes which is available in internet which i took it as reference and wrote it on my own.

**PROCEDURE**

It is in the codes folder.

To analyse the code please go to the SRIP folder.

**RESULT**

The graph is displayed and works as XOR and XNOR Gate.

**ISSUES**

If there is any issue occurs please mention it in the issues.

**CONTACT**

For clearing the doubts, raise an issue or contact me through mail : [vishwanimadasu2001@gmail.com](mailto:vishwanimadasu2001@gmail.com)

Thank you.

To take part in quiz got to quiz folder which is available in SRIP folder.