



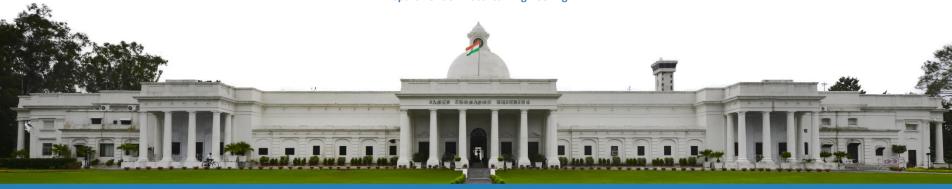


Charging Infrastructure

Lecture-6
Revisiting Diode Bridge rectifier-II

Dr. Apurv Kumar Yadav

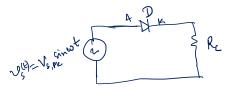
Department of Electrical Engineering



1-\$ input, 1-\$ AC-DC converter 3-\$ juput, 3-\$ AC-DC converter

1-4 Diode Bridge Rectibier (uncombolled Rectibier) (i) Halb-bridge Rectibier (i) full-bridge Rectibier

[1-\$ Half-bridge rectifier with Resistive load



D' turns on when the 'vs' is in the positive half, it is reverse biased when 'vs' is in the vegative half upde.





V5 AS,PK w= 27 bs (bs= supply frequency) To Ybs on to 2x V2= { V5, , V570 VI average value ob Dd, Vd, avg = + (1/5, pk sin wt)-dt = Vs, plc [- coswt] } $= \frac{\sqrt{5}, p_{L}}{\sqrt{1}} \left(- \left(\cos \frac{\omega T}{2} - \cos 0 \right) \right)$ = Vs, ple (- (con T- cono)) = Vs. pre [- (-1-1)] Vd, avoz =



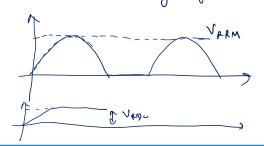


$$\dot{x}_{s} = \begin{cases} \dot{x}_{d} & ; \forall_{s} > 0 \\ 0 & ; \forall_{s} < 0 \end{cases}$$

Diode selution

v/g ratings

- Peak repeatitive reverse voltage (NAPM)
- Peak reverse voltage
- Reverse voltage or Dr. Blodaing rollage



Current ratings

- → Average forward wwwent, I = cm)

 → Average rectified borward wwent
- Repeatitive Peak borward convent, Iran
- Non-Repeatitive Peak forward wwent, Irsm

Diode selection

Voltage rating

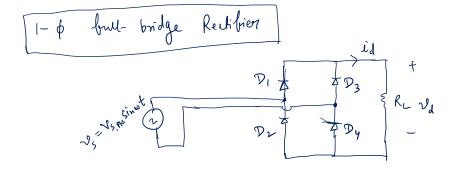


The power is being delinered to the resistive load only in Positive half yele

The output is pulsating in nature

Thereage output voltage is proportional to Vs, px (which is peak of the Applied rollage)

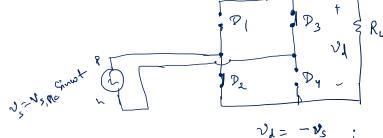
The warest drawn is not continuous







When is in positive half upde (4,70) D, & Dy gets borward biased D2 & D3 get reverse biased When V_s is in negative half uscle (V_s Lo), D_2 of D_3 is borward biased, D_i L D_y is reverse biased

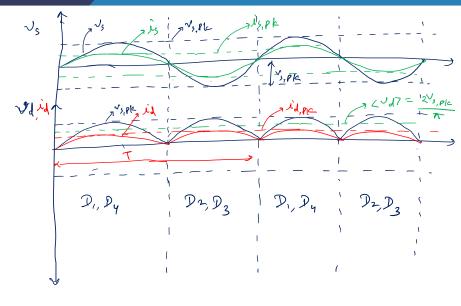






$$\frac{1}{4}, pc = \frac{V_{5}, pc}{R_{L}}$$

$$\frac{1}{4}, pc = \frac{V_{5}, pc}{R_$$







Diode Selection

V_{RRM} = V_{d,PK} = V_{s,PK}

Average werent, T_F(ong) =
$$\frac{1}{T}$$
 $\int_{R_{b}}^{V_{s,PK}} \frac{V_{s,PK} \cdot sinwt}{R_{b}} \cdot dt$

$$\begin{array}{rcl}
 & = & \frac{v_{s,pk}}{\pi R_L} \\
\hline
T_{F,qk} & T_{R_L}
\end{array}$$

$$\begin{array}{rcl}
 & = & \frac{v_{s,pk}}{\pi R_L} \\
\hline
T_{r,qk} & T_{r,qk}
\end{array}$$

$$\begin{array}{rcl}
 & = & \frac{v_{s,pk}}{\pi R_L}
\end{array}$$





9 Merene

7 The output voltage is uncontrolled, it only depends on input Actollage (Vs. Plc)

1 The input wevent is ginusoidal in natural

1 Power is delinered in both the wycle (*ve &-ve) to R...

1 Power is delinered in both the wycle (*ve &-ve) to R...

1 The output voltage is not contant, it is rectified DC voltage

2) Vd(t) = [Vs. t)

it is continuously vorying





Thank You





