

# ATIGADDA MADHAN KUMAR

## Design and Verification Engineer

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India

## EDUCATION

### M.Tech (VLSI)

#### Gitam University

2024 - 2026 Bangalore

### B.Tech

#### AP IIIT RGUKT RK VALLEY

2020 - 2024 Kadapa

- Electronics and Communication Engineering

## KEY ACHIEVEMENTS

### NMMS SCHOLARSHIP

Prepared myself for 1 month on solving Aptitude, Reasoning, English Vocabulary, Verbal Ability etc and then resulted as One of the toppers in my Mandal.

### Got Selected For IIIT

I worked hard during my SSC and then got highest CGPA in Public Examinations resulting in getting selected for 6-year Integrated B.Tech Programme at AP IIIT RGUKT RK VALLEY.

## PASSIONS

### Cricket

### Design and Verification

### Design For Testability

### Digital Design

### Low Power Design

### Meditation

### Watching movies

### Workouts

## PROJECTS

### Design and Verification of Router1x8

2024 Kadapa

I implemented simple 1x8 router design using Verilog.

- Router routes the data from source to one of the eight destinations. It includes some internal modules like FIFO, Synchronizer, FSM, Register.

### Verification of Dual Port Ram

2024 Kadapa

My role is completely writing the design using System Verilog

- This Project is aimed to verify Dual Port Ram using System Verilog by implementing a TB environment.
- This project gave me some realtime Verification Engineer Industry work experience

### Design and Verification of RISC Processor

2023 kadapa

I implemented the complete processor with pipelining process

- This project is aimed to implement RiSC Processor using Pipelining

## SUMMARY

I am a Advanced VLSI Design and Verification Trainee at Maven Silicon and also an M.tech student at Gitam University.

I am very passionate to learn new technologies and advancements in VLSI domain and to contribute my knowledge and skillset in an semiconductor industry.

## TRAINING / COURSES

### Advanced VLSI Design and Verification

Maven Silicon

### Frontend VLSI Design and Verification using Verilog

Suretrust

## SKILLS

### Verilog

### Digital Design

### System Verilog

### Verification

### CRCDV

### RTL Design

### Synthesis

### CMOS

### Code Coverage

### Functional Coverage

### UVM

### SVA

### Linting

### PERL

### Linux

### TCL

### Modelsim

### QuartusPrime

### Xilinx Vivado

### Synopsys VCS

### Questa Sim

### Mobaxterm

### Adaptability

### Team Building

### Decision Making

### Critical Thinking

### Problem Solving

## CERTIFICATIONS

### Frontend Design and Verification using Verilog

I had got hands on experience writing Verilog designs and Testbenches

### System On Chip Design

I learnt about realtime VLSI Technology how it works.