# **Finite Impulse Response(FIR) Filter implementation on PYNQ**

This project shows how to use a PYNQ board to run a Finite Impulse Response (FIR) filter, combining hardware and Python in a Jupyter Notebook. It includes steps to set up the filter, define its coefficients, and process input signals using the board’s programmable logic. The filtered output is displayed and analyzed through simple visualizations. This project is a practical example of using FPGA hardware with Python for signal processing.