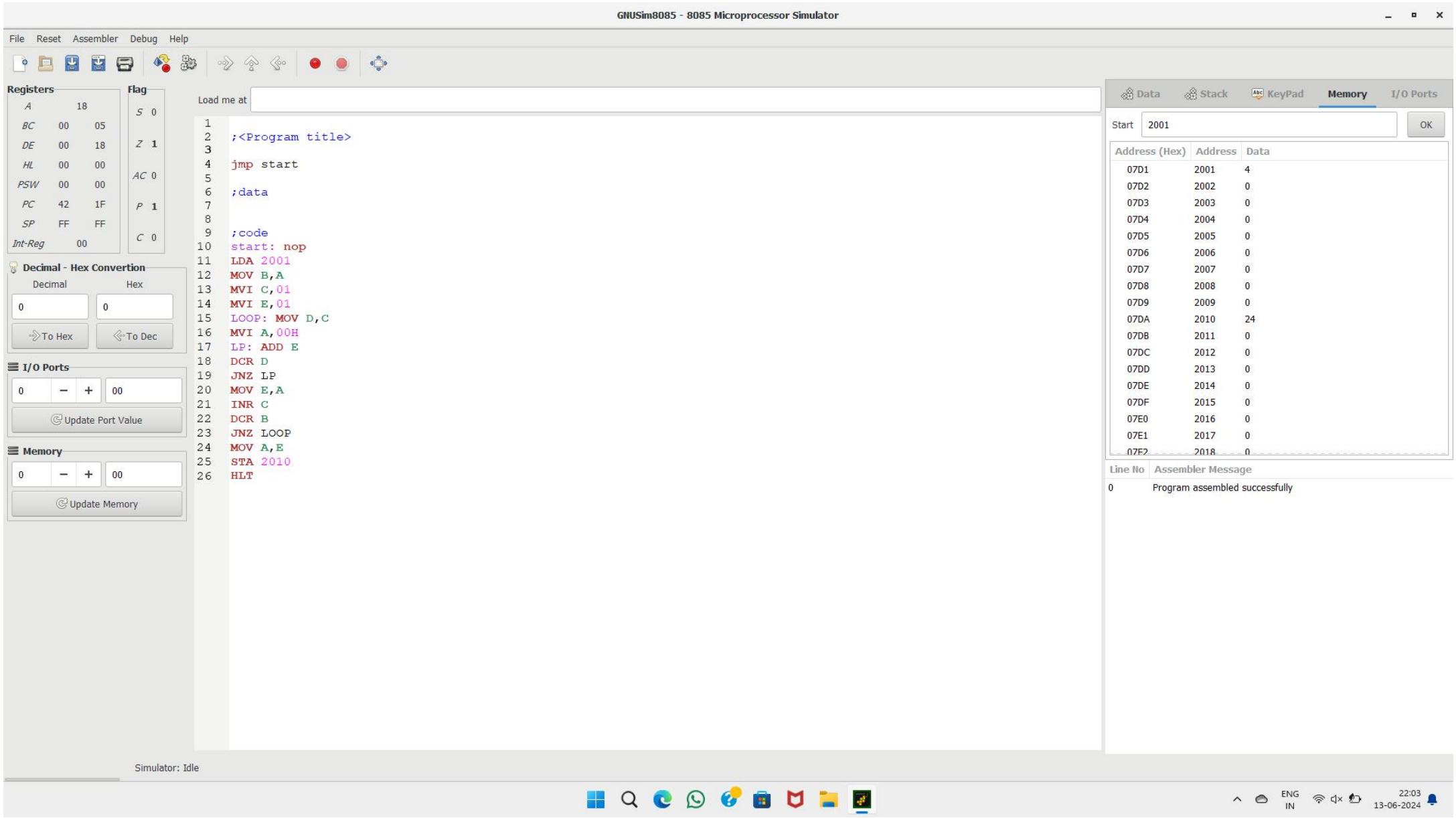
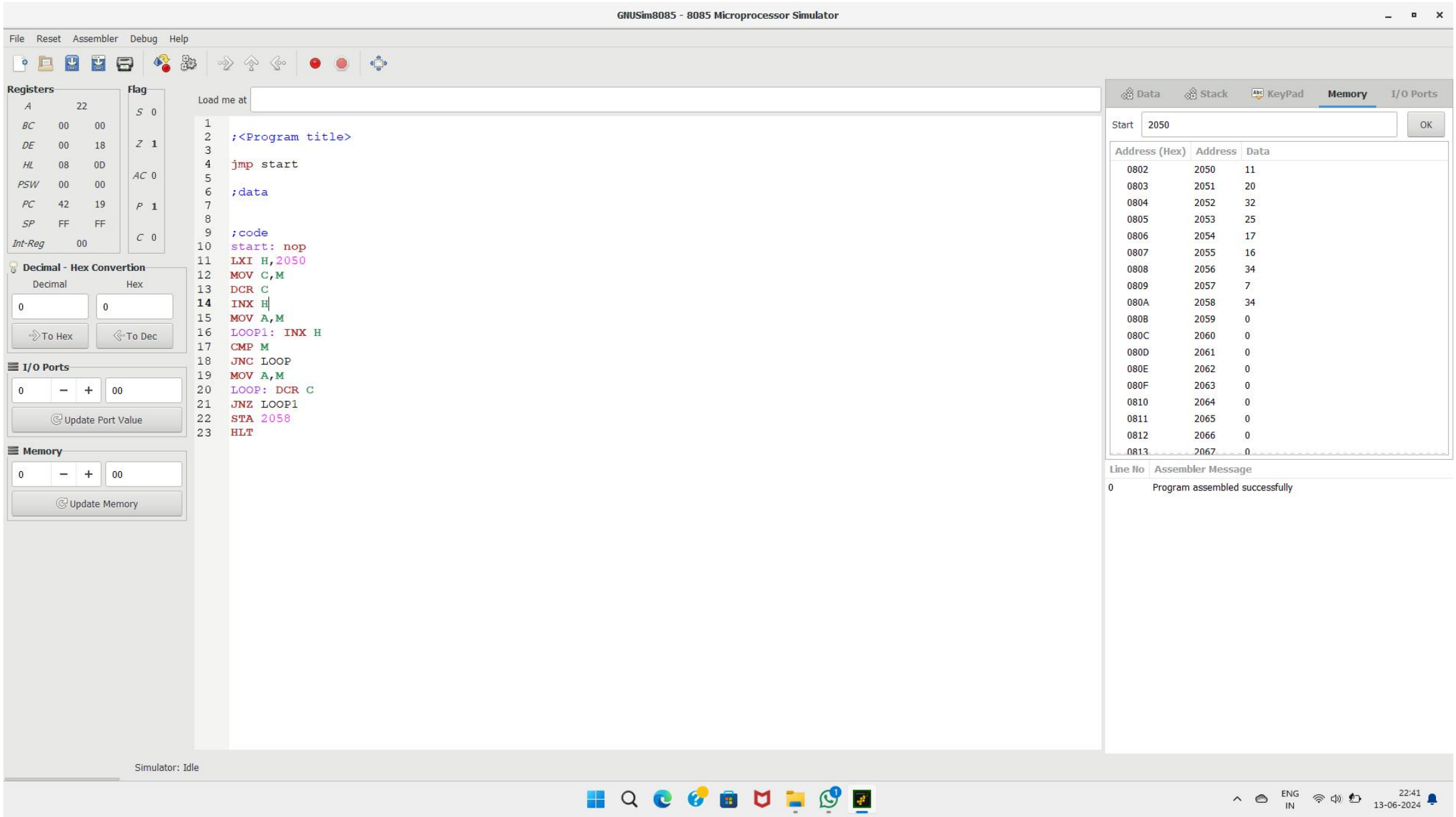
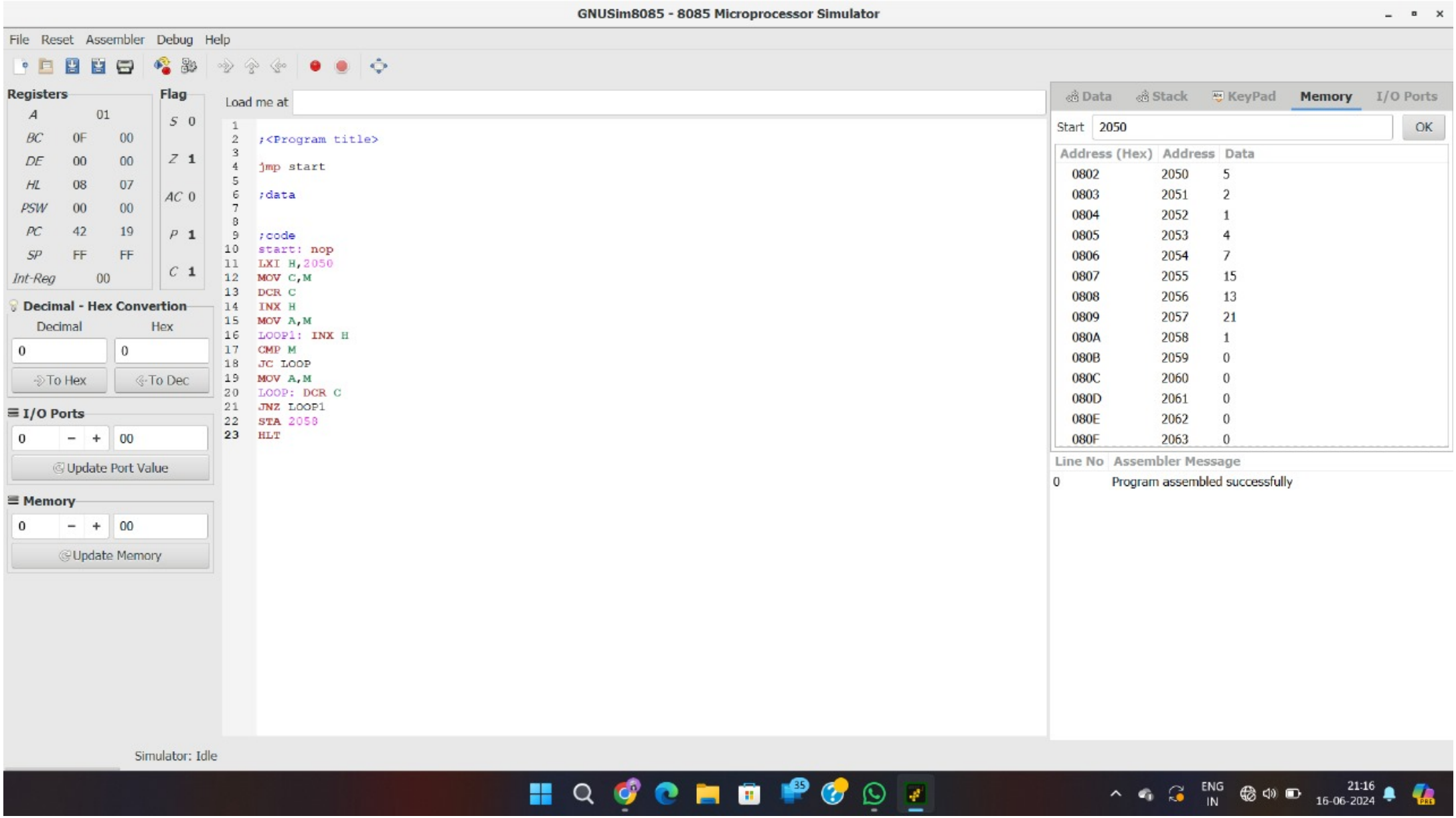
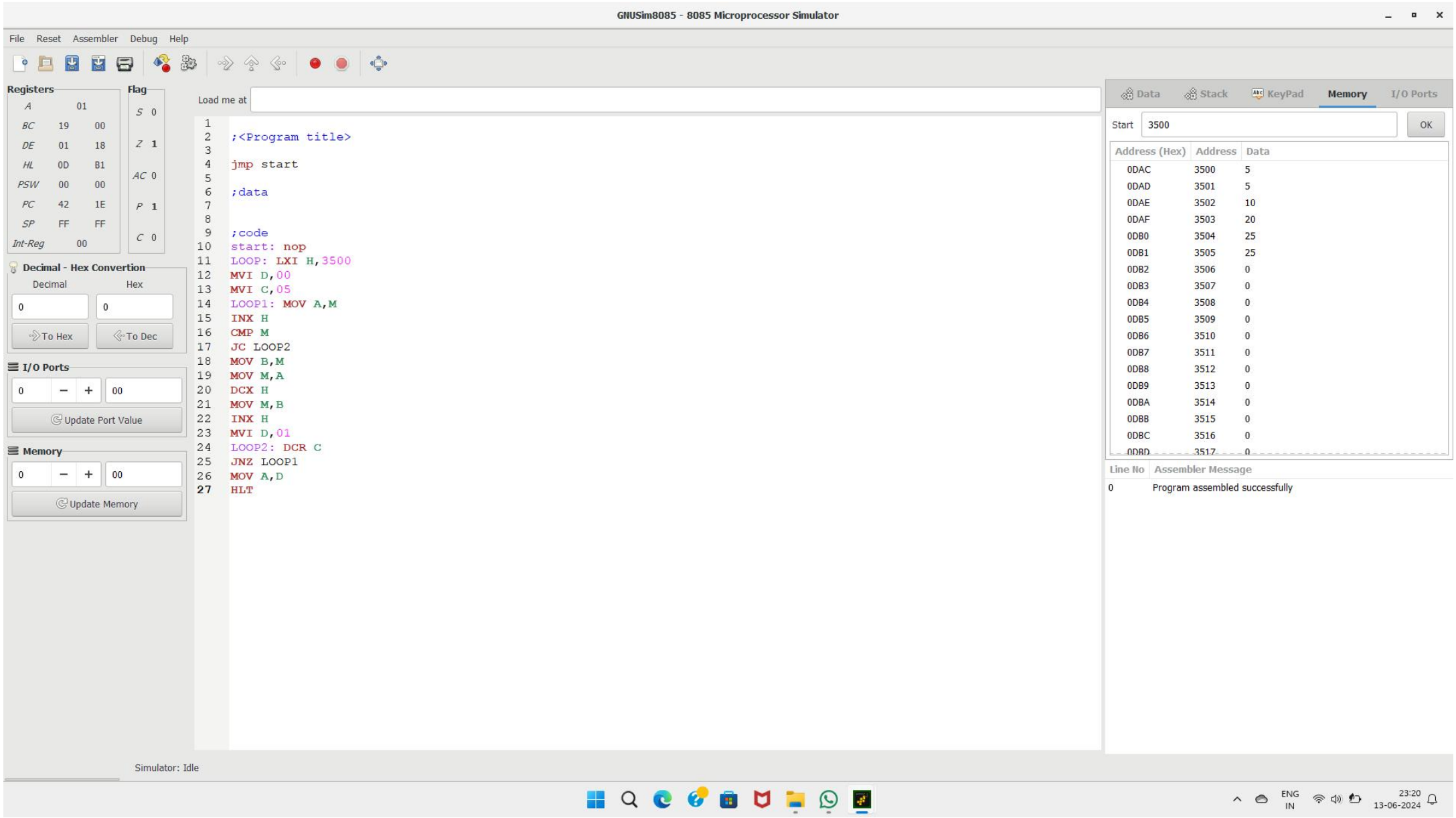
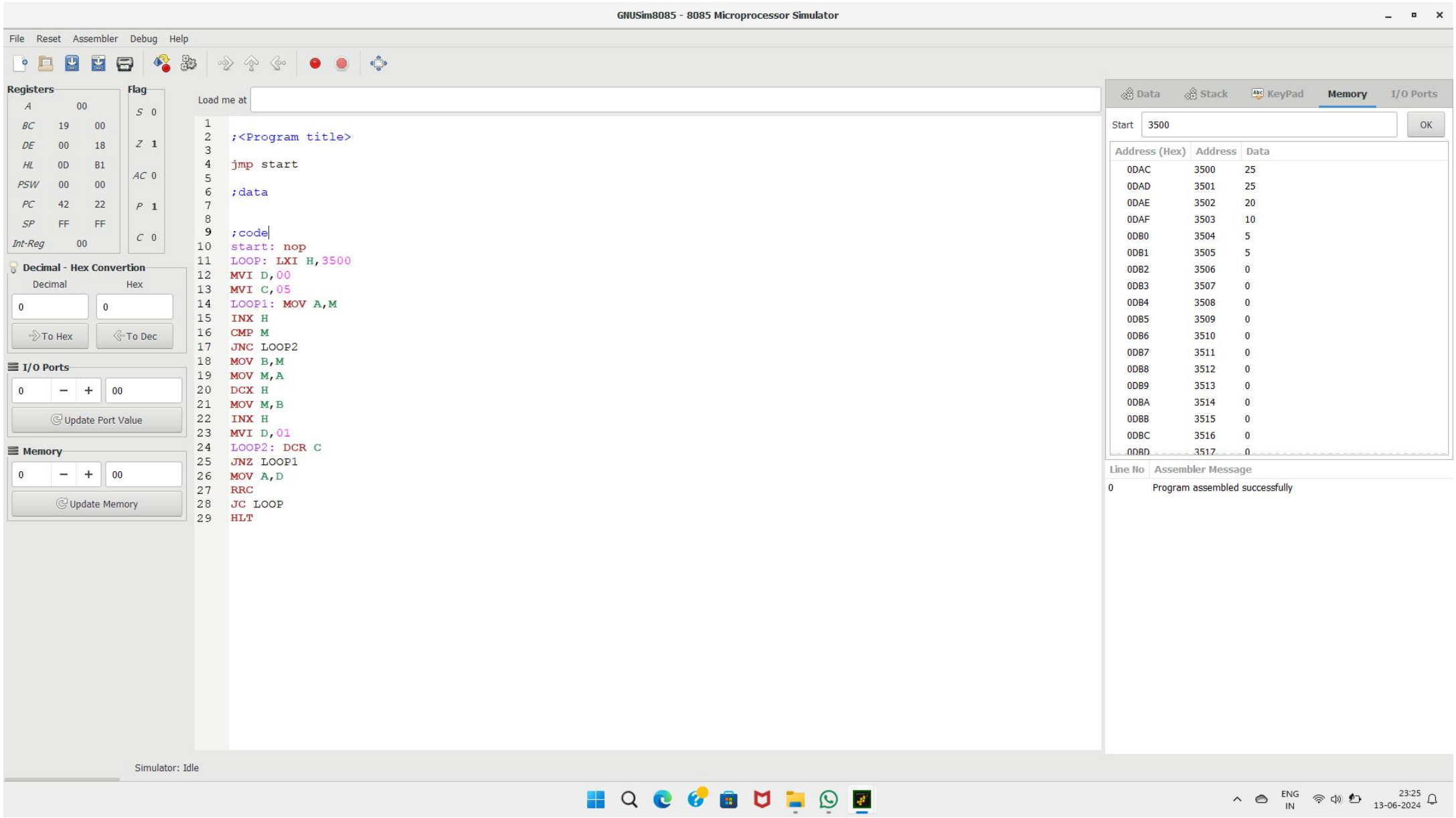
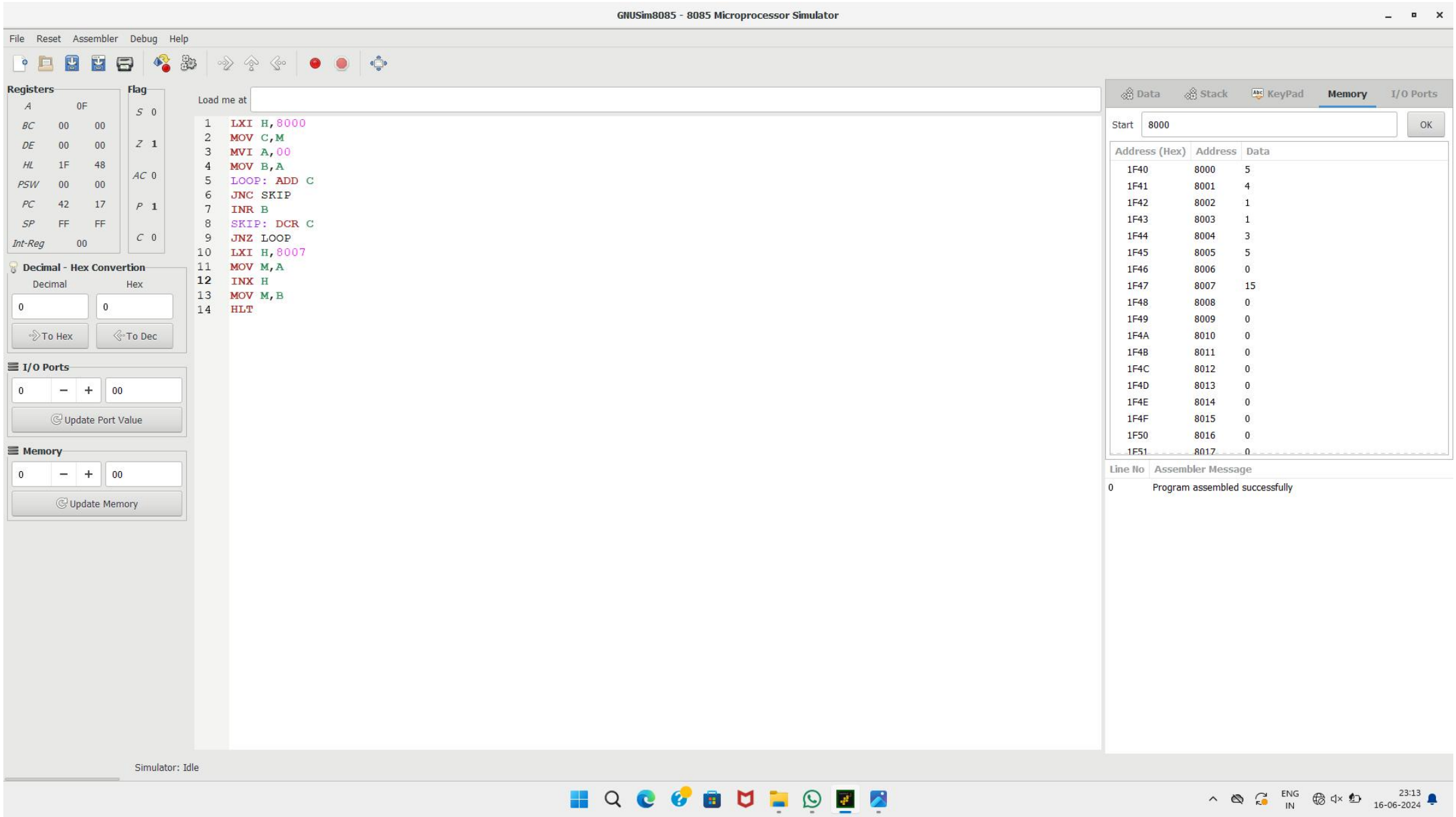

GNUSim8085 - 8085 Microprocessor Simulator
File Reset Assembler Debug Help
Registers
A 18
BC 00 05
DE 00 18
HL 00 00
PSW 00 00
PC 42 1F
SP FF FF
Int-Reg 00
Flag
S 0
Z 1
AC 0
P 1
C 0
Decimal - Hex Convertion
Decimal
Hex
0
0
To Hex
To Dec
I/O Ports
0
00
Update Port Value
Memory
0
00
Update Memory
Load me at
1
2 ;<Program title>
3
4 jmp start
5
6 ;data
7
8
9 ;code
10 start: nop
11 LDA 2001
12 MOV B,A
13 MVI C,01
14 MVI E,01
15 LOOP: MOV D,C
16 MVI A,00H
17 LP: ADD E
18 DCR D
19 JNZ LP
20 MOV E,A
21 INR C
22 DCR B
23 JNZ LOOP
24 MOV A,E
25 STA 2010
26 HLT
Data
Stack
KeyPad
Memory
I/O Ports
Start
2001
OK
Address (Hex) Address Data
07D1 2001 4
07D2 2002 0
07D3 2003 0
07D4 2004 0
07D5 2005 0
07D6 2006 0
07D7 2007 0
07D8 2008 0
07D9 2009 0
07DA 2010 24
07DB 2011 0
07DC 2012 0
07DD 2013 0
07DE 2014 0
07DF 2015 0
07E0 2016 0
07E1 2017 0
07E2 2018 0
Line No Assembler Message
0 Program assembled successfully
Simulator: Idle
ENG
IN
22:03
13-06-2024

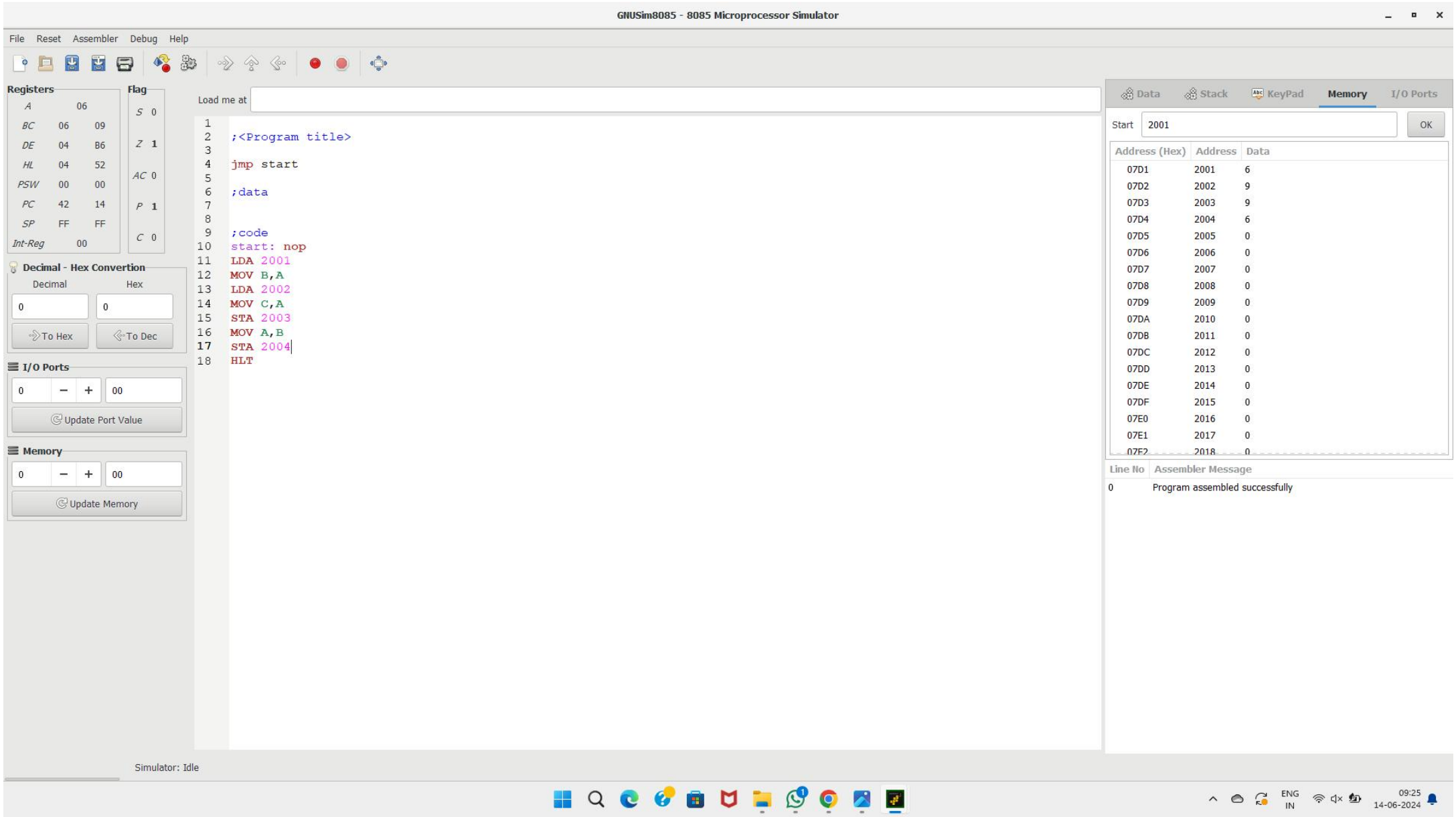

GNUSim8085 - 8085 Microprocessor Simulator
File Reset Assembler Debug Help
Registers
A 22
BC 00 00
DE 00 18
HL 08 0D
PSW 00 00
PC 42 19
SP FF FF
Int-Reg 00
Flag
S 0
Z 1
AC 0
P 1
C 0
Decimal - Hex Convertion
Decimal
Hex
0
0
To Hex
To Dec
I/O Ports
0
00
Update Port Value
Memory
0
00
Update Memory
Load me at
1
2 ;<Program title>
3
4 jmp start
5
6 ;data
7
8
9 ;code
10 start: nop
11 LXI H,2050
12 MOV C,M
13 DCR C
14 INX H
15 MOV A,M
16 LOOP1: INX H
17 CMP M
18 JNC LOOP
19 MOV A,M
20 LOOP: DCR C
21 JNZ LOOP1
22 STA 2058
23 HLT
Data
Stack
KeyPad
Memory
I/O Ports
Start
2050
OK
Address (Hex) Address Data
0802 2050 11
0803 2051 20
0804 2052 32
0805 2053 25
0806 2054 17
0807 2055 16
0808 2056 34
0809 2057 7
080A 2058 34
080B 2059 0
080C 2060 0
080D 2061 0
080E 2062 0
080F 2063 0
0810 2064 0
0811 2065 0
0812 2066 0
0813 2067 0
Line No Assembler Message
0 Program assembled successfully
Simulator: Idle
ENG
IN
22:41
13-06-2024

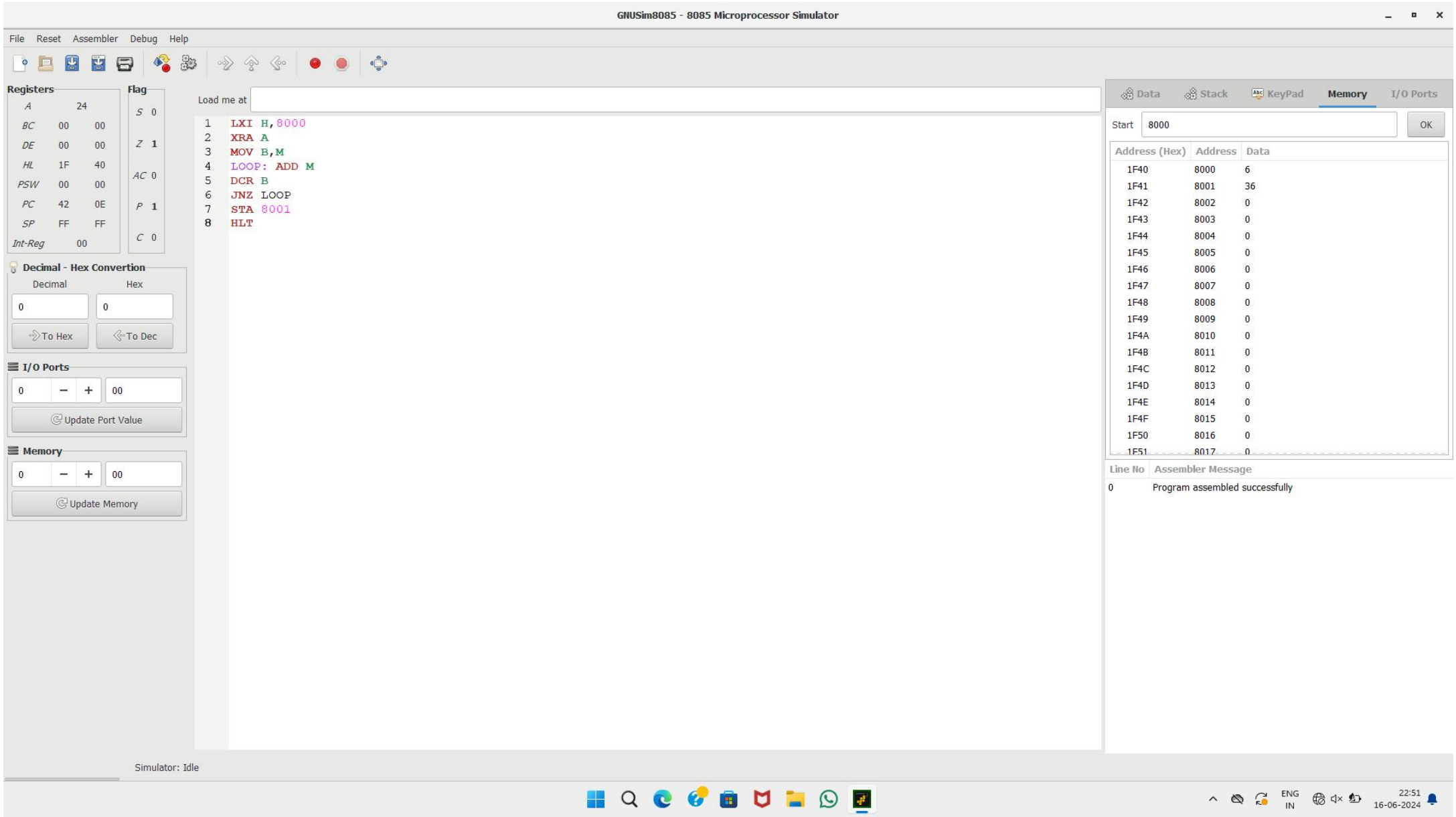

GNUSim8085 - 8085 Microprocessor Simulator
File Reset Assembler Debug Help
Registers
A 01
BC 0F 00
DE 00 00
HL 08 07
PSW 00 00
PC 42 19
SP FF FF
Int-Reg 00
Flag
S 0
Z 1
AC 0
P 1
C 1
Decimal - Hex Convertion
Decimal
Hex
0
0
To Hex
To Dec
I/O Ports
0 - + 00
Update Port Value
Memory
0 - + 00
Update Memory
Load me at
1
2 ;<Program title>
3
4 jmp start
5
6 ;data
7
8
9 ;code
10 start: nop
11 LXI H,2050
12 MOV C,M
13 DCR C
14 INX H
15 MOV A,M
16 LOOP1: INX H
17 CMP M
18 JC LOOP
19 MOV A,M
20 LOOP: DCR C
21 JNZ LOOP1
22 STA 2050
23 HLT
Data
Stack
KeyPad
Memory
I/O Ports
Start 2050
OK
Address (Hex) Address Data
0802 2050 5
0803 2051 2
0804 2052 1
0805 2053 4
0806 2054 7
0807 2055 15
0808 2056 13
0809 2057 21
080A 2058 1
080B 2059 0
080C 2060 0
080D 2061 0
080E 2062 0
080F 2063 0
Line No Assembler Message
0 Program assembled successfully
Simulator: Idle
ENG
IN
21:16
16-06-2024


GNUSim8085 - 8085 Microprocessor Simulator
File Reset Assembler Debug Help
Registers
A 01
BC 19 00
DE 01 18
HL 0D B1
PSW 00 00
PC 42 1E
SP FF FF
Int-Reg 00
Flag
S 0
Z 1
AC 0
P 1
C 0
Decimal - Hex Convertion
Decimal
Hex
0
0
To Hex
To Dec
I/O Ports
0
00
Update Port Value
Memory
0
00
Update Memory
Load me at
1
2 ;<Program title>
3
4 jmp start
5
6 ;data
7
8
9 ;code
10 start: nop
11 LOOP: LXI H,3500
12 MVI D,00
13 MVI C,05
14 LOOP1: MOV A,M
15 INX H
16 CMP M
17 JC LOOP2
18 MOV B,M
19 MOV M,A
20 DCX H
21 MOV M,B
22 INX H
23 MVI D,01
24 LOOP2: DCR C
25 JNZ LOOP1
26 MOV A,D
27 HLT
Data
Stack
KeyPad
Memory
I/O Ports
Start
3500
OK
Address (Hex)
Address
Data
0DAC 3500 5
0DAD 3501 5
0DAE 3502 10
0DAF 3503 20
0DB0 3504 25
0DB1 3505 25
0DB2 3506 0
0DB3 3507 0
0DB4 3508 0
0DB5 3509 0
0DB6 3510 0
0DB7 3511 0
0DB8 3512 0
0DB9 3513 0
0DBA 3514 0
0DBB 3515 0
0DBC 3516 0
0DBD 3517 0
Line No
Assembler Message
0 Program assembled successfully
Simulator: Idle
ENG
IN
23:20
13-06-2024


GNUSim8085 - 8085 Microprocessor Simulator
File Reset Assembler Debug Help
Registers
A 00
BC 19 00
DE 00 18
HL 0D B1
PSW 00 00
PC 42 22
SP FF FF
Int-Reg 00
Flag
S 0
Z 1
AC 0
P 1
C 0
Decimal - Hex Convertion
Decimal
Hex
0
0
To Hex
To Dec
I/O Ports
0
00
Update Port Value
Memory
0
00
Update Memory
Load me at
1
2 ;<Program title>
3
4 jmp start
5
6 ;data
7
8
9 ;code
10 start: nop
11 LOOP: LXI H,3500
12 MVI D,00
13 MVI C,05
14 LOOP1: MOV A,M
15 INX H
16 CMP M
17 JNC LOOP2
18 MOV B,M
19 MOV M,A
20 DCX H
21 MOV M,B
22 INX H
23 MVI D,01
24 LOOP2: DCR C
25 JNZ LOOP1
26 MOV A,D
27 RRC
28 JC LOOP
29 HLT
Data
Stack
KeyPad
Memory
I/O Ports
Start
3500
OK
Address (Hex) Address Data
0DAC 3500 25
0DAD 3501 25
0DAE 3502 20
0DAF 3503 10
0DB0 3504 5
0DB1 3505 5
0DB2 3506 0
0DB3 3507 0
0DB4 3508 0
0DB5 3509 0
0DB6 3510 0
0DB7 3511 0
0DB8 3512 0
0DB9 3513 0
0DBA 3514 0
0DBB 3515 0
0DBC 3516 0
0DBD 3517 0
Line No Assembler Message
0 Program assembled successfully
Simulator: Idle
ENG
IN
23:25
13-06-2024


GNUSim8085 - 8085 Microprocessor Simulator
File Reset Assembler Debug Help
Registers
A 0F
BC 00 00
DE 00 00
HL 1F 48
PSW 00 00
PC 42 17
SP FF FF
Int-Reg 00
Flag
S 0
Z 1
AC 0
P 1
C 0
Decimal - Hex Convertion
Decimal
Hex
0
0
To Hex
To Dec
I/O Ports
0
00
Update Port Value
Memory
0
00
Update Memory
Load me at
1 LXI H,8000
2 MOV C,M
3 MVI A,00
4 MOV B,A
5 LOOP: ADD C
6 JNC SKIP
7 INR B
8 SKIP: DCR C
9 JNZ LOOP
10 LXI H,8007
11 MOV M,A
12 INX H
13 MOV M,B
14 HLT
Data
Stack
KeyPad
Memory
I/O Ports
Start
8000
OK
Address (Hex) Address Data
1F40 8000 5
1F41 8001 4
1F42 8002 1
1F43 8003 1
1F44 8004 3
1F45 8005 5
1F46 8006 0
1F47 8007 15
1F48 8008 0
1F49 8009 0
1F4A 8010 0
1F4B 8011 0
1F4C 8012 0
1F4D 8013 0
1F4E 8014 0
1F4F 8015 0
1F50 8016 0
1F51 8017 0
Line No Assembler Message
0 Program assembled successfully
Simulator: Idle
ENG
IN
23:13
16-06-2024


GNUSim8085 - 8085 Microprocessor Simulator
File Reset Assembler Debug Help
Registers
A 06
BC 06 09
DE 04 B6
HL 04 52
PSW 00 00
PC 42 14
SP FF FF
Int-Reg 00
Flag
S 0
Z 1
AC 0
P 1
C 0
Decimal - Hex Convertion
Decimal
Hex
0
0
To Hex
To Dec
I/O Ports
0
00
Update Port Value
Memory
0
00
Update Memory
Load me at
1
2 ;<Program title>
3
4 jmp start
5
6 ;data
7
8
9 ;code
10 start: nop
11 LDA 2001
12 MOV B,A
13 LDA 2002
14 MOV C,A
15 STA 2003
16 MOV A,B
17 STA 2004
18 HLT
Data
Stack
KeyPad
Memory
I/O Ports
Start
2001
OK
Address (Hex)
Address
Data
07D1 2001 6
07D2 2002 9
07D3 2003 9
07D4 2004 6
07D5 2005 0
07D6 2006 0
07D7 2007 0
07D8 2008 0
07D9 2009 0
07DA 2010 0
07DB 2011 0
07DC 2012 0
07DD 2013 0
07DE 2014 0
07DF 2015 0
07E0 2016 0
07E1 2017 0
07E2 2018 0
Line No
Assembler Message
0
Program assembled successfully
Simulator: Idle
ENG
IN
09:25
14-06-2024


GNUSim8085 - 8085 Microprocessor Simulator
File Reset Assembler Debug Help
Registers
A 24
BC 00 00
DE 00 00
HL 1F 40
PSW 00 00
PC 42 0E
SP FF FF
Int-Reg 00
Flag
S 0
Z 1
AC 0
P 1
C 0
Decimal - Hex Convertion
Decimal
Hex
0
0
To Hex
To Dec
I/O Ports
0
00
Update Port Value
Memory
0
00
Update Memory
Load me at
1 LXI H,8000
2 XRA A
3 MOV B,M
4 LOOP: ADD M
5 DCR B
6 JNZ LOOP
7 STA 8001
8 HLT
Data
Stack
KeyPad
Memory
I/O Ports
Start
8000
OK
Address (Hex) Address Data
1F40 8000 6
1F41 8001 36
1F42 8002 0
1F43 8003 0
1F44 8004 0
1F45 8005 0
1F46 8006 0
1F47 8007 0
1F48 8008 0
1F49 8009 0
1F4A 8010 0
1F4B 8011 0
1F4C 8012 0
1F4D 8013 0
1F4E 8014 0
1F4F 8015 0
1F50 8016 0
1F51 8017 0
Line No Assembler Message
0 Program assembled successfully
Simulator: Idle
ENG
IN
22:51
16-06-2024