28/2/24.

- Arm architecture.
  It specifies how a processor will behave.
- RISC
- Reduced Instruction set compuber,
- Micro architecture

  It is the design of processor

- ip core

  Micro architecture is installed in software i.e. ipcore.

- processor Family.

  Arm processor family has multiple processor

  Arm (1985) to Arm 11 (2002)

  2005 → naming scheme changed.

  Cortex - [lebver] [number]

  Digits & hardware characteristics

Profile

Application - Features powerful processors

Realtime - example of control unit for automotive
                                              systems

Microcontroller profile -

Arm makes money with
        Architecture of processor cores.

Arm (soc)

ARM.
TRM -> about processores
CIM (Configuration Integration manual)

soc Databasheet -> Info on specific
                                    so.c

Note.

28/2/24

Assignment - 7

write a shell script to run a
c File and execute

29/2/24

ps - What process is going on

ps -ax - complete process

When we see this we will get
to know abt process ID at
First
ex: 2210

we use ls /proc/ 2210 to
know about Files.

process creation:-

We nedd system call to access
in kernel.

system() → system call

Fork ()
Wait ()
wait pid ()
exec ()

clone ()

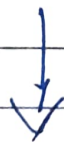} system call.

clone () is very important
it should be kept safe.

orphan process - parent is gone
 but child is running.

Fork () → child process

To stop zombie } wait ()
     process { wait pid ()

To access

Fork 1

↓ process is going on

Fork ()