

Madhushree Parameshwara

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Training

Pursuing **JAVA Enterprise Application Development** training in **X-workz** Software Development training centre, Bangalore.

Technical Skills

Languages	CORE JAVA, C, HTML, CSS
IDE	Eclipse, Visual Studio
Source Control Tool	Git Hub

Qualification

“Bachelor of Engineering in Electronics and Communication from Visvesvaraya Technological University, Belgaum
Graduated in 2020 with 7.2 CGPA”.

Technical Summery

JAVA	<ul style="list-style-type: none">❖ Knowledge on Variables, Datatypes, Parameters and Arguments, Arrays, Enum, Key words.❖ Good exposor of Access Specifiers, Method Return type, Wrapper Class.❖ Knowledge on DTO and DAO.❖ Understanding of Classes, Objects, Methods, Constructors, Exceptions.❖ Understanding of OOPs Principles<ol style="list-style-type: none">1. Abstraction<ul style="list-style-type: none">✦ Practical Knowledge on Abstract Class and Interface.2. Encapsulation3. Inheritance<ul style="list-style-type: none">✦ Understanding of types of Inheritance.4. Polymorphism<ul style="list-style-type: none">✦ Understanding of types of Polymorphism.
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	<ul style="list-style-type: none"> ❖ Practical Knowledge on Overridden Methods (equals (), toString (), hashCode (), clone ()). ❖ Understanding of Exception Handling <ul style="list-style-type: none"> ✦ Knowledge on Hierarchy of Collection and Understanding of checked and unchecked exceptions. ❖ Understanding of Collections <ul style="list-style-type: none"> ✦ Knowledge on Hierarchy, subtypes and Implementor Classes. ❖ Understanding of Memory Allocation (Stack and Heap). ❖ Good exposor of Standard Package. ❖ Understanding of Data Structures.
HTML	<ul style="list-style-type: none"> ❖ Knowledge on Attributes, Headings, Paragraphs, Styles and Validations. ❖ Understanding of Lists, Tables, Forms and HTML Links.
CSS	<ul style="list-style-type: none"> ❖ Knowledge on Margins, CSS Hight and width, Padding and Links. ❖ Understanding of Selectors, Combinators, CSS Lists, Tables, Forms, Flex and Overflow.

Academic Project Details

“Design and Verification of FIR Filter using Pipelined Distributed Arithmetic”

Summary:

The project includes the design, implementation and synthesis of FIR Filter. FIR Filters are used in digital signal processing applications to meet the required frequency specifications.

Software application used: Xilinx 8.1i

Internship

“Did Internship (2019) in the Organization **Semi-ksha Semiconductors**, Learnt about RTL Design and Design Verification”.