YASH GUPTA LABEND EXAM S202000/0234 Paper Code - 2 MIM: To down the Input and Outpil Transister waveform for BC547A Transistor Software Required: LT Spice Circuit Diagram RZ IKSL

To Obstan outset resistan

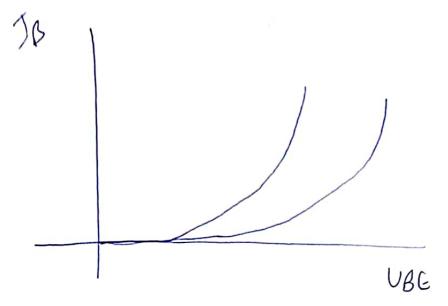
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De when ve	e is at const	al 2V
VOB	IB (MA)	VOE.
0.2		
0.6		

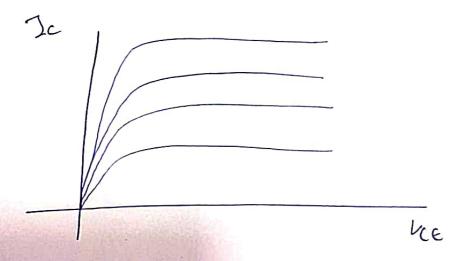
(2) When to	Io is at	10 91 A
le c	Ic (MA)	KE
	0.32	1-06
2	0.6	2.08
3	0.67	1.17
4	0.65	2.17
5	D.66	3.17
6	6-67	7.17

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Graph



Tous UDE graph at constant VEE



Ic vs VCE at constal IB

Conclusion

to obtain troisiture find Ie and USE
for a constit Is.