

# ASSIGNMENT

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Q-1

A D flipflop takes only a single input, the D (data) input. The master-slave

Configuration has the advantage of being edge-triggered, making it easier to use in larger circuits, since the inputs to a flip-flop often depend on the state of its output.

The circuit consists of two D flip flop connected together. When the clock is high, the D input is stored in the first latch, but the second latch cannot change state. When the clock is low, the first latch's output is stored in second latch, but the first latch change state.

The result is that output can only change state when the clock makes a transition from high to low.