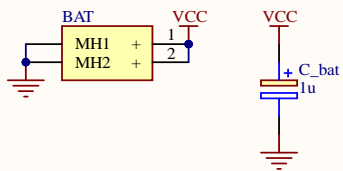
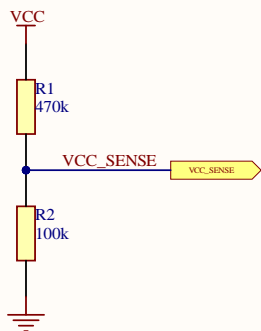


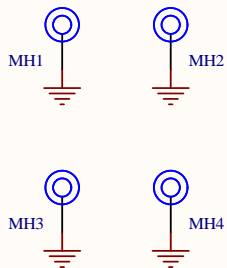
BAT. Header



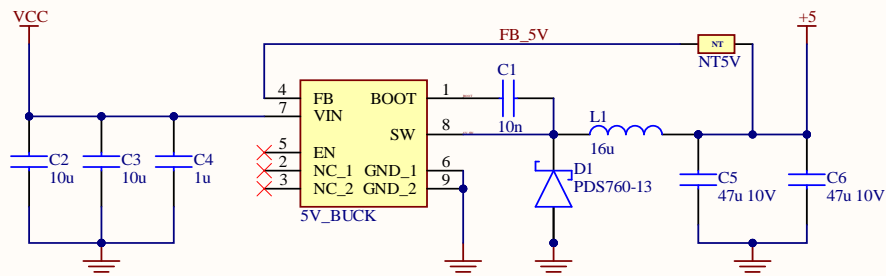
VSense



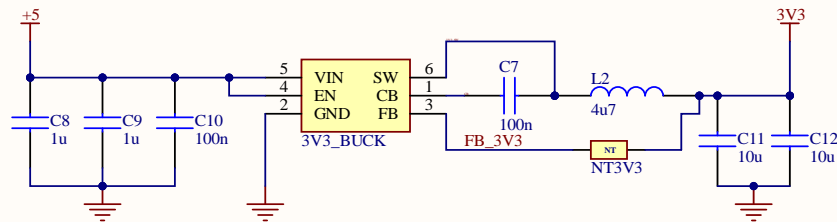
Mounting Holes




5V/3A Buck



3V3/1A Buck

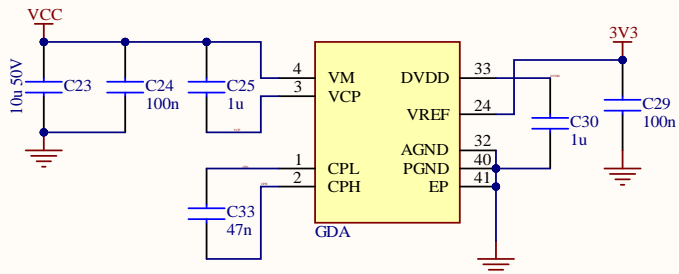


Notes;

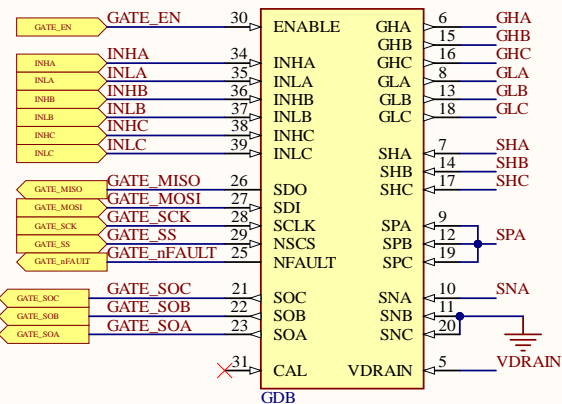
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Size: A4	Number:1	Revision:0.1	
Date: 12/4/2025	Time: 8:01:19 PM	Sheet 4 of 7	
File: C:\Users\USER\Desktop\Altium\RC Car\RC_Car\Power.SchDoc			

Maduranga Mathara Arachchi
17/21, Gnanawimala Road,
Athurugiriya
Colombo,
Sri Lanka.

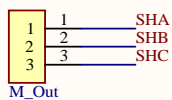
Gate Driver Power



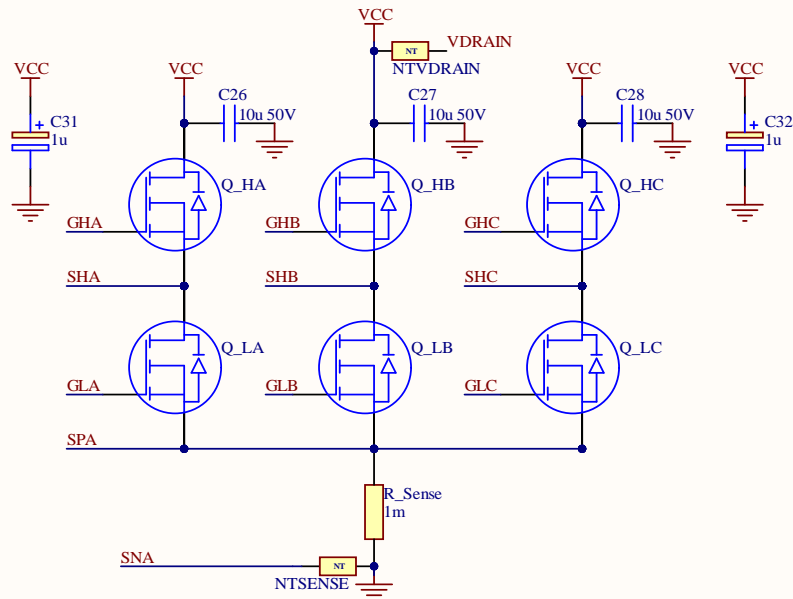
Gate Driver I/Os




Motor Out



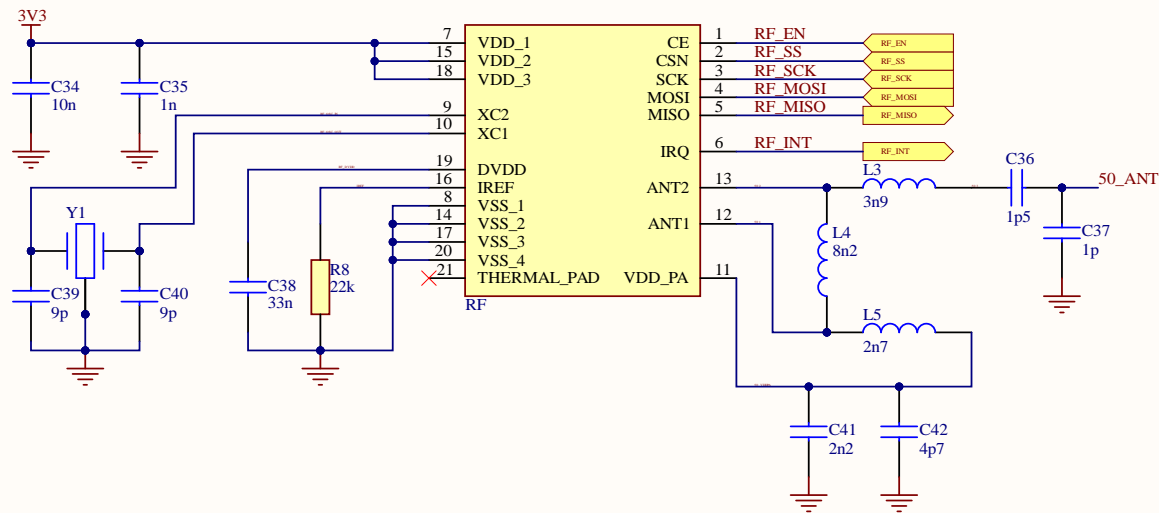
Half Bridge



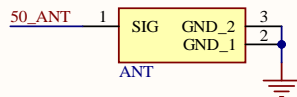
Notes;

Title <i>Bridge</i>			Maduranga Mathara Arachchi 17/21, Gnanawimala Road, Athurugiriya Colombo, Sri Lanka.	
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Date: 12/4/2025	Time: 8:01:19 PM	Sheet6 of 7		
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RF Transceiver



UFL Conn.



Notes;

Title <i>RF</i>			<i>Maduranga Mathara Arachchi</i> 17/21, Gnanawimala Road, <i>Athurugiriya</i> Colombo, <i>Sri Lanka.</i>	
Size: <i>A4</i>	Number:1	Revision:0.1		
Date: <i>12/4/2025</i>	Time: <i>8:01:19 PM</i>	Sheet7 of 7		
File: C:\Users\USER\Desktop\Altium\RC Car\RC_Car\RF.SchDoc				

