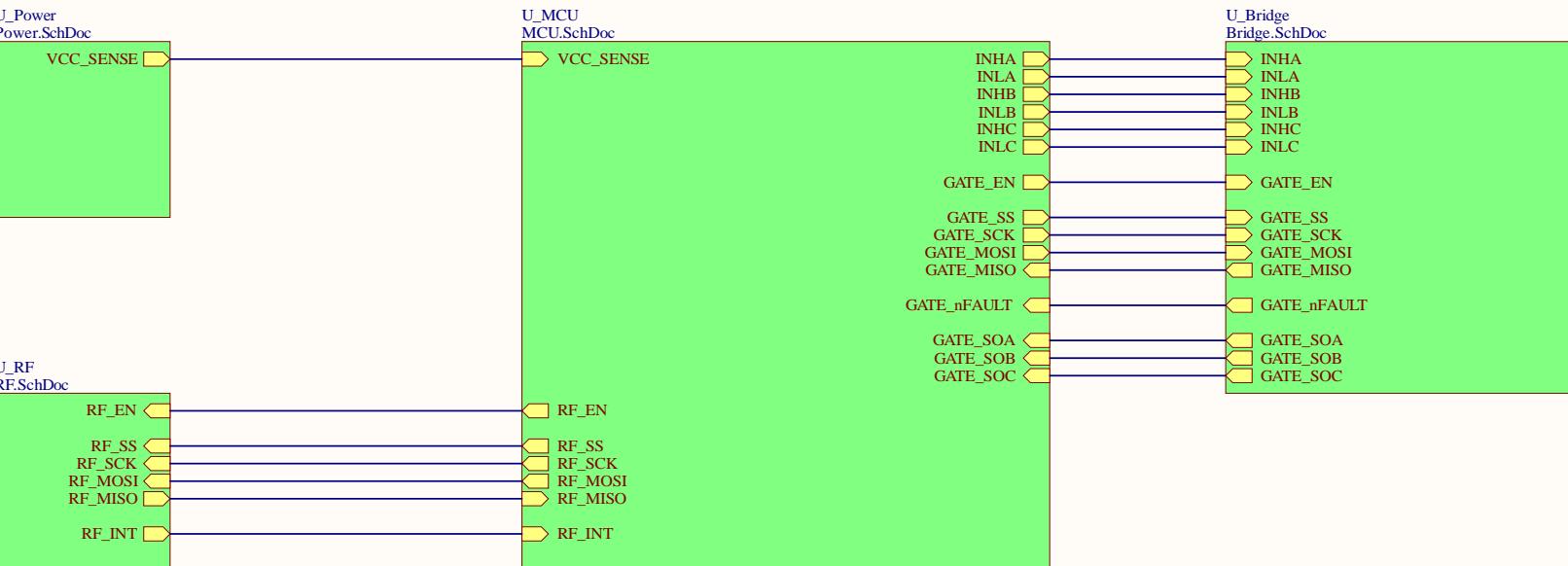


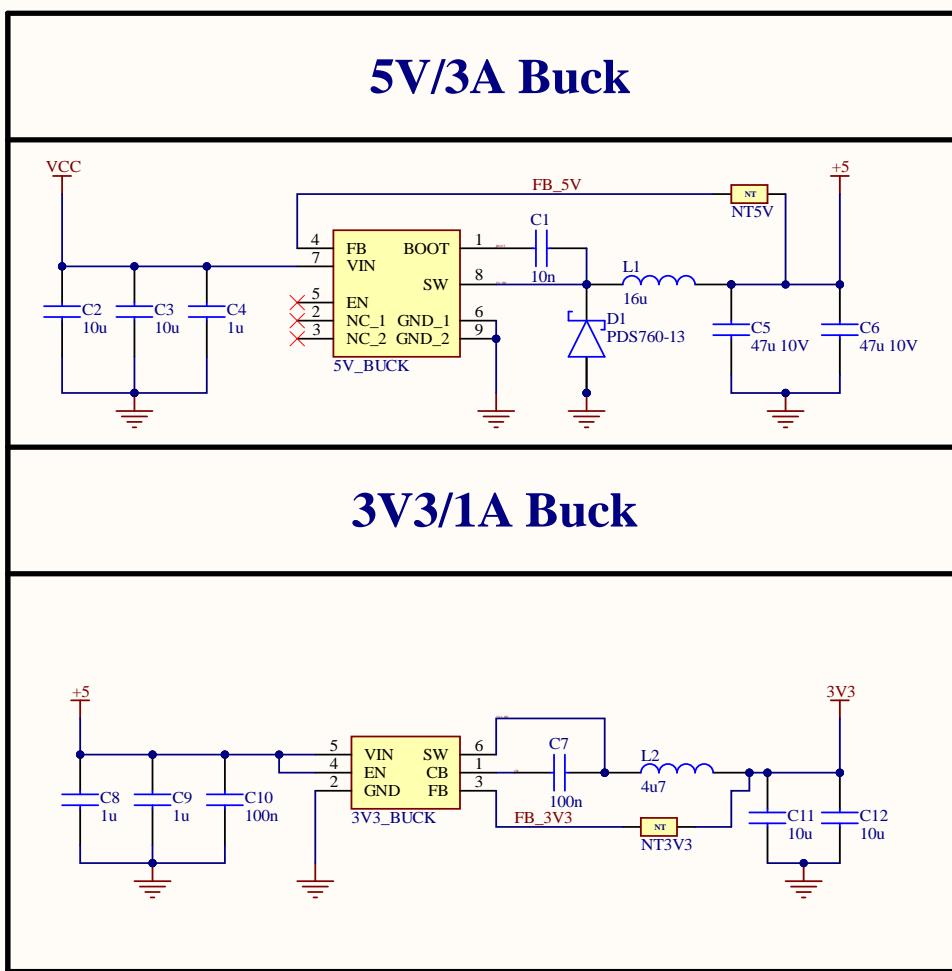
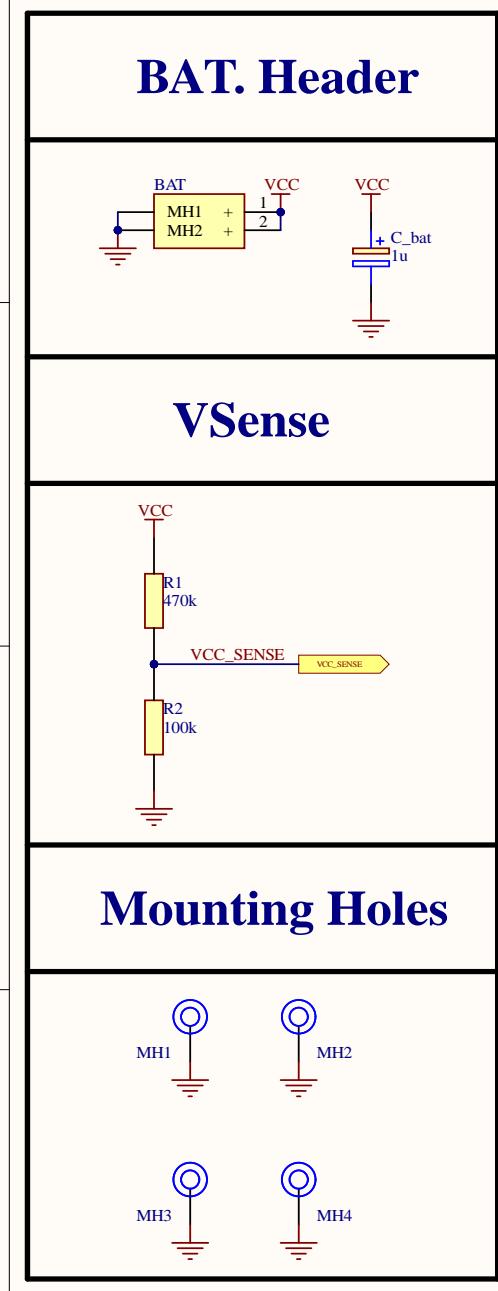
A



B

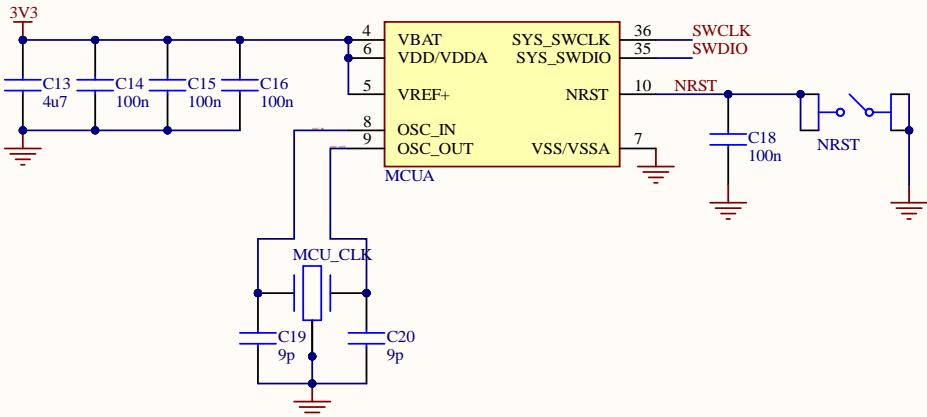
C

D

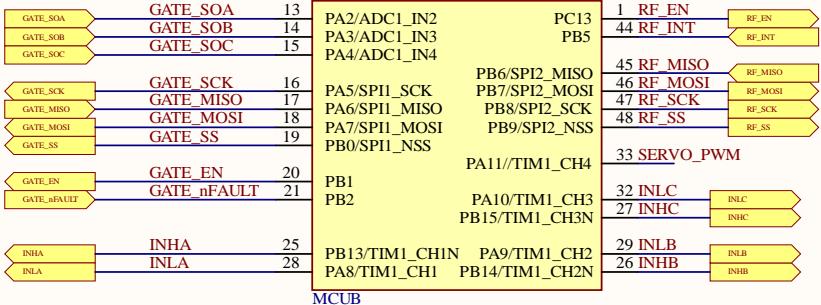


Title: <b>Power</b>	Size: A4	Number: 1	Revision: 0.1	Maduranga Mathara Arachchi: 17/21, Gnanawimala Road, Athurugiriya Colombo, Sri Lanka.
Date: 12/4/2025	Time: 8:01:19 PM	Sheet 4 of 7		
File: C:\Users\USER\Desktop\Altium\RC Car\RC_Car\Power.SchDoc				<b>Altium</b>

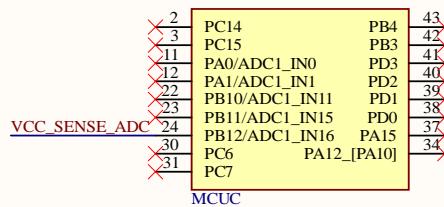
## Power, Clock & Reset



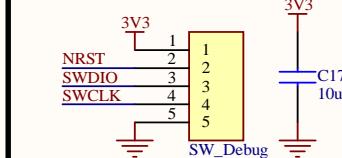
## MCU Communications



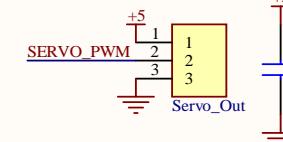
## GPIOs/ADCs



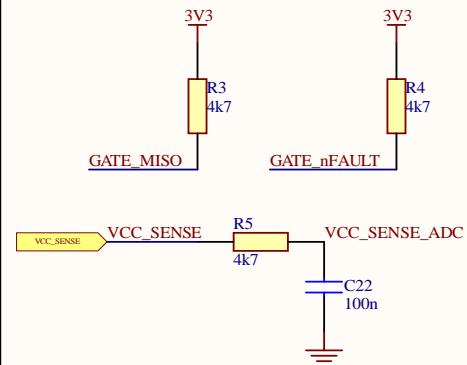
## SW Debug



## Servo Out



## Pull Ups & VSense



Notes;

Title **MCU**

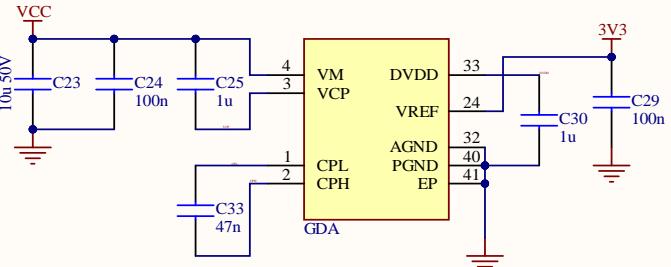
Size: **A4** Number:**1** Revision:**0.1**

Date: **12/4/2025** Time: **8:01:19 PM** Sheet**5** of **7**

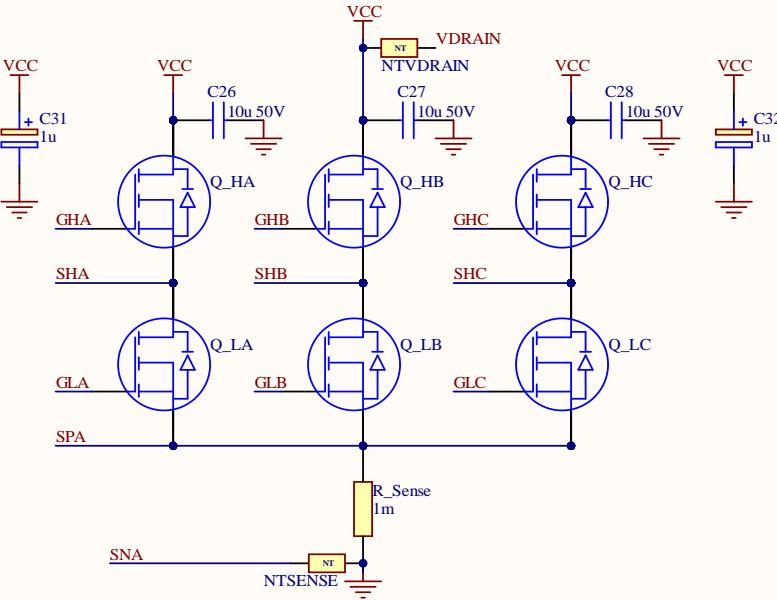
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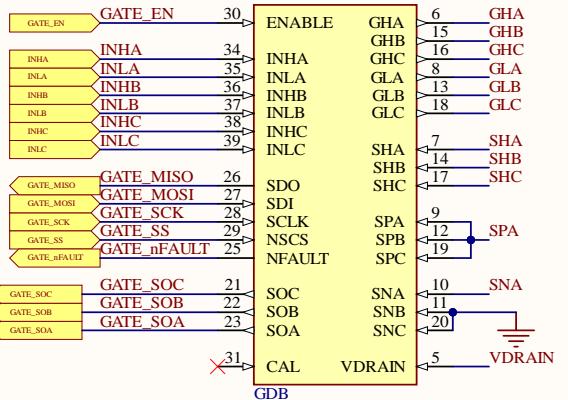
## Gate Driver Power



## Half Bridge

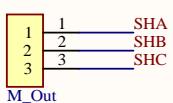


## Gate Driver I/Os



Notes;

## Motor Out



Title **Bridge**

Size: A4 Number:1 Revision:0.1

Date: 12/4/2025 Time: 8:01:19 PM Sheet 6 of 7

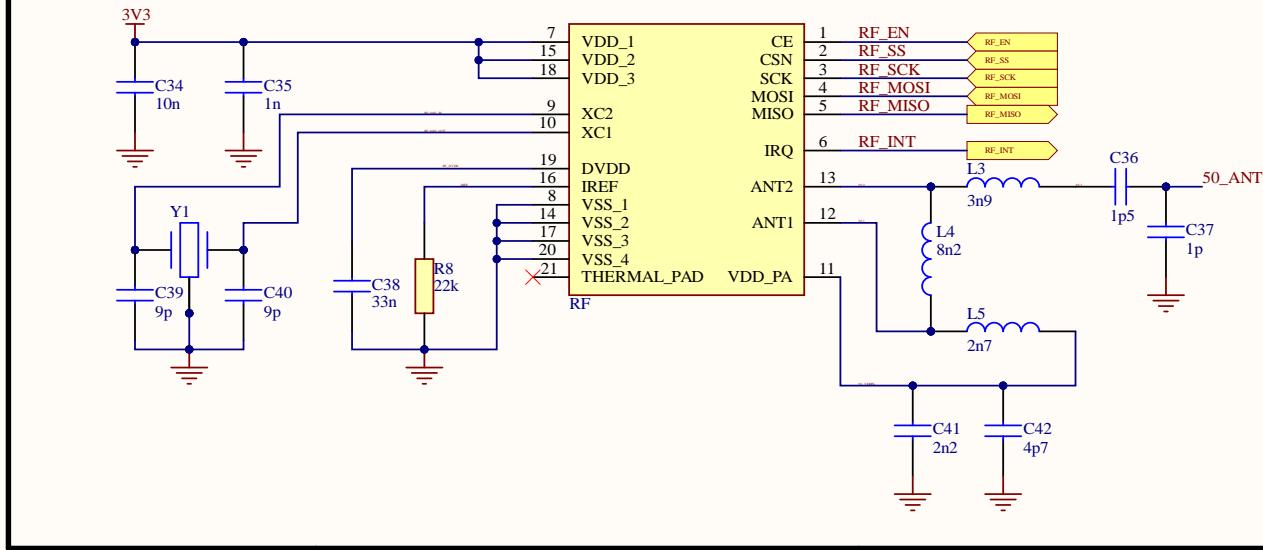
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Maduranga Mathara Arachchi  
17/21, Gnanawimala Road,  
Athurugiriya  
Colombo,  
Sri Lanka.

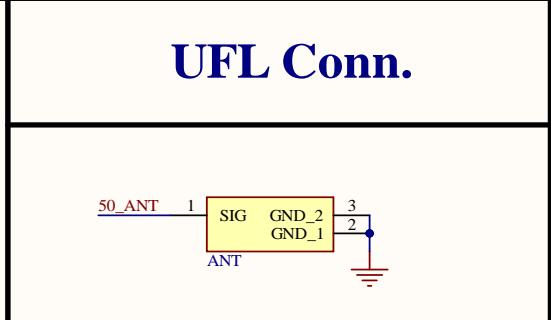


Notes;

## RF Transceiver



## UFL Conn.



Title <b>RF</b>			<i>Maduranga Mathara Arachchi</i> 17/21, Gnanawimala Road, Athurugiriya Colombo, Sri Lanka.
Size: A4	Number:1	Revision:0.1	
Date: 12/4/2025	Time: 8:01:19 PM	Sheet 7 of 7	
File: C:\Users\USER\Desktop\Altium\RC Car\RC_Car\RF.SchDoc			<b>Altium</b>

