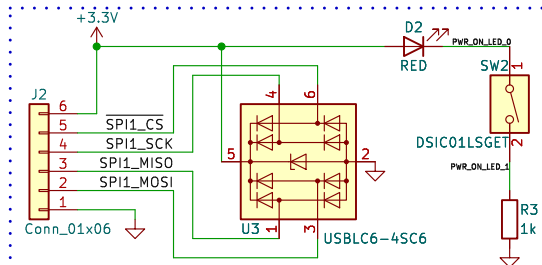


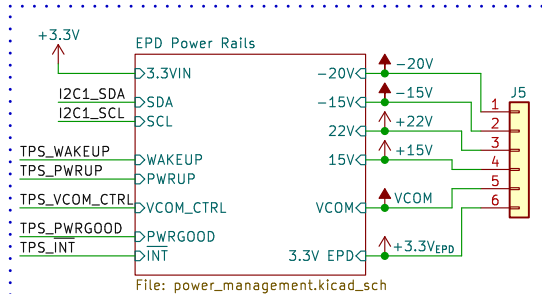
ED060SC4 Driver Board

Main Power & SPI

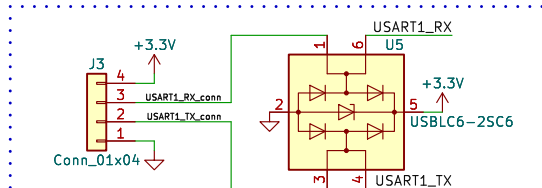


- This is the interface of the driver board.
- The user is expected to supply 3.3V and GND via this header.
- The commands can then be send via SPI to the MCU.

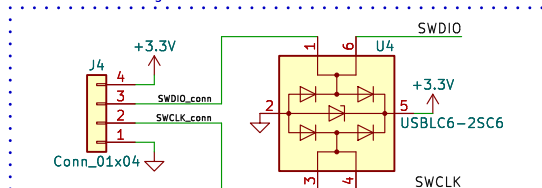
Power Rails



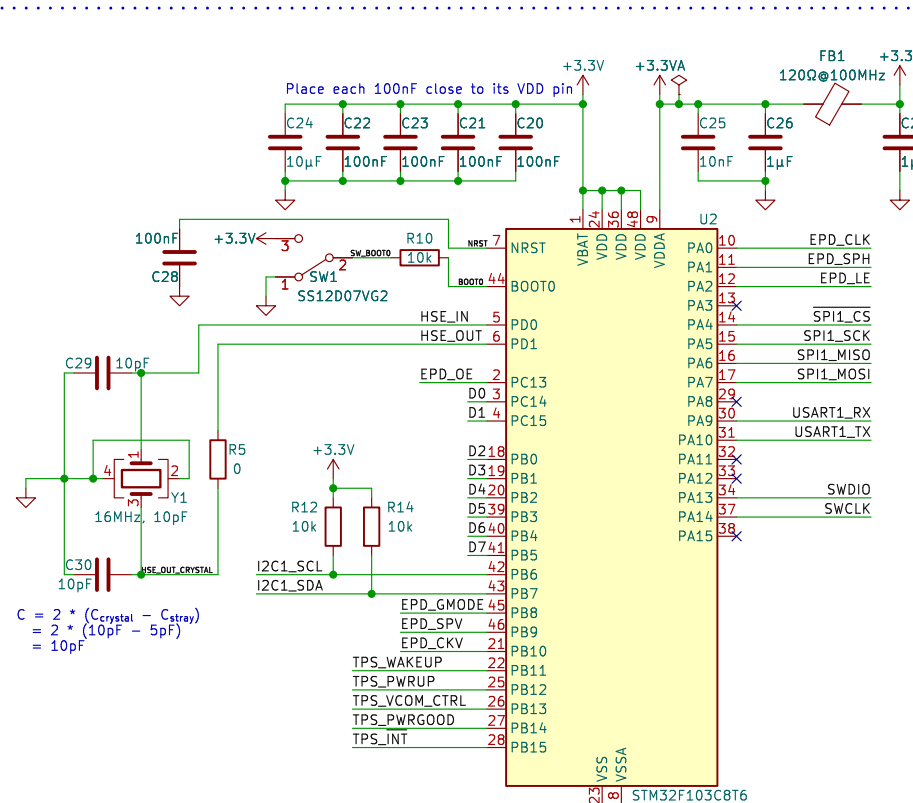
UART – Programming



Serial Wire Debug



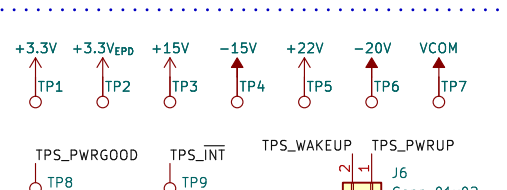
STM32 Microcontroller



Pin-out via STM32CubeMX.
The I2C 10k resistors come from the TPS65185 datasheet.
100nF capacitor used on NRST to prevent from spurious resets.

BOOT0 is not strictly needed, as the STM32 will be programmed via SWD but it doesn't hurt.

Test Points



- : Number of pins required from the μC : 32

- TPS 65185: 7 (4 min)
 - SDA & SCL (I²C)
 - WAKEUP & PWRUP
 - PWRGOOD (Can be omitted)
 - VCOM_CTRL (Can be omitted and connected to 3.3V)
 - INT (Can be omitted and left floating)

ED060SC4: 15

- . - GMODE
- . - SPV
- . - CKV
- . - CL
- . - LE
- . - OE
- . - SPH
- . - D0 to D7 (Data Bus)

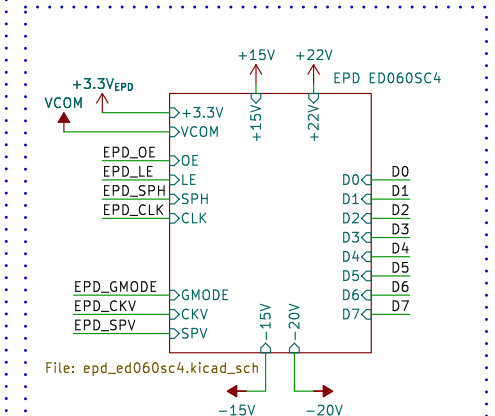
```

: STM32F103C8 Reserved: 10

```

- - SWD (SWCLK, SWDIO)
- - USART (RX, TX)
- - SPI (SCK, MOSI, MISO, CS)
- - Oscillator (OSC_IN, OSC_OUT)

- EPD Screen



https://github.com/Mael-Le-Garrec/ED060SC4_driver_board

matrix

Sheet: /

File: ED060SC4_SPI_driver_board.kicad_sch

Title: ED060SC4 SPI Driver Board

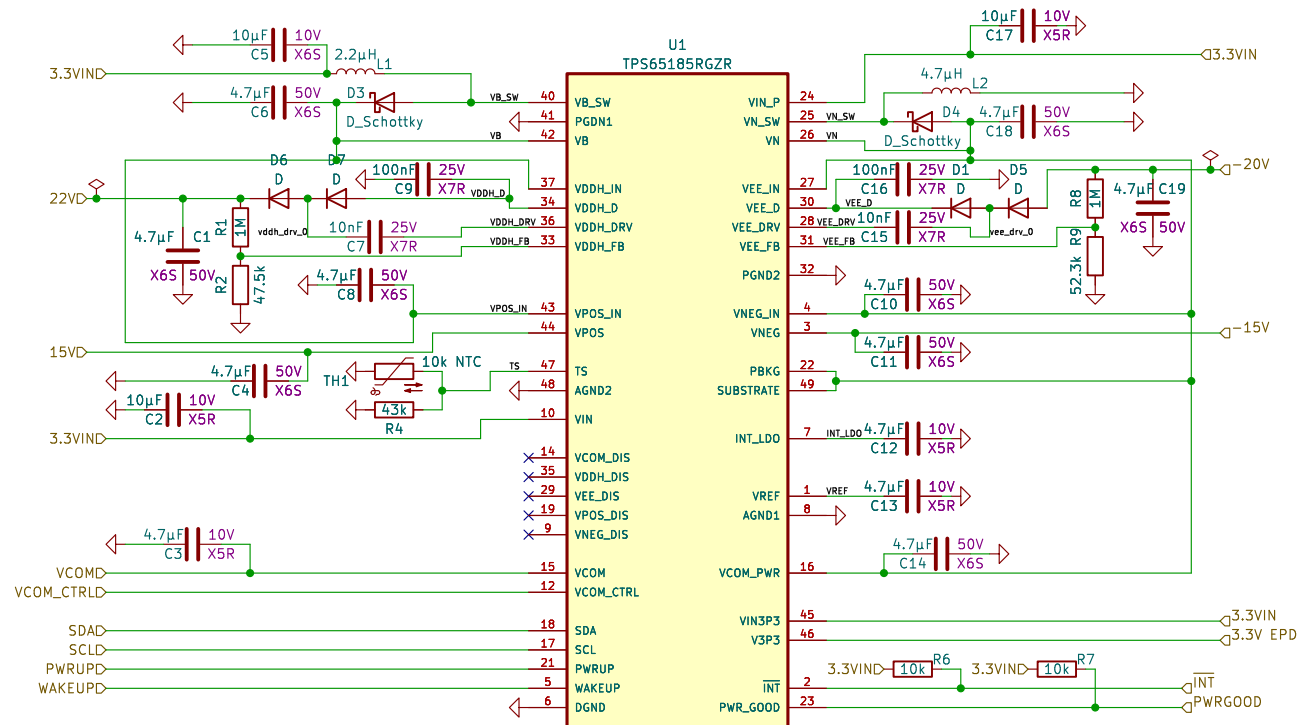
Size: A4	Date: 2023-04-12
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Size: A4	Date:
KiCad E.D.A.	kicad 7.0.1

Rev: 0.1

Id: 1/3

EPD Power Rails



This schematic is based on one found in TI's "TPS65185 Evaluation Module" user's guide. It is suited for the required rails -20V, -15V, 15V, 22V and 3.3V.

This schematic assumes a VIN power of 3.3V, that is also used for the pull-up resistors of $\overline{\text{INT}}$ and PWR_GOOD.

Sheet: /EPD Power Rails/
File: power_management.kicad_sch

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Size: A4

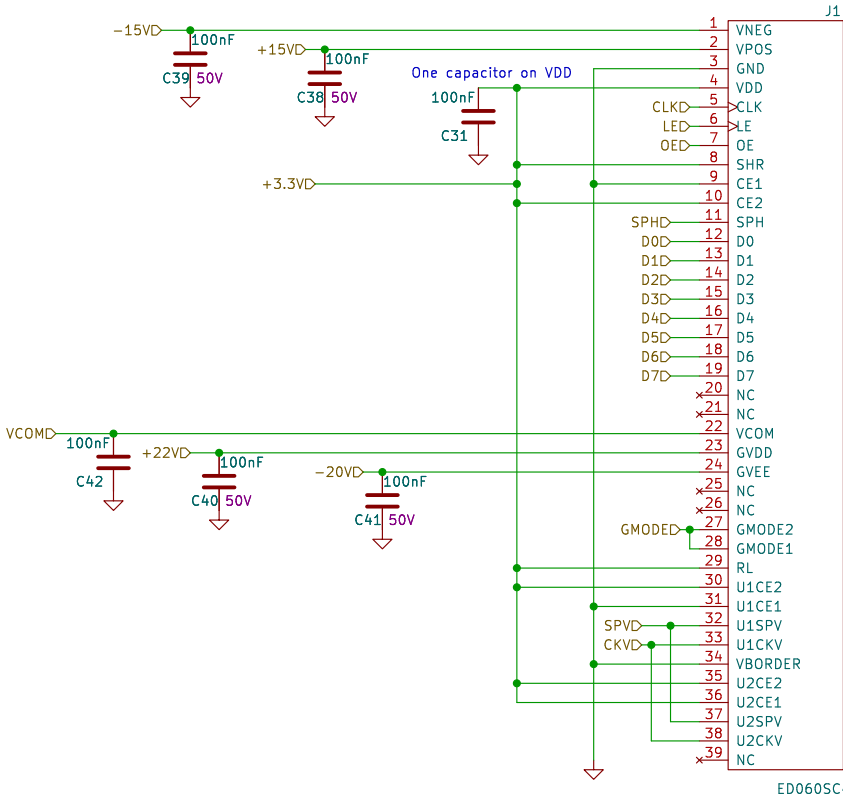
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KiCad E.D.A. kicad 7.0.1

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EPD ED060SC4



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KiCad E.D.A. kicad 7.0.1

Rev:

Id: 3/3