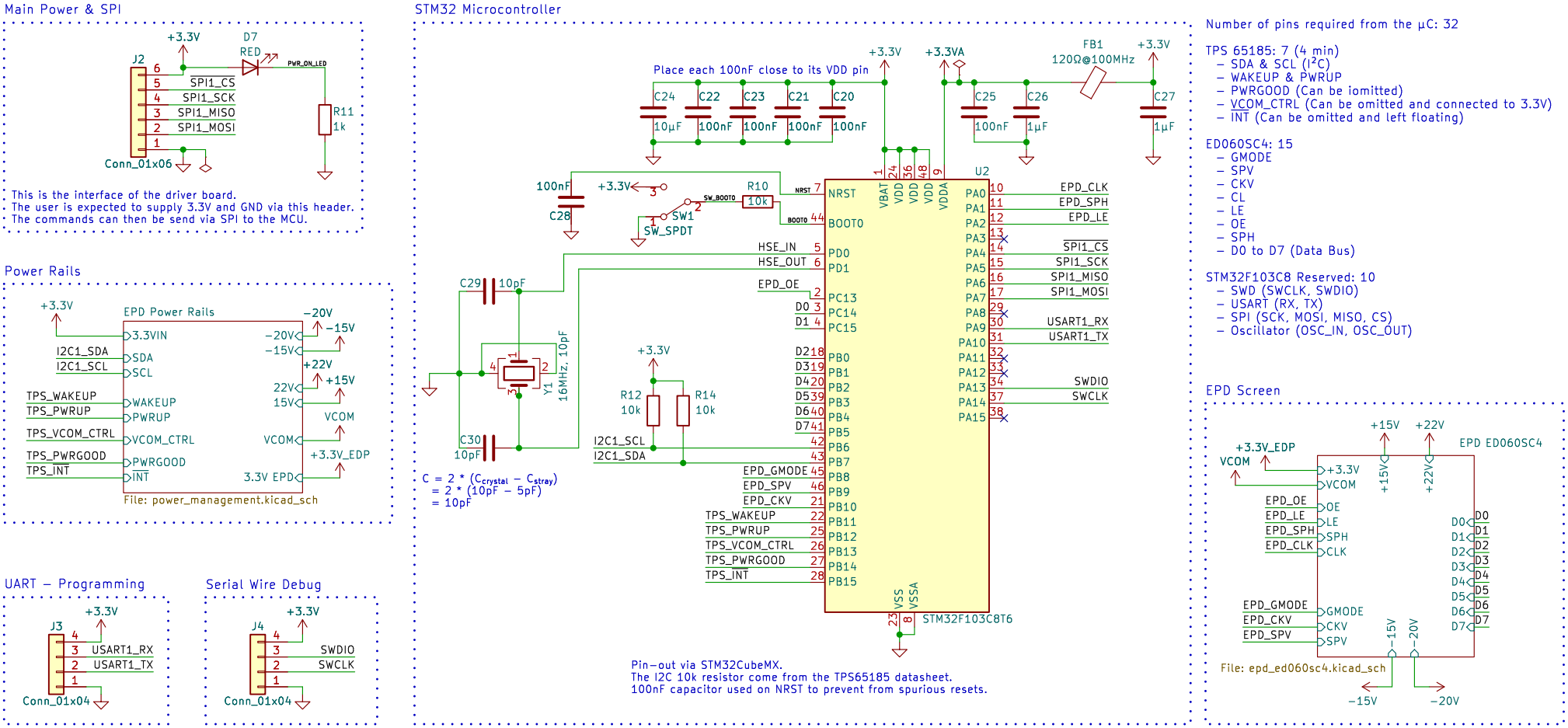


https://github.com/Mael-Le-Garrec/ED060SC4_driver_board

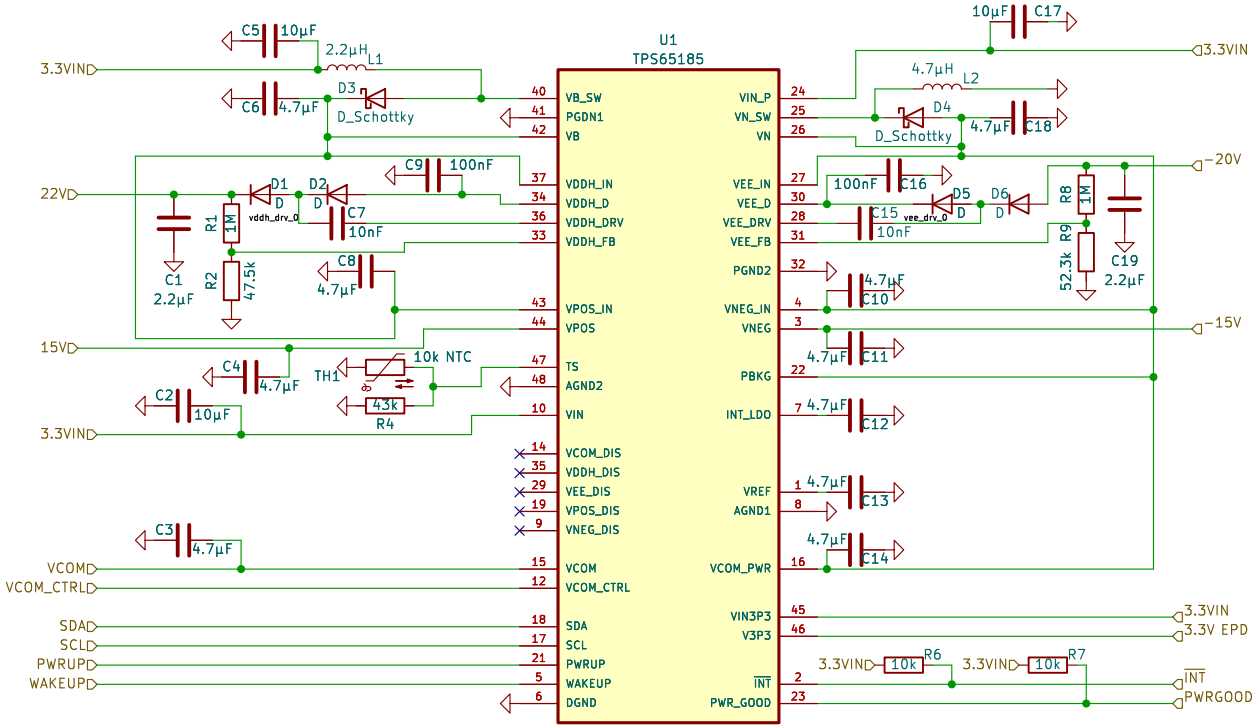
hatrix

Sheet: /

File: ED060SC4_SPI_driver_board.kicad_sch

Title: ED060SC4 SPI Driver Board

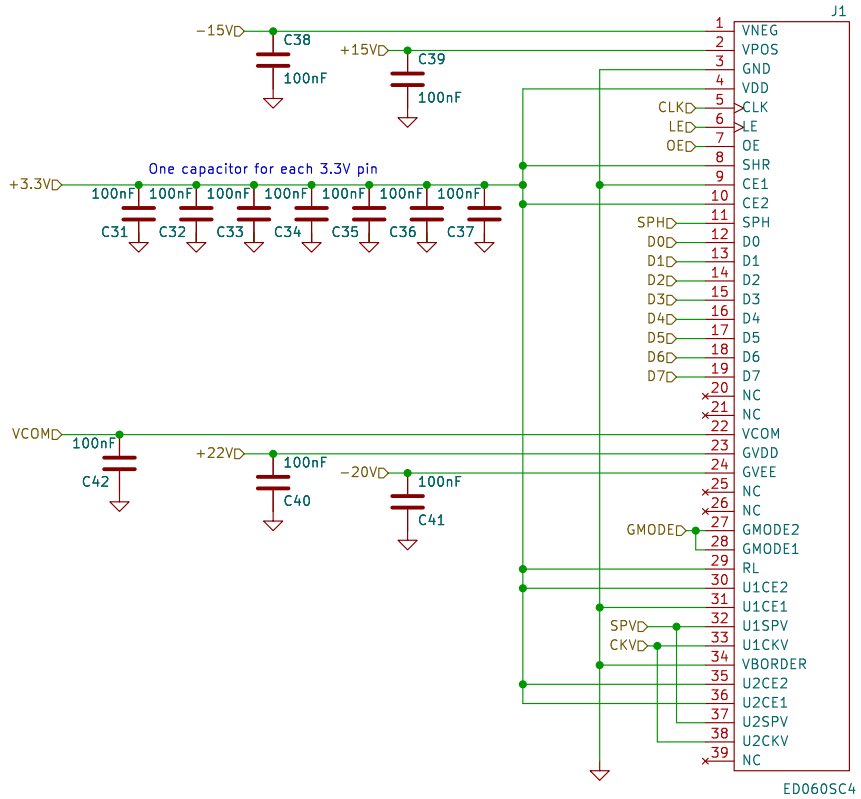
Size: A4	Date: 2023-04-12	**Rev: 0.1**
KiCad E.D.A. kicad (7.0.0)		Id: 1/3



This schematic is based on the "Typical Application" of the TPS65185 datasheet in Section 9.2.
It is suited for the required rails −20V, −15V, 15V, 22V and 3.3V.

This schematic assumes a VIN power of 3.3V, that is also used for the pull−up resistors of $\overline{\text{INT}}$ and PWR_GOOD.

Sheet: /EPD Power Rails/		
File: power_management.kicad_sch		
Title:		
Size: A4	Date:	**Rev: 0.1**
KiCad E.D.A. kicad (7.0.0)		Id: 2/3



100nF capacitors should be OK everywhere.
I got no data whatsoever to support that claim though.

Sheet: /EPD ED060SC4/		
File: epd_ed060sc4.kicad_sch		
Title:		
Size: A4	Date:	**Rev:**
KiCad E.D.A. kicad (7.0.0)		Id: 3/3