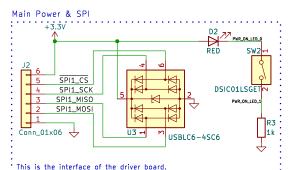
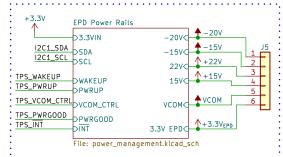
# **ED060SC4 Driver Board**

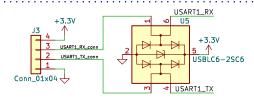


. The user is expected to supply 3.3V and GND via this header.

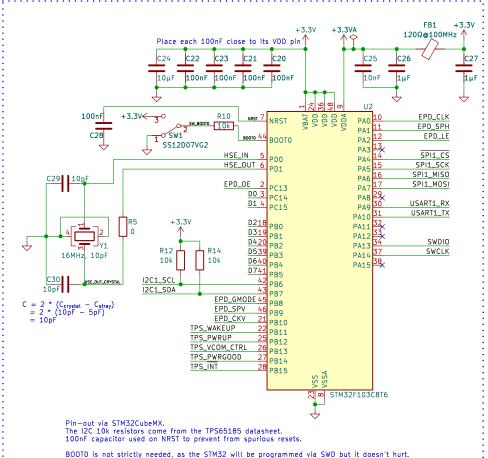
· The commands can then be send via SPI to the MCU.



# UART - Programming



#### STM32 Microcontroller



### Number of pins required from the $\mu C\colon 32$

- TPS 65185: 7 (4 min) - SDA & SCL (I2C)
- WAKEUP & PWRUP
- PWRGOOD (Can be iomitted)
- VCOM\_CTRL (Can be omitted and connected to 3.3V)
- INT (Can be omitted and left floating)

#### ED060SC4: 15 – GMODE

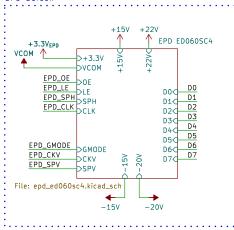
- SPV
- CKV
- CL - LE
- OE
- SPH - D0 to D7 (Data Bus)

## STM32F103C8 Reserved: 10

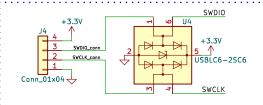
- SWD (SWCLK, SWDIO)
   USART (RX, TX)
   SPI (SCK, MOSI, MISO, CS)

- Oscillator (OSC\_IN, OSC\_OUT)

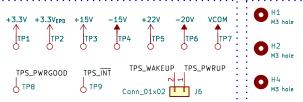
# EPD Screen



#### Serial Wire Debug



# Test Points



#### Mounting Holes



### https://github.com/Mael-Le-Garrec/ED060SC4\_driver\_board

#### hatrix

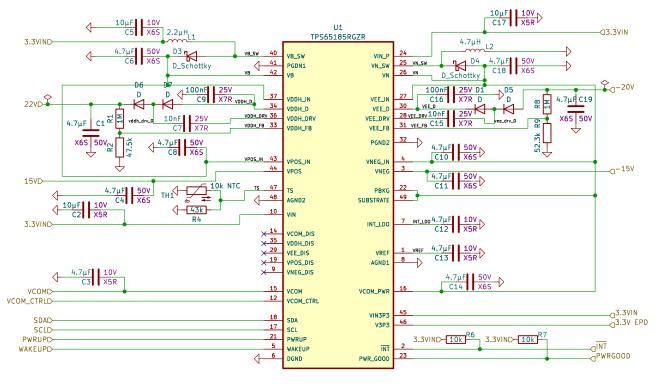
Sheet: /

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#### Title: FD060SC4 SPI Driver Roard

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Size: A4	Date: 2023-04-12	Rev: 0.1
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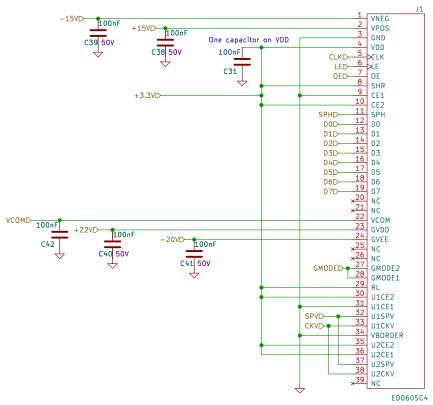
# **EPD Power Rails**



This schematic is based on one found in TI's "TPS65185 Evaluation Module" user's guide. It is suited for the required rails -20V, -15V, 15V, 22V and 3.3V.

This schematic assumes a VIN power of 3.3V, that is also used for the pull-up resistors of INT and PWR\_GOOD.





100nF capacitors should be OK everywhere. I got no data whatsoever to support that claim though.

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