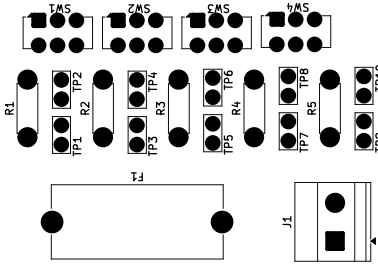


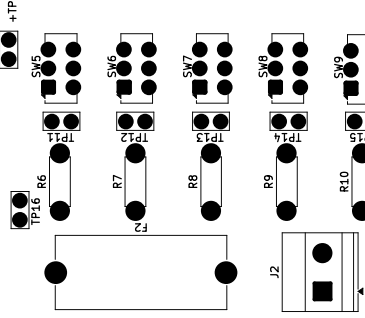
## SERIES RESISTORS



BS ECE – 2A  
©2025–2026  
EE – 2011

Scan for GitHub Repo

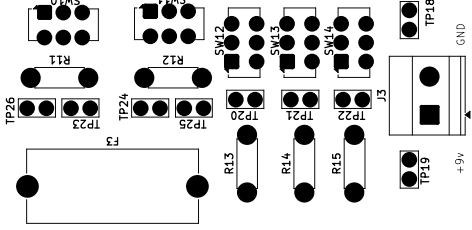
## PARALLEL RESISTORS



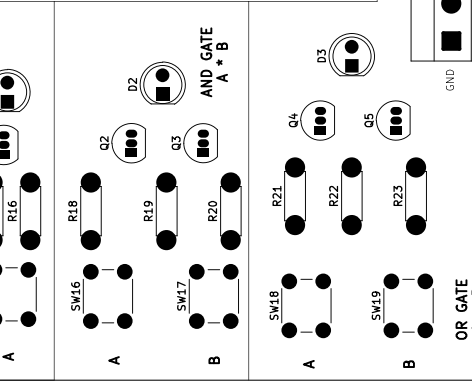
Collaboration: OKSJEAN  
Domingo, Andre Marick C.  
Lauglaug, Jhonas G.  
Magdamin, Royce B.  
Magdamin, John Paul M.  
Tablos, Gean G.



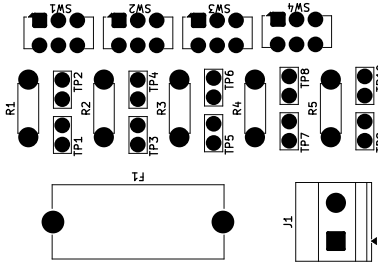
## SERIES-PARALLEL RESISTORS



## FUNDAMENTALS OF LOGIC GATES: NOT, AND, OR



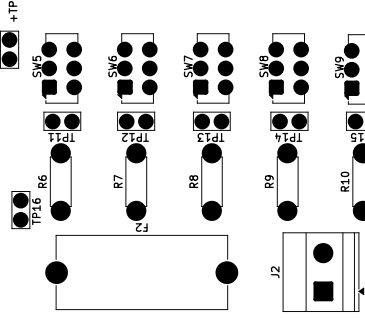
## SERIES RESISTORS



BS ECE – 2A  
©2025–2026  
EE – 2011

Scan for GitHub Repo

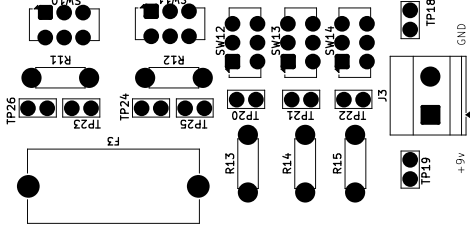
## PARALLEL RESISTORS



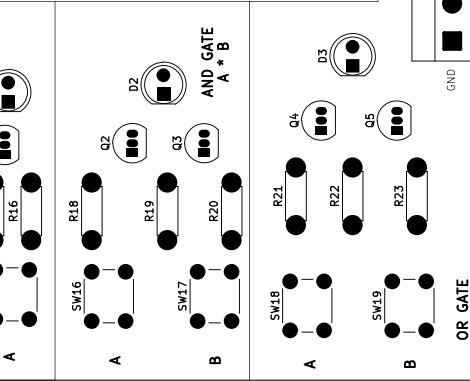
Collaboration: OKSJEAN  
Domingo, Andre Marick C.  
Lauglaug, Jhonas G.  
Magdamin, Royce B.  
Magdamin, John Paul M.  
Tablos, Gean G.



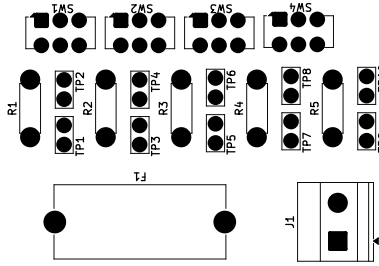
## SERIES-PARALLEL RESISTORS



## FUNDAMENTALS OF LOGIC GATES: NOT, AND, OR



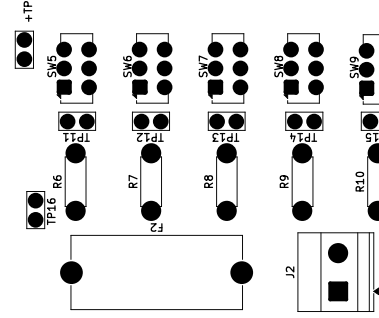
## SERIES RESISTORS



BS ECE – 2A  
©2025–2026  
EE – 2011

Scan for GitHub Repo

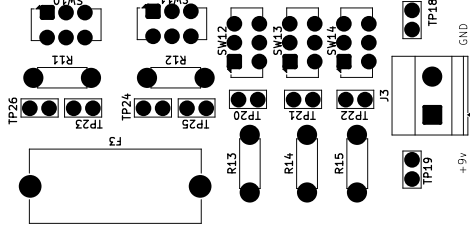
## PARALLEL RESISTORS



Collaboration: OKSJEAN  
Domingo, Andre Marick C.  
Lauglaug, Jhonas G.  
Magdamin, Royce B.  
Magdamin, John Paul M.  
Tablos, Gean G.



## SERIES-PARALLEL RESISTORS



## FUNDAMENTALS OF LOGIC GATES: NOT, AND, OR

