# Exercise 1

1.

Table, calendar

Description automatically generated

|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| Instr | Components/Hardware element used by the instruction | | | | | | | | |
| R-type | Read PC (2ns) | Instruction memory | Control | Register read |  | Mux (PC) | Write PC | Register write |  |
|  | Add (PC) |  |  |  | ALU Control | ALU |  |  |
|  |  |  |  |  |  |  |  |  |
| Load | Read PC (2ns) |  |  |  |  |  |  |  |  |
|  | Instruction memory |  |  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |
|  |  |  |  |  |  |  |  |  |  |

# Exercise 2