# QUESTION

Consider a cache that can store 4 words,

Consider a Main Memory of 64 words

For the following addresses of RAM accesses, find the hits and misses using direct mapping:

☐ 10,12,10,12,63,58,34,10,34,63, 33,10, 63

# QUESTION

Consider a cache that can store 4 words,

Consider a Main Memory of 64 words

LRU is used as replacement policy.

For the following addresses of RAM accesses, find the hits and misses using fully associative mapping:
- 10,12,10,12,63,58,34,10,34,63,33,10, 63

# QUESTION

Consider a cache that can store 4 words,

Consider a Main Memory of 64 words

LRU is used as replacement policy.

For the following addresses of RAM accesses, find the hits and misses using 2 way set associative mapping:

- 10,12,10,12,63,58,34,10,34,63,33,10, 63