INSTRUCTION FOR PIN CHECK

Description:

This script will compare the pin names and pin directions between lef, lib and verilog files. Pass and Fail reports will be generated based on comparison result.

- Run the script with ip type (e.g., IO/MEMORY/LOGIC) and library path.

System Requirements:

- Python 3.7.2+(3.7.2 preferred)

Script Path: /proj/eda_vendor_sgnonsde/IP_QA_ulkasemi/users/QA_Automation/UAT/FECheck/pin_check

Script Extension Setting Path: <Script Path>/EXTENSION

```
VERILOG = {
            .v
}
LEF = {
            .lef,
            .lef_*
}
LIB = {
            .lib,
            .lib_*
}
```

- Change verilog, lef and lib files extension if necessary. For multiple extension use comma (,).

Run Script:

Go to your Run Directory.

```
>>> alias python "/tool/pandora64/.package/python-3.7.2/bin/python3"
```

Help Command:

```
>>> python <Script Path>/pinCheckFE.py --help
```

Run Command:

```
>>> python <Script Path>/pinCheckFE.py -i <IP_TYPE> -l <LIBRARY_PATH>
```

- After <-i> command provide ip type (e.g., IO/MEMORY/LOGIC).
- After <-l> command provide library path from where lef, lib and Verilog files will be compared.

Report:

You will find the excel report in the following path: **<RUN_DIR>/FE_CHECK/report/pin_check.xlsx**

** For Memory all lef, lib and verilog files will be merged into ALL.lef, ALL.lib and ALL.v files to compare.

** For other IP Type files will be compared individually with other views.