

## **Md. Rezwanur Rahman**

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### **Career objective**

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As a highly motivated Electrical & Electronics Engineering student, my career objective is to pursue a graduate degree which will enhance my problem solving skills & broaden my knowledge in specific areas of EEE such as analog & mixed signal IC design, neuromorphic computing, VLSI design etc. I want to incorporate my core knowledge with my programming skills to explore the latest research on computer hardware development. I want to explore how the latest AI revolution is redefining the boundaries of computation. Moreover I'm interested in the rapidly evolving intersection between the physical & digital world using cyber physical systems.

### **Education**

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#### **BSc in Electrical & Electronics Engineering**

**February 2022**

Rajshahi University of Engineering & Technology (RUET)  
Rajshahi, Bangladesh

**CGPA – 3.41 ( Last 60 credit - 3.66 )**

**Core courses:** Electrical circuits , Electronics fundamentals , Engineering Mathematics, Electrical machine, Power system, Power electronics, Control System, Digital electronics, Digital Signal Processing, Microprocessor & interfacing, VLSI design, Embedded systems, Processing & fabrication technology, Communication engineering, Biomedical Engineering etc

### **Test Scores**

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#### **GRE – 312**

**March, 2023**

(Verbal – 150 , Quant -162 , AWA-3.0 )

#### **IELTS – 7**

**November 2022**

( Speaking – 7, Listening – 7.5, Reading – 6.5, Writing -6.5 )

### **Work Experience**

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#### **Industrial Attachment**

**May 2019**

Katakhali 50 MW peaking power plant, BPDB

#### **Workshop on HVDC power generation & Transmission**

**July 2018**

IEEE RUET Student Branch

## Thesis

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### **Design & performance analysis of Aperture coupled patch antenna with defected ground structures using CST & HFSS**

(Under supervision of Prof. Dr. Shamim Anower, EEE, RUET)

Outcome : Aperture coupled patch antenna with E type defected ground structure was simulated which produces 15 dB more return loss, 2.3 Ghz more bandwidth & 2.40 less VSWR than regular ground slot patch antenna. Two major simulation software CST & HFSS were used to compare the outputs.

## Projects

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### **Algorithm Based Projects:**

- Delivery truck routing app using Dijkstra algorithm
- Sudoku solver using Backtracking algorithm
- Phone directory using Trie algorithm

### **IOT projects:**

- Arduino based weather monitoring system
- Fire alarm system using thermistor & 555 timer IC

## Software Skills

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- C++ Programming
- MATLAB
- MICROWIND EDA software
- CST
- Ansys HFSS
- Microsoft Office

## Reference

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### **Dr. Md. Shamim Anower**

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### **Dr. Md Masud Rana**

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