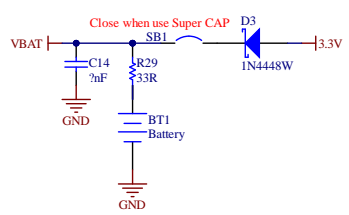


BAT-Backup

Circuit diagram for BAT-Backup:

- 3.3V supply connected to diode D3 (1N4448W).
- Diode D3 is in series with resistor R29 (33R).
- Resistor R29 is connected to node SBI.
- Node SBI is connected to capacitor C14 (2nF) to GND.
- Node SBI is also connected to battery BT1.
- Battery BT1 is labeled "Battery".
- A red arrow points to node SBI with the text "Close when use Super CAP".



3.3V

R1 10K

R2 10K

PA13 SWDIO

PA14 SWCLK

P2

1

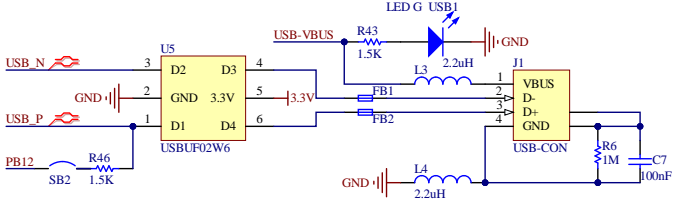
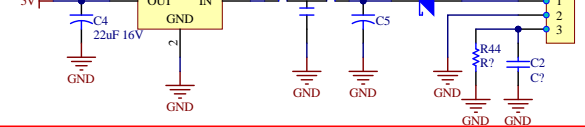
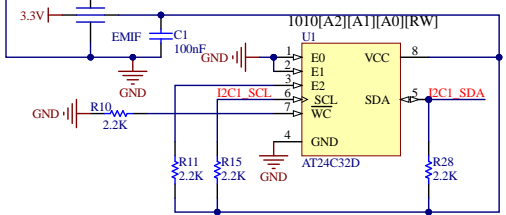
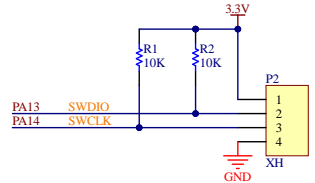
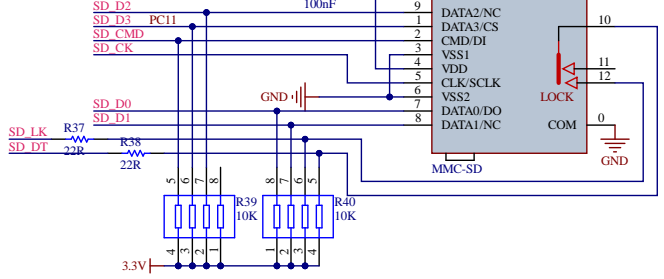
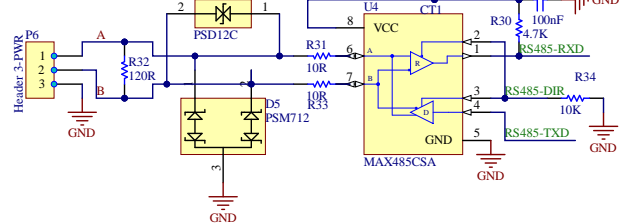
2

3

4

XH

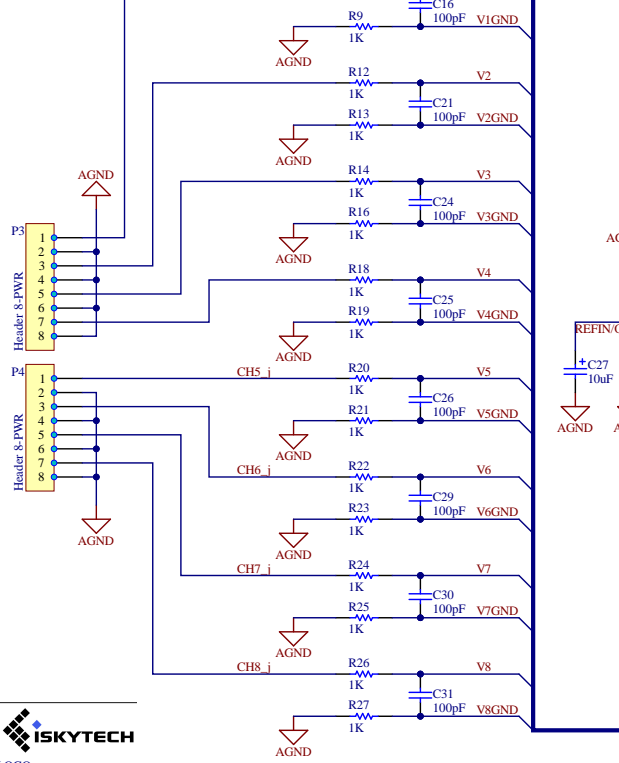
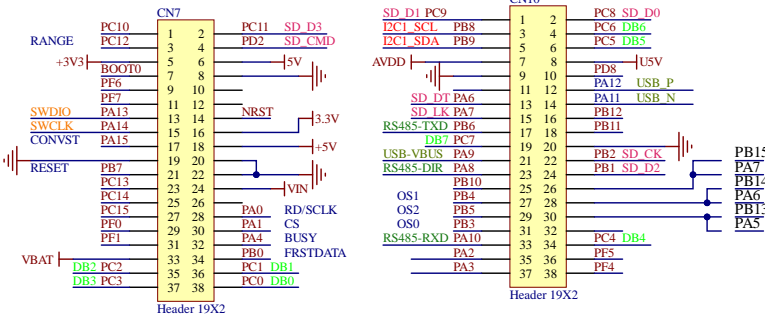
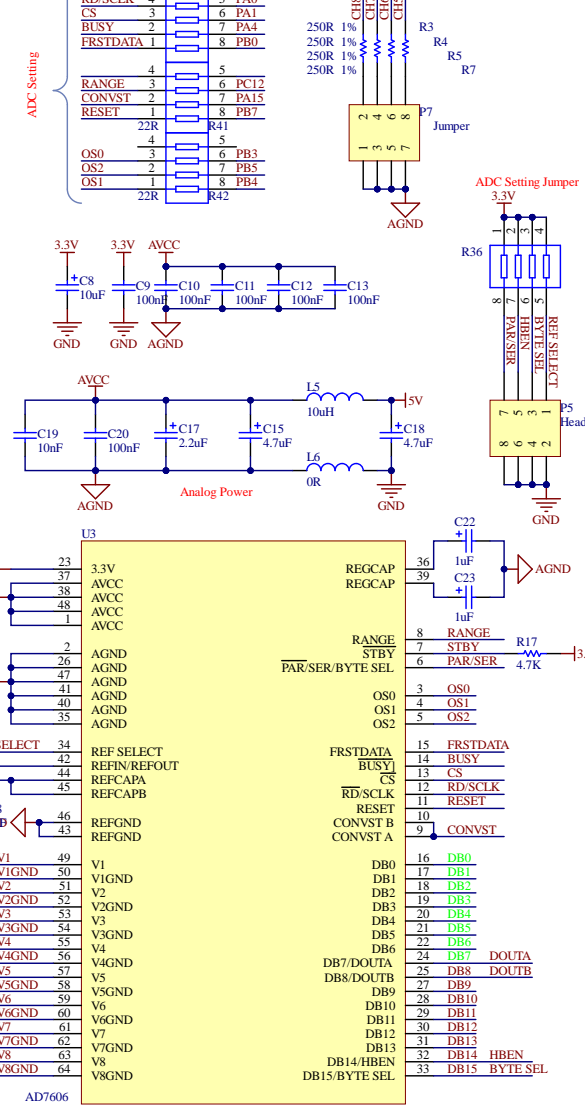
GND

[illegible]

The diagram illustrates the power and ground connections for the IISKYTECH module. It features two 8-pin headers, both labeled 'Header 8-PWR'. The top header is connected to the AGND and V1GND through V8GND pins. The bottom header is connected to the AGND and V1GND through V8GND pins. The connections are as follows:

- Header 8-PWR (Top):**
 - Pin 1: AGND
 - Pin 2: V1GND
 - Pin 3: V2GND
 - Pin 4: V3GND
 - Pin 5: V4GND
 - Pin 6: V5GND
 - Pin 7: V6GND
 - Pin 8: V7GND
- Header 8-PWR (Bottom):**
 - Pin 1: AGND
 - Pin 2: V1GND
 - Pin 3: V2GND
 - Pin 4: V3GND
 - Pin 5: V4GND
 - Pin 6: V5GND
 - Pin 7: V6GND
 - Pin 8: V7GND

The connections are made through 1K resistors (R9-R31) and 100pF capacitors (C16-C31). The AGND and V1GND through V8GND pins are connected to the ground plane. The REFIN/OUT pin is connected to the AGND and V1GND through V8GND pins. The AGND and V1GND through V8GND pins are connected to the ground plane. The AGND and V1GND through V8GND pins are connected to the ground plane.

[illegible][illegible]

Date: 1 December, 2019

Sheet 1 of 1

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Quantity	LibRef	Footprint	Designator	Description	Comment
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