

B.Sc. Computer Engineering student at the University of Tehran (ranked 73/60,000 in the 2023 Iranian National Entrance Exam) with strong foundations in algorithms, data structures, and computer systems. Interested in research at the intersection of algorithms and ML-for-systems, with experience designing an LLM-assisted controlled learning experiment and performing statistical analysis in R.

EDUCATION & TRAININGS

2023 – Expected graduation: 2027	<div>B.Sc. Computer Engineering</div> <div>University of Tehran</div> <ul style="list-style-type: none"> <li>Rank 73 out of 60,000 in the 2023 Iranian National Entrance Exam</li> <li>Active in academic competitions and university associations</li> <li>Major GPA: 3.5/4.0; top 10% of cohort</li> </ul>
2018 – 2023	<div>Diploma in Mathematics &amp; Physics</div> <div>National Organization for Development of Exceptional Talents (NODET)</div> <ul style="list-style-type: none"> <li>Math &amp; Computer Olympiad participant (accepted 2022 in Computer Olympiad)</li> <li>GPA: 3.9/4.0 (Rank 3)</li> </ul>

EXPERIENCE

2024 – Present	<div>LLM-assisted Statistics Learning Experiment</div> <div>Probability &amp; Statistics course project</div> <div>University of Tehran</div> <ul style="list-style-type: none"> <li>Designed a controlled educational experiment with 100 students solving Poisson-distribution exercises under two conditions (control vs. AI-assisted).</li> <li>Evaluated learning via a final exam (no AI tools allowed); approximately 60% of the AI-assisted group outperformed the control group.</li> <li>Implemented data collection, statistical analysis, and visualization in R; summarized results for reporting.</li> </ul> <div>Skills used:</div> <div>PYTHONRDATA ANALYSISEXPERIMENTAL DESIGN</div>
2023 – Present	<div>Student Projects</div> <div>University of Tehran</div> <ul style="list-style-type: none"> <li>Implemented core data structures and algorithms (graphs, dynamic programming, greedy methods) in C and C++ as part of university programming courses (BP, AP).</li> <li>Participated in ACM and IEEE student chapter activities and mentored junior students on basic algorithmic thinking and coding concepts.</li> <li>Completed multiple course projects under tight deadlines while maintaining strong academic performance.</li> </ul> <div>Skills used:</div> <div>C C++PYTHONVERILOGHTMLCSSJAVASCRIPT</div>
2021 – 2022	<div>Full-Stack Development</div> <div>Full-Stack Developer</div> <div>idArasham, Iran</div> <ul style="list-style-type: none"> <li>Developed and maintained web applications using HTML/CSS/JavaScript with backend components in C++/Python.</li> <li>Collaborated with hardware and network teams to integrate web applications with internal infrastructure and resolve deployment/connectivity issues.</li> <li>Proposed and implemented small improvements to increase maintainability.</li> </ul> <div>Skills used:</div> <div>C++PYTHONHTMLCSSJAVASCRIPT</div>

## COURSEWORK

### Done:

Basic Programming (C), Advanced Programming (C++), Discrete Mathematics

### Ongoing:

Probability & Statistics (R), Data Structures & Algorithms (Python), Logic Circuit / Digital Systems (Verilog)

## LANGUAGES

Persian  
English  
German

Native  
C1  
A2

## SKILLS, TOOLS & PLATFORMS

### ✓ Programming languages

- C
- C++
- Python
- R
- JavaScript
- HTML
- CSS
- Verilog

### ✓ Tools & Platforms

- Git
- Linux
- Docker
- VS Code
- ModelSim
- LLMs & Prompt Engineering
- n8n

### ✓ Algorithms & Data

- Graph algorithms; dynamic programming; greedy methods
- Algorithm design and complexity analysis
- Statistical analysis and visualization in R
- Experimental design (A/B-style evaluation)

### ✓ CS Fundamentals

- Computer architecture and system operations
- Operating systems basics
- Database concepts and SQL
- Object-oriented programming (OOP)

## RESEARCH INTERESTS

- Algorithms and complexity; graph algorithms and combinatorial optimization
- Learning-augmented algorithms and ML-for-systems (scheduling, caching, resource allocation)
- Operating systems, distributed systems, and efficient runtime systems
- Hardware–software co-design and digital systems (Verilog-based architectures, accelerators)