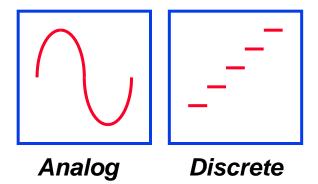
CHAPTER 1: BINARY SYSTEMS

- **★ DIGITAL COMPUTER & DIGITAL SYSTEMS**
- ***** BINARY NUMBERS
- **★ NUMBER BASE CONVERSION**
- ***** COMPLEMENTS
- **★ SIGNED BINARY NUMBERS**
- ***** BINARY CODES
- **★** BINARY STORAGE ELEMENTS

Digital Systems

★ Discrete Data

- Examples:
 - ♦ 26 letters of the alphabet (A, B ... etc)
 - **♦** 10 decimal digits (0, 1, 2 ... etc)



- Combine together
 - **♦** Words are made of letters (University ... etc)
 - **♦** Numbers are made of digits (4241 ... etc)

★ Binary System

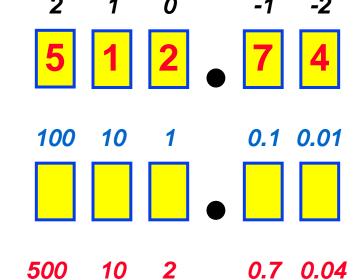
- Only '0' and '1' digits
- Can be easily implemented in electronic circuits

Decimal Number System

- **★** Base (also called radix) = 10
 - 10 digits { 0, 1, 2, 3, 4, 5, 6, 7, 8, 9 }



- **★ Digit Position**
 - Integer & fraction
- **★ Digit Weight**
 - Weight = $(Base)^{Position}$
- **★** Magnitude
 - Sum of "Digit x Weight"
- **★** Formal Notation

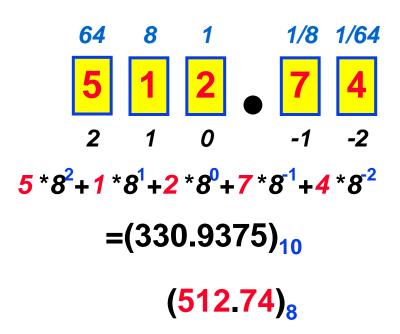


$$d_2^*B^2+d_1^*B^1+d_0^*B^0+d_{-1}^*B^{-1}+d_{-2}^*B^{-2}$$

 $(512.74)_{10}$

Octal Number System

- \star Base = 8
 - 8 digits { 0, 1, 2, 3, 4, 5, 6, 7 }
- **★ Weights**
 - Weight = $(Base)^{Position}$
- **★ Magnitude**
 - Sum of "Digit x Weight"
- **★** Formal Notation



Binary Number System

- \star Base = 2
 - 2 digits { 0, 1 }, called binary digits or "bits"
- **★ Weights**
 - Weight = $(Base)^{Position}$
- **★ Magnitude**
 - Sum of "Bit x Weight"
- **★** Formal Notation
- **★** Groups of bits

- 4
 2
 1
 1/2
 1/4

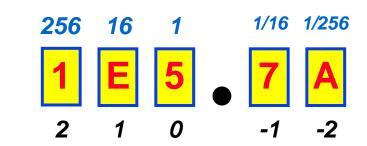
 1
 0
 1
 0
 1

 2
 1
 0
 -1
 -2
- $1*2^{2}+0*2^{1}+1*2^{0}+0*2^{-1}+1*2^{-2}$
 - $=(5.25)_{10}$
 - $(101.01)_2$
- 4 bits = Nibble 1 0 1 1
- 8 bits = Byte

11000101

Hexadecimal Number System

- \star Base = 16
 - 16 digits { 0, 1, 2, 3, 4, 5, 6, 7, 8, 9, A, B, C, D, E, F }
- **★ Weights**
 - Weight = $(Base)^{Position}$
- **★** Magnitude
 - Sum of "Digit x Weight"
- **★** Formal Notation



$$1*16^{2}+14*16^{1}+5*16^{0}+7*16^{-1}+10*16^{-2}$$

$$=(485.4765625)_{10}$$

 $(1E5.7A)_{16}$

The Power of 2

| n | 2 ⁿ |
|---|------------------------------------|
| 0 | 20=1 |
| 1 | 21=2 |
| 2 | 22=4 |
| 3 | 2 ³ = 8 |
| 4 | 2 ⁴ = 1 6 |
| 5 | 25=32 |
| 6 | 26=64 |
| 7 | 2 ⁷ = 128 |

| n | 2 ⁿ |
|----|-----------------------|
| 8 | 28=256 |
| 9 | 29=512 |
| 10 | $2^{10} = 1024$ |
| 11 | 211=2048 |
| 12 | 212=4096 |
| 20 | 2 ²⁰ =1M |
| 30 | 2 ³⁰ =1G |
| 40 | 2 ⁴⁰ =1T |

Kilo

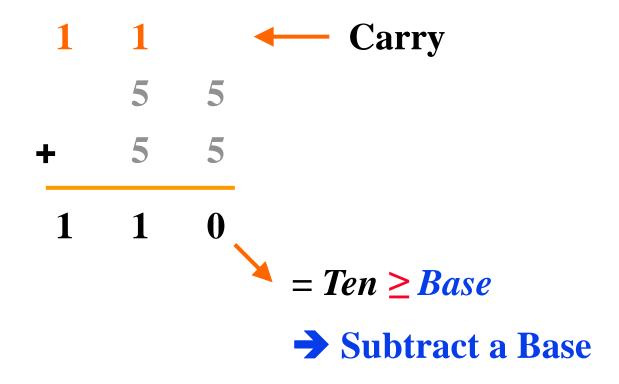
Mega

Giga

Tera

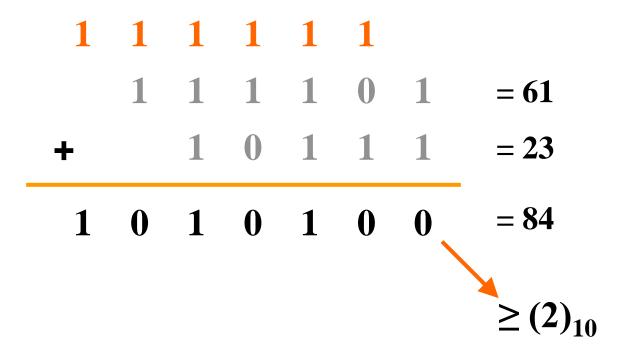
Addition

★ Decimal Addition



Binary Addition

★ Column Addition



Binary Subtraction

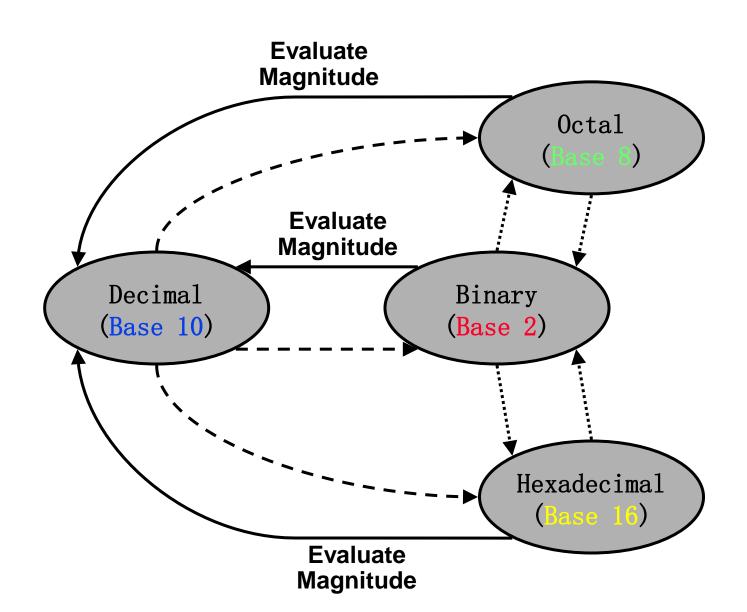
★ Borrow a "Base" when needed

Binary Multiplication

★ Bit by bit

| | | | 1 | 0 | 1 | 1 | 1 |
|---|---|---|---|---|---|---|---|
| X | | | | 1 | 0 | 1 | 0 |
| | | | 0 | 0 | 0 | 0 | 0 |
| | | 1 | 0 | 1 | 1 | 1 | |
| | 0 | 0 | 0 | 0 | 0 | | |
| 1 | 0 | 1 | 1 | 1 | | | |
| 1 | 1 | 1 | 0 | 0 | 1 | 1 | 0 |

Number Base Conversions



Decimal (Integer) to Binary Conversion

- **★** Divide the number by the 'Base' (=2)
- **★** Take the remainder (either 0 or 1) as a coefficient
- **★** Take the quotient and repeat the division

Example: $(13)_{10}$

| | Quotient | Remainder | Coefficient |
|----------------|----------|-------------|-------------------------|
| 13 /2 = | 6 | 1 | $a_0 = 1$ |
| 6 / 2 = | 3 | 0 | $a_1 = 0$ |
| 3 / 2 = | 1 | 1 | $a_{2}^{-} = 1$ |
| 1 / 2 = | 0 | 1 | $a_3 = 1$ |
| Answ | er: (1 | $(a_3 a_2)$ | $a_1 a_0)_2 = (1101)_2$ |
| | | 1 | |
| | | MSB | LSB |

Decimal (*Fraction*) to Binary Conversion

- **★** Multiply the number by the 'Base' (=2)
- **★** Take the integer (either 0 or 1) as a coefficient
- **★** Take the resultant fraction and repeat the division

Example: $(0.625)_{10}$

Answer:
$$(0.625)_{10} = (0.a_{-1} a_{-2} a_{-3})_2 = (0.101)_2$$

MSB LSB

Decimal to Octal Conversion

Example: $(175)_{10}$

Quotient Remainder Coefficient
$$175 / 8 = 21$$
 7 $a_0 = 7$ $21 / 8 = 2$ 5 $a_1 = 5$ $2 / 8 = 0$ 2 $a_2 = 2$

Answer:
$$(175)_{10} = (a_2 a_1 a_0)_8 = (257)_8$$

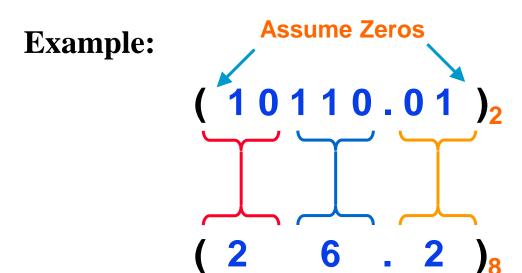
Example: $(0.3125)_{10}$

Answer:
$$(0.3125)_{10} = (0.a_{-1} a_{-2} a_{-3})_8 = (0.24)_8$$

Binary – Octal Conversion

$$\star 8 = 2^3$$

★ Each group of 3 bits represents an octal digit



| Octal | Binary |
|-------|--------|
| 0 | 0 0 0 |
| 1 | 001 |
| 2 | 010 |
| 3 | 011 |
| 4 | 100 |
| 5 | 101 |
| 6 | 110 |
| 7 | 111 |

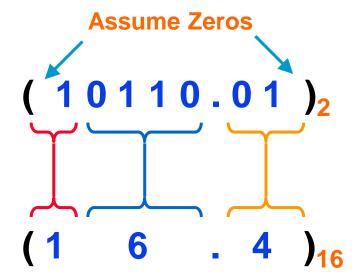
Works both ways (Binary to Octal & Octal to Binary)

Binary - Hexadecimal Conversion

$$\star 16 = 2^4$$

★ Each group of 4 bits represents a hexadecimal digit





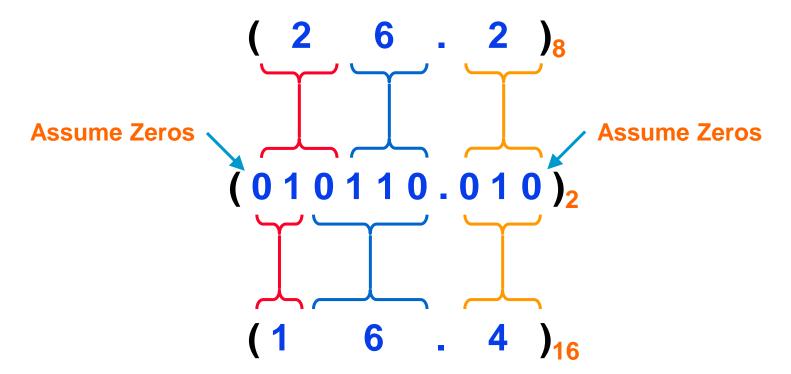
| Hex | Binary |
|---------|--------|
| 0 | 0000 |
| 1 | 0001 |
| 2 | 0010 |
| 3 | 0011 |
| 4 | 0100 |
| 5 | 0101 |
| 6 | 0110 |
| 7 | 0111 |
| 8 | 1000 |
| 9 | 1001 |
| A | 1010 |
| В | 1011 |
| C | 1100 |
| D | 1101 |
| ${f E}$ | 1110 |
| ${f F}$ | 1111 |

Works both ways (Binary to Hex & Hex to Binary)

Octal - Hexadecimal Conversion

★ Convert to Binary as an intermediate step

Example:



Works both ways (Octal to Hex & Hex to Octal)

Decimal, Binary, Octal and Hexadecimal

| Decimal | Binary | Octal | Hex |
|----------------|--------|-------|-----|
| 00 | 0000 | 00 | 0 |
| 01 | 0001 | 01 | 1 |
| 02 | 0010 | 02 | 2 |
| 03 | 0011 | 03 | 3 |
| 04 | 0100 | 04 | 4 |
| 05 | 0101 | 05 | 5 |
| 06 | 0110 | 06 | 6 |
| 07 | 0111 | 07 | 7 |
| 08 | 1000 | 10 | 8 |
| 09 | 1001 | 11 | 9 |
| 10 | 1010 | 12 | A |
| 11 | 1011 | 13 | В |
| 12 | 1100 | 14 | C |
| 13 | 1101 | 15 | D |
| 14 | 1110 | 16 | E |
| 15 | 1111 | 17 | F |

Complements

- **★ 1's Complement (***Diminished Radix* Complement)
 - All '0's become '1's
 - All '1's become '0's

Example (10110000)₂

 $\Rightarrow (01001111)_2$

If you add a number and its 1's complement ...

 $\begin{array}{r}
 10110000 \\
 + 01001111 \\
 \hline
 11111111
 \end{array}$

Complements

★ 2's Complement (*Radix* Complement)

• Take 1's complement then add 1

• Toggle all bits to the left of the first '1' from the right

Example:

OR

Number: 10110000 10110000

1's Comp.: 01001111

+ 1

01010000 01010000

Negative Numbers

- **★** Computers Represent Information in '0's and '1's
 - '+' and '-' signs have to be represented in '0's and '1's
- **★3 Systems**
 - Signed Magnitude
 - 1's Complement
 - 2's Complement

All three use the *left-most bit* to represent the sign:

- **♦ '0' ⇒ positive**
- ♦ '1' ⇒ negative

Signed Magnitude Representation

★ Magnitude is magnitude, does not change with sign



$$(+3)_{10} \Rightarrow (\ 0\ 0\ 1\ 1)_{2}$$
 $(-3)_{10} \Rightarrow (\ 1\ 0\ 1\ 1)_{2}$
Sign Magnitude

★ Can't include the sign bit in 'Addition'

$$\begin{array}{c} 0 \ 0 \ 1 \ 1 \Rightarrow (+3)_{10} \\ + 1 \ 0 \ 1 \ 1 \Rightarrow (-3)_{10} \\ \hline \\ 1 \ 1 \ 1 \ 0 \Rightarrow (-6)_{10} \end{array}$$

1's Complement Representation

- **★** Positive numbers are represented in "Binary"
 - Magnitude (Binary)
- **★** Negative numbers are represented in "1's Comp."
 - Code (1's Comp.)

$$(+3)_{10} \Rightarrow (0\ 011)_{2}$$

$$(-3)_{10} \Rightarrow (1\ 100)_{2}$$

★ There are 2 representations for '0'

$$(+0)_{10} \Rightarrow (0\ 000)_{2}$$

$$(-0)_{10} \Rightarrow (1\ 111)_{2}$$

1's Complement Range

★ 4-Bit Representation

$$2^4 = 16$$
 Combinations

$$-7 \leq \text{Number} \leq +7$$

$$-2^3 + 1 \le \text{Number} \le +2^3 - 1$$

★ n-Bit Representation

$$-2^{n-1}+1 \le \text{Number} \le +2^{n-1}-1$$

| _ | |
|----------------|-----------|
| Decimal | 1's Comp. |
| + 7 | 0111 |
| + 6 | 0110 |
| + 5 | 0101 |
| + 4 | 0100 |
| + 3 | 0011 |
| + 2 | 0010 |
| + 1 | 0001 |
| + 0 | 0000 |
| -0 | 1111 |
| -1 | 1110 |
| -2 | 1101 |
| -3 | 1100 |
| -4 | 1011 |
| -5 | 1010 |
| -6 | 1001 |
| - 7 | 1000 |

2's Complement Representation

- **★** Positive numbers are represented in "Binary"
 - Magnitude (Binary)
- **★** Negative numbers are represented in "2's Comp."
 - 1 Code (2's Comp.)

$$(+3)_{10} \Rightarrow (0\ 011)_{2}$$

$$(-3)_{10} \Rightarrow (1\ 101)_2$$

★ There is 1 representation for '0' 1's Comp. 1111

$$(+0)_{10} \Rightarrow (0\ 000)_{2} \qquad + \qquad 1$$

$$(-0)_{10} \Rightarrow (0\ 000)_{2} \qquad 1 \quad 0 \quad 0 \quad 0$$

25 / 45

2's Complement Range

★ 4-Bit Representation

$$2^4 = 16$$
 Combinations

$$-8 \le Number \le +7$$

$$-2^3 \le \text{Number} \le +2^3 - 1$$

★ n-Bit Representation

$$-2^{n-1} \le \text{Number} \le +2^{n-1}-1$$

| Decimal | 2's Comp. |
|-----------|-----------|
| + 7 | 0111 |
| + 6 | 0110 |
| + 5 | 0101 |
| + 4 | 0100 |
| + 3 | 0011 |
| + 2 | 0010 |
| + 1 | 0001 |
| + 0 | 0000 |
| -1 | 1111 |
| -2 | 1110 |
| -3 | 1101 |
| -4 | 1100 |
| -5 | 1011 |
| -6 | 1010 |
| -7 | 1001 |
| -8 | 1000 |

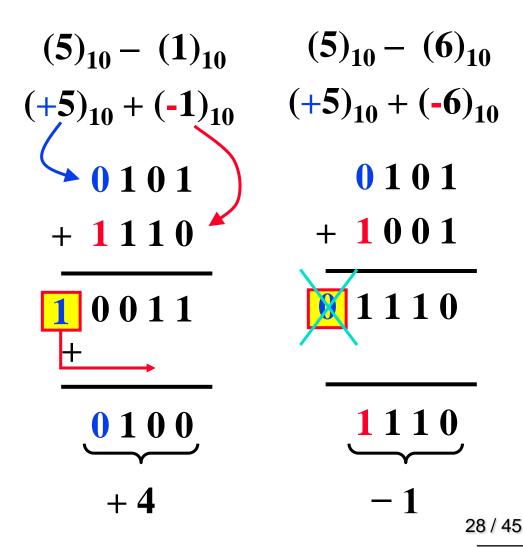
Number Representations

★ 4-Bit Example

| | Unsigned Binary | Signed Magnitude | 1's Comp. | 2's Comp. |
|----------|--------------------|---------------------|-------------|-------------|
| Range | $0 \le N \le 15$ | -7 ≤ N ≤ +7 | -7 ≤ N ≤ +7 | -8 ≤ N ≤ +7 |
| Positive | | 0 | 0 | 0 |
| | Binary | Binary | Binary | Binary |
| Negative | X | 1 0 0 | 1 0 0 | 1 |
| | | Binary | 1's Comp. | 2's Comp. |

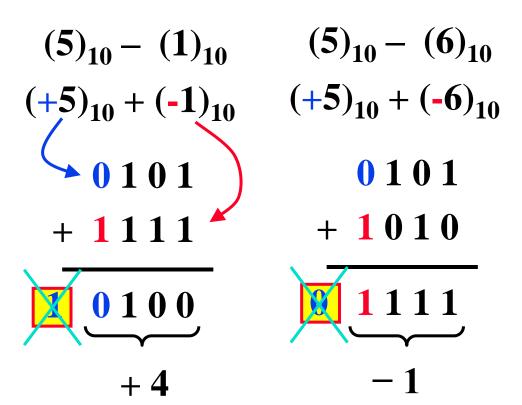
Binary Subtraction Using 1's Comp. Addition

- **★** Change "Subtraction" to "Addition"
- ★ If "Carry" = 1 then add it to the LSB, and the result is positive (in Binary)
- ★ If "Carry" = 0 then the result is negative (in 1's Comp.)



Binary Subtraction Using 2's Comp. Addition

- **★** Change "Subtraction" to "Addition"
- **★** If "Carry" = 1 ignore it, and the result is positive (in Binary)
- ★ If "Carry" = 0 then the result is negative (in 2's Comp.)



Binary Codes

\star Group of *n* bits

- Up to 2^n combinations
- Each combination represents an element of information

★ Binary Coded Decimal (BCD)

- Each Decimal Digit is represented by 4 bits
- $(0-9) \Rightarrow$ Valid combinations
- $(10-15) \Rightarrow$ Invalid combinations

| Decimal | BCD |
|---------|------|
| 0 | 0000 |
| 1 | 0001 |
| 2 | 0010 |
| 3 | 0011 |
| 4 | 0100 |
| 5 | 0101 |
| 6 | 0110 |
| 7 | 0111 |
| 8 | 1000 |
| 9 | 1001 |

BCD Addition

- **★** One decimal digit + one decimal digit

Example:

$$\begin{array}{c} + 3 \\ + 0011 \\ \hline 8 \\ & > 1000 \end{array}$$

• If the result is two decimal digits (≥ 10), then binary addition gives invalid combinations

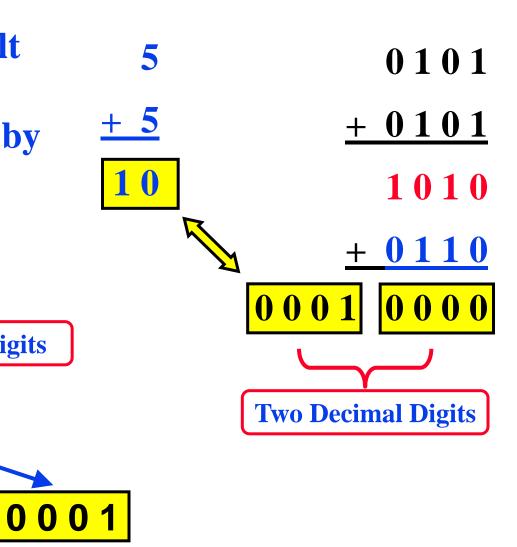
Example: 5 0101 + 5 + 0101 $0001 \longrightarrow 10$ 1010

BCD Addition

★ If the binary result is greater than 9, correct the result by adding 6

Multiple Decimal Digits

0101



Gray Code

- **★** One bit changes from one code to the next code
- **★ Different than Binary**

| Decimal | Gray |
|----------------|------|
| 00 | 0000 |
| 01 | 0001 |
| 02 | 0011 |
| 03 | 0010 |
| 04 | 0110 |
| 05 | 0111 |
| 06 | 0101 |
| 07 | 0100 |
| 08 | 1100 |
| 09 | 1101 |
| 10 | 1111 |
| 11 | 1110 |
| 12 | 1010 |
| 13 | 1011 |
| 14 | 1001 |
| 15 | 1000 |

| Binary |
|--------|
| 0000 |
| 0001 |
| 0010 |
| 0011 |
| 0100 |
| 0101 |
| 0110 |
| 0111 |
| 1000 |
| 1001 |
| 1010 |
| 1011 |
| 1100 |
| 1101 |
| 1110 |
| 1111 |

ASCII Code

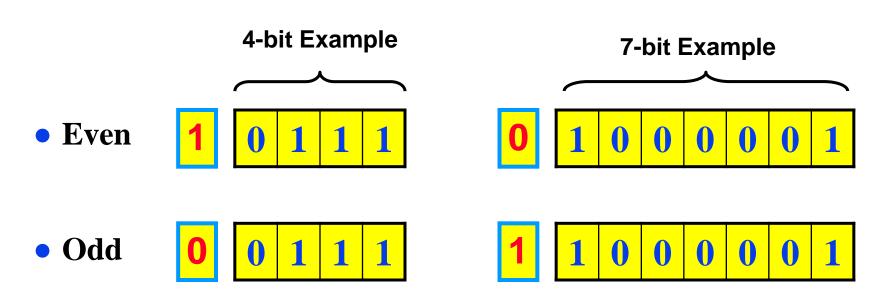
American Standard Code for Information Interchange

| Info | 7-bit Code |
|-------------------------|------------|
| A | 1000001 |
| В | 1000010 |
| • | • |
| • | • |
| $\overline{\mathbf{Z}}$ | 1011010 |
| | 1011010 |
| _ | 1100001 |
| a | 1100001 |
| b | 1100010 |
| • | • |
| • | • |
| • | • |
| Z | 1111010 |
| | |
| @ | 1000000 |
| ? | 0111111 |
| + | 0101011 |
| | |

Error Detecting Codes

★ Parity

One bit added to a group of bits to make the total number of '1's (including the parity bit) even or odd



★ Good for checking single-bit errors

Binary Logic

★ Operators

NOT

AND

Otherwise
$$z' = 0$$

• OR

Otherwise
$$z' = 0$$

Binary Logic

★ Truth Tables, Boolean Expressions, and Logic Gates

AND

| x | y | Z |
|---|---|---|
| 0 | 0 | 0 |
| 0 | 1 | 0 |
| 1 | 0 | 0 |
| 1 | 1 | 1 |

$$z = x \cdot y = x y$$

$$x$$
 y $-z$

OR

| x | y | z |
|---|---|---|
| 0 | 0 | 0 |
| 0 | 1 | 1 |
| 1 | 0 | 1 |
| 1 | 1 | 1 |

$$z = x + y$$

NOT

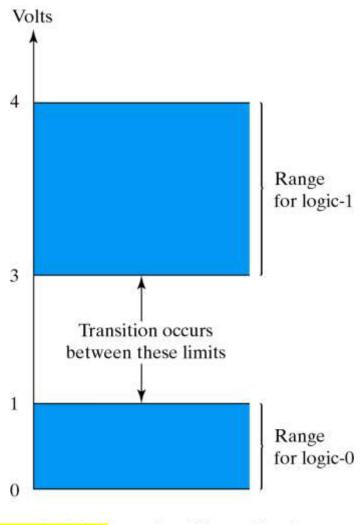
| x | z |
|---|---|
| 0 | 1 |
| 1 | 0 |

$$z = \overline{x} = x'$$

$$x \longrightarrow z$$

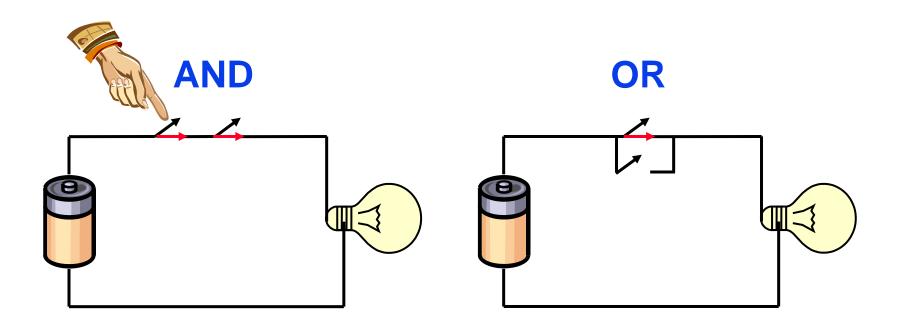
Logic Signals

- **★** Binary '0' is represented by a "low" voltage (range of voltages)
- **★** Binary '1' is represented by a "high" voltage (range of voltages)
- **★** The "voltage ranges" guard against noise



Example of binary signals

Switching Circuits



- **★ Mano**
 - Chapter 1
 - **♦** 1-2
 - **♦** 1-7
 - **♦** 1-9
 - **♦ 1-10**
 - **♦ 1-11**
 - **♦ 1-16**
 - **♦** 1-18
 - **♦** 1-20
 - **◆ 1-24(a)**
 - **♦ 1-29**

- **★** Write your family name in ASCII with odd parity
- **★** Decode the following ASCII string (with MSB = parity):

Is the parity even or odd?

★ Mano

- 1-2 What is the exact number of bytes in a system that contains (a) 32K byte, (b) 64M byte, and (c) 6.4G byte?
- 1-7 Express the following numbers in decimal: $(10110.0101)_2$, $(16.5)_{16}$, and $(26.24)_8$.
- 1-9 Convert the hexadecimal number 68BE to binary and then from binary convert it to octal.
- 1-10 Convert the decimal number 345 to binary in two ways:
 (a) convert directly to binary, (b) convert first to
 hexadecimal, then from hexadecimal to binary. Which
 method is faster?

- **1-11** Do the following conversion problems:
 - (a) Convert decimal 34.4375 to binary.
 - (b) Calculate the binary equivalent of 1/3 out to 8 places. Then convert from binary to decimal. How close is the result to 1/3?
 - (c) Convert the binary result in (b) into hexadecimal. Then convert the result to decimal. Is the answer the same?
- 1-16 Obtain the 1's and 2's complements of the following binary numbers:
 - (a) 11101010 (b) 011111110 (c) 00000001 (d) 10000000
 - (e) 00000000

- 1-18 Perform subtraction on the following unsigned binary numbers using the 2's-complement of the subtrahend. Where the result should be negative, 2's complement it and affix a minus sign.
 - (a) 11011 11001 (b) 110100 10101 (c) 1011 110000 (d) 101010 101011
- 1-20 Convert decimal +61 and +27 to binary using the signed-2's complement representation and enough digits to accommodate the numbers. Then perform the binary equivalent of (+27) + (-61), (-27) + (+61) and (-27) + (-61). Convert the answers back to decimal and verify that they are correct.

- 1-24 Represent decimal number 6027 in (a) BCD
- 1-29 The following is a string of ASCII characters whose bit patterns have been converted into hexadecimal for compactness: 4A EF 68 6E 20 C4 EF E5. Of the 8 bits in each pair digit, the leftmost is a parity bit. The remaining bits are the ASCII code.
 - (a) Convert to bit form and decode the ASCII
 - (b) Determine the parity used: odd or even.