Maheedhar A

Full Stack Developer/Digital Native

I am a motivated Full Stack Developer/ML Enthusiast with a wide knowledge in the field of Programming.



maheedharsumson@gmail.com

Chennai, India

in linkedin.com/in/maheedhar-a-b06a64194

978 980 8813

portfolio-mahi.netlify.app/

github.com/Maheedhar15

EDUCATION

B.S in Data Science and Applications Indian Institute of Technology Madras

10/2020 - 04/2023

GPA - 7.8

Courses

- Diploma in Programming
- Full Stack Web
 Development along with
 Linux Commands
- Programming using JAVA
- Data Structures and Algorithms in Python
- Database Management Systems

- Diploma in Data Science
- Business Data Management
- Tools in Data Science
- Machine Learning Techniques
- Business Analytics

B.E in Electronics and Communication Engineering

SSN College of Engineering

10/2020 - 04/2023

GPA - 8.55

Courses

- Linear Integrated Circuits
- Analog Communication
- Linear Algebra

SKILLS

Frontend Development

communication

Critical Thinking

Backend Development

Leadership

Python

Computational and Algorithmic thinking

Crash-n-watch (04/2023 - 04/2023)

PERSONAL PROJECTS

 This app was created using ReactJs as the frontend and RapidAPI for collecting the data.

Kanban Application (08/2022 - 09/2022)

 This Project was created using Vue JS as frontend and flask as backed and helps the users to track daily activities and remind them if they have any backlogs.

Campus Placement prediction using Machine Learning (03/2023 - 04/2023)

 This project can be used to predict the placement of students using various ML models like Logistic Regression, Decision Trees, Gradient Boosting Classifier etc.

WORK EXPERIENCE

Summer Training Intern

Defence Electronics Research Laboratory (DRDO)

08/2022 - 09/2022

Hyderabad, India

Achievements/Tasks

- Gained Knowledge about the history of programmable electronics
- Gained Knowledge about the Structure and functioning of FPGA and how it is going to be the future of the electronics industry
- Programmed the FPGA using the software Vivado Design Suite using VHDL Programming
- Created an Application using C language in the Software Development Kit of the FPGA to debug and test the programmed FPGA

Contact: Srinivasa Seshachary (Mentor)

ROLES

SSN IEEE Club (09/2022 - 04/2023)

Web Master at SSN IEEE

ACHIEVEMENTS

Five Star Coder(Python) at Hackerrank

Four Star Coder(Problem Solving) at Hackerrank

LANGUAGES

English

Full Professional Proficiency

Tamil

Native or Bilingual Proficiency

Hindi

Limited Working Proficiency