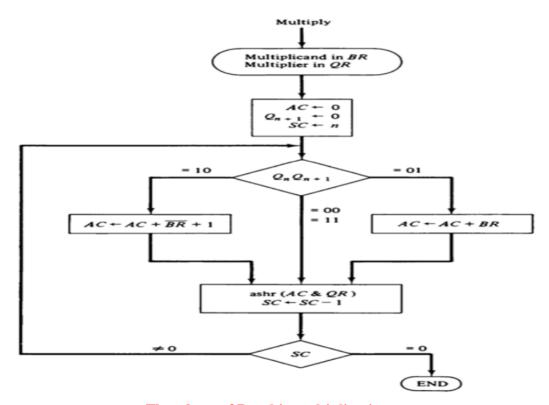
# COMPUTER ORGANIZATION AND ARCHITECTURE <u>UNIT –III</u>

## **TOPIC- BOOTH'S MULTIPLICATION ALGORITHM PART-2**

### Flowchart of Booth's algorithm



Flowchart of Booth's multiplication

#### **EXAMPLE 1:**

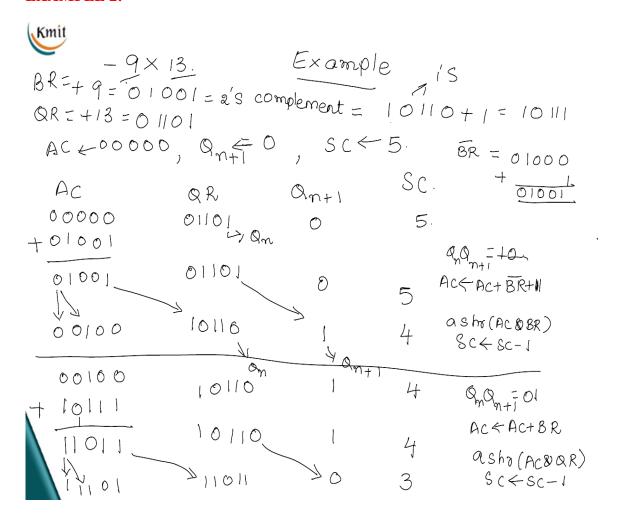
Example -9 X -13 = + 117

SC 101
101
100
011
010
001
000

**Product= AC & QR= 0001110101=117** 

Since the msb bit of product is 0, the sign of the product is positive= +117

#### **EXAMPLE 2:**



+ 01001 + 01001 AC	QR 11011 11011	9 <sub>m+1</sub>	SC. 3 0 3	$Q_nQ_{n+1}=10$ $ACC+AC+BR+1$ $aShv(AC&QR)$ $&CC-SC-1$			
700110	) 01101 an	(	2.				
00011	01101	I	2.	ashr(Aclar)			
00001	10110 Van	1	1	SCESC			
1000 + 10111 11000	10110	ļ	1				
	10110	ſ	ſ	$Q_n Q_{n+1} = A_1 + A_2 + A_2 = A_1 + A_2 = A_$			
	01011	0	0	ashr(A SCSC			
Ac	Q R		On-	+ 1	8 C		
AC	01011		0		$\bigcirc$	SC = 0	
1	=1, is a					end.	
product = 1110001011							
= 0001110100 +							
0001110101 > -117.							

Since the MSB bit of the product is 1, it indicates that the result is not the actual product. Perform the 2's complement of the product stored in AQ & QR to get the actual product.