# **GUNDALA MAHESH BABU**

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## **CAREER OBJECTIVE**

Seeking a challenging role to leverage my skills and drive organizational growth. Eager to learn, develop, and contribute to a thriving environment.

### **EDUCATION**

•	B.E in Electronics and Communication Engineering	9.47 CGPA
	Sri Venkateswara College of Engineering, Tirupati	2020 - 2024
•	Intermediate -MPC	9.61CGPA
	Sri Chaitanya junior college, Nellore	2018 - 2020
•	10 <sup>th</sup> class	9.7 GPA
	A.P.S.W.R Centre of Excellence, Pulivendula.	2017 - 2018

### **TECHNICAL SKILLS**

- Java

   Object Oriented Programming System (OOPS), Arrays, Strings, Packages, Exception Handling, Wrapper Classes, Collections, Object Class, File Handlings, Java Database Connectivity (JDBC).
- Spring Boot RESTful API's, HTTP Methods (Get, Post, Put, Delete), PA Repository.
- MySQL DDL, DML, DQL, DCL, TCL, Sub Queries, Joins, Views, Normalization, Constrains
- HTML, CSS, JavaScript (DOM Concept)

### **PROJECTS**

• Project Title: RESTful API with Spring Boot and Node.js Frontend

Project Area: Full-stack development, RESTful APIs, Spring Boot, Database interaction.

**Description**: This project demonstrates the creation of a RESTful API using Spring Boot and its integration with a Node.js frontend. The API exposes endpoints for performing various HTTP methods (GET, POST, PUT, DELETE) to interact with a database. Users can create, read, update, and delete data through these endpoints. The Node.js frontend is responsible for consuming the API and displaying the retrieved data in an attractive and user-friendly manner.

• **Project Title: Personal-Portfolio-Website** (Live Demo link).

Project Area: Using HTML, CSS, JavaScript

**Description**: HTML structures the content, CSS styles the look, and JavaScript adds interactivity, creating dynamic user experience. This project demonstrates expertise in the core web development stack - HTML, CSS, and JavaScript.

• Academic Project: To Design Full Adder Circuit by using CMOS and Pass Transistor Logic.

Project Area : VLSI Design

**Description**: Exploring CMOS and PTL full adders goes beyond the circuit itself. It reveals valuable design principles applicable to broader problems, empowering you to make informed choices and craft elegant solutions in future project

### **INTERNSHIP**

- Honed my data analysis and atmospheric modelling skills by analysing climate data and developing a
  mesoscale weather prediction model during an internship at the National Atmospheric Research
  Laboratory (NARL).
- I recently completed a one-month internship where I was awarded an Excellency Certificate for my contributions to the study of the latest advancements in microwave and laser-based scintillometers.

### **CERTIFICATES**

- I earned a **Java Full Stack Developer certification** after completing a 6-month program at Thops Tech Career Solution.
- Course completion on SQL and Relational Databases in CognitiveClass.ai powered by IBM Developer Skills Network.
- Course completion on Front End Development CSS in Great Learning.
- Course completion on Internet of Things from **NPTL**.
- I received many appreciation certificates in my college.

### **ACADEMIC ACHIEVEMENTS**

- Secure 1<sup>st</sup> position in 8<sup>th</sup> semester of B.E (Electronics and Communication Engineering) by scoring 92%
- Secure 1st position in both SSC and Intermediate by scoring 90% above.

### **STRENGTHS**

- Passionate about learning and growth
- Manage time efficiently
- In any role, I seek out new challenges to continuously improve myself and contribute significantly

### **HOBBIES**

- I enjoy spending time with my mom and helping her out with various tasks at home.
- I enjoy combining exercise and relaxation by tending to my garden in my free time.

### **DECLARATION**

I, GUNDALA MAHESH BABU do hereby confirm that the information given is true to the best of my knowledge.

**GUNDALA MAHESH BABU**