

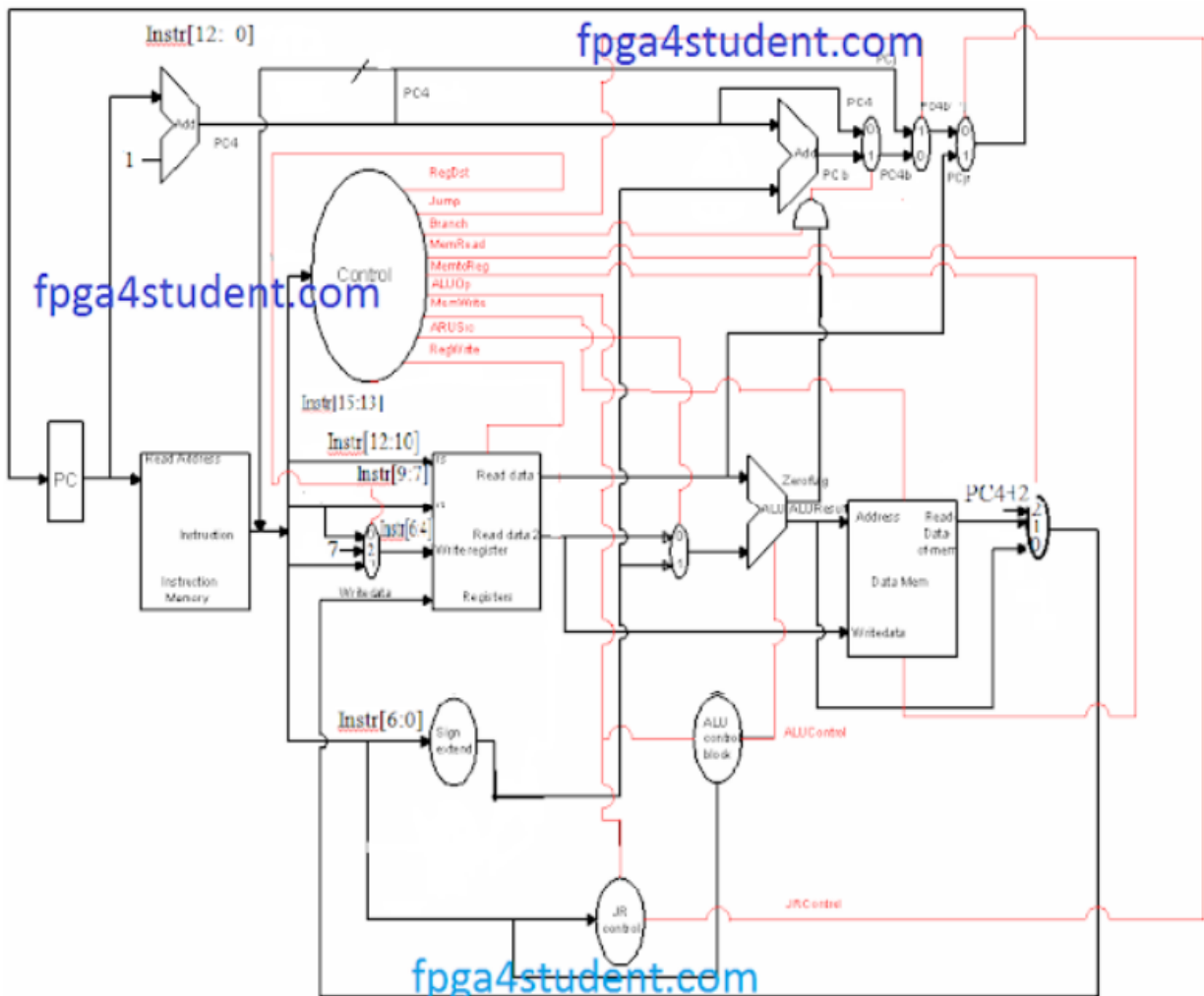
EE 705: VLSI Design Lab

Lab Task: Unit-23

Mahesh Shahaji Patil

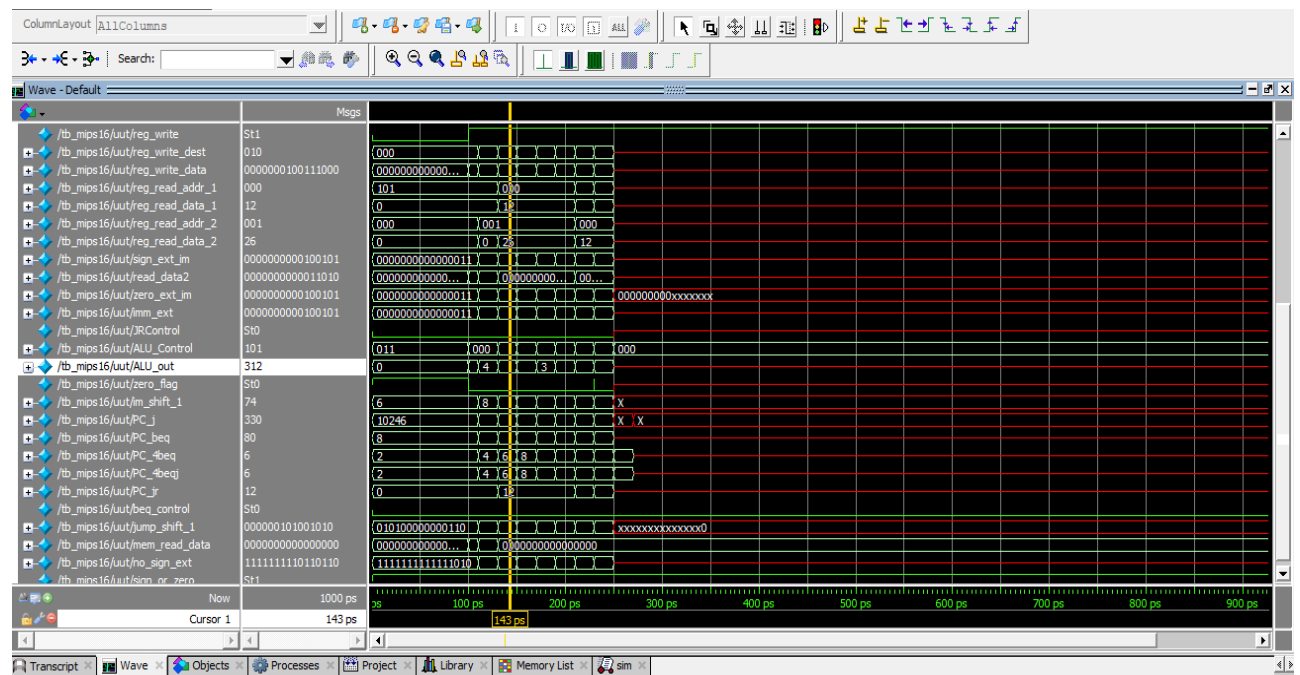
Roll No.20307R010

Data Path for the given 16-bit single cycle implementation of MIPS: -

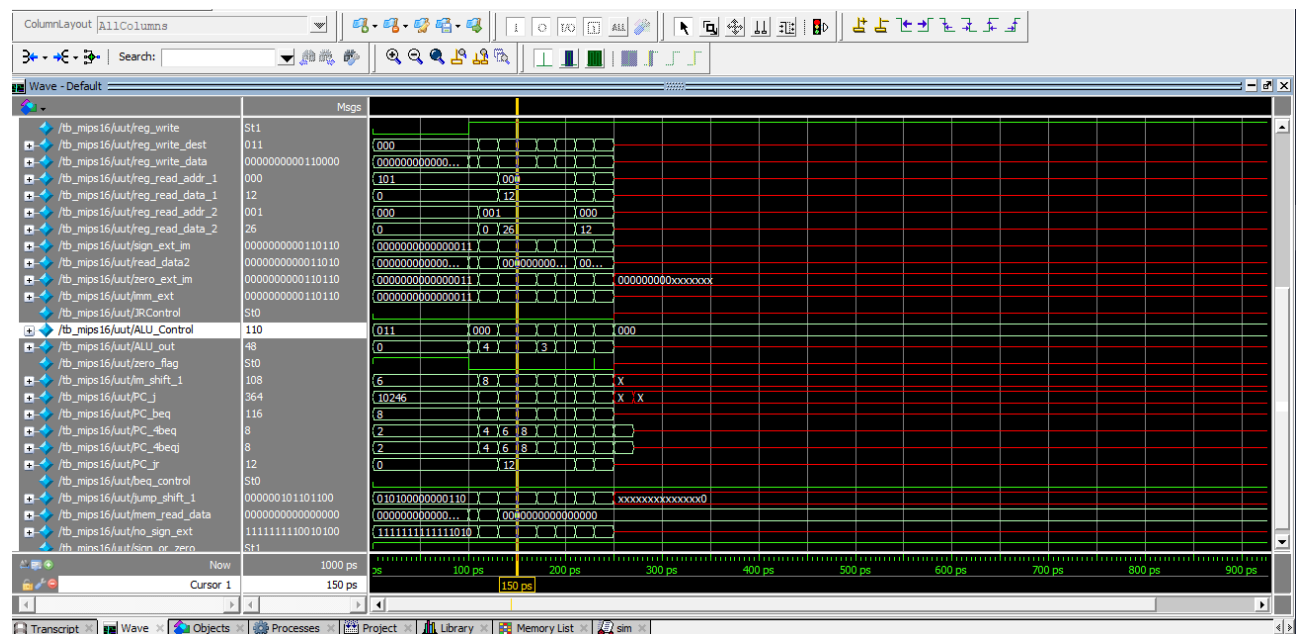


Simulation Results of some of the instructions: -

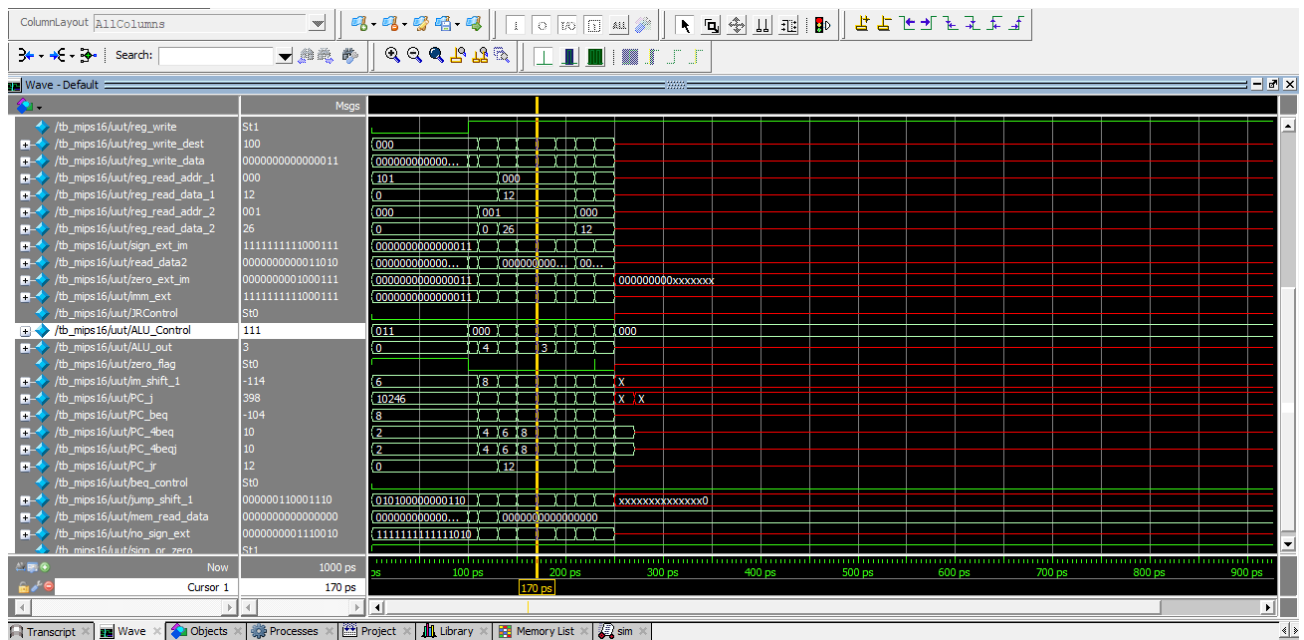
1) Multiplication: -



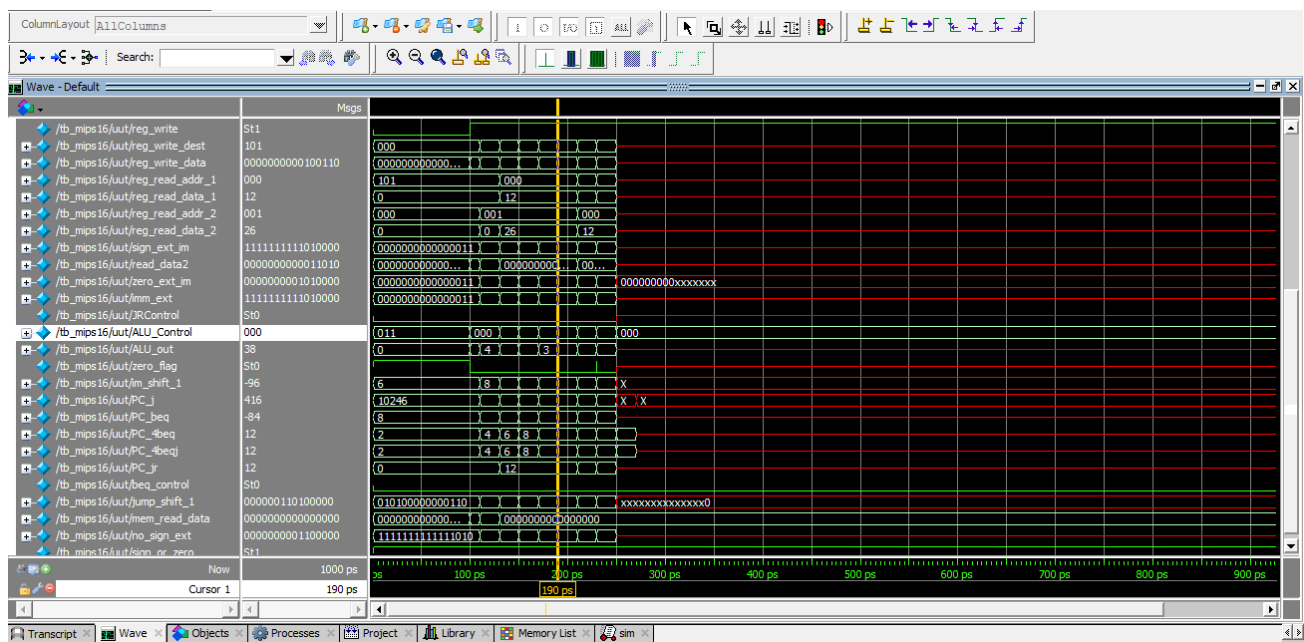
2) Shift Left Logical: -



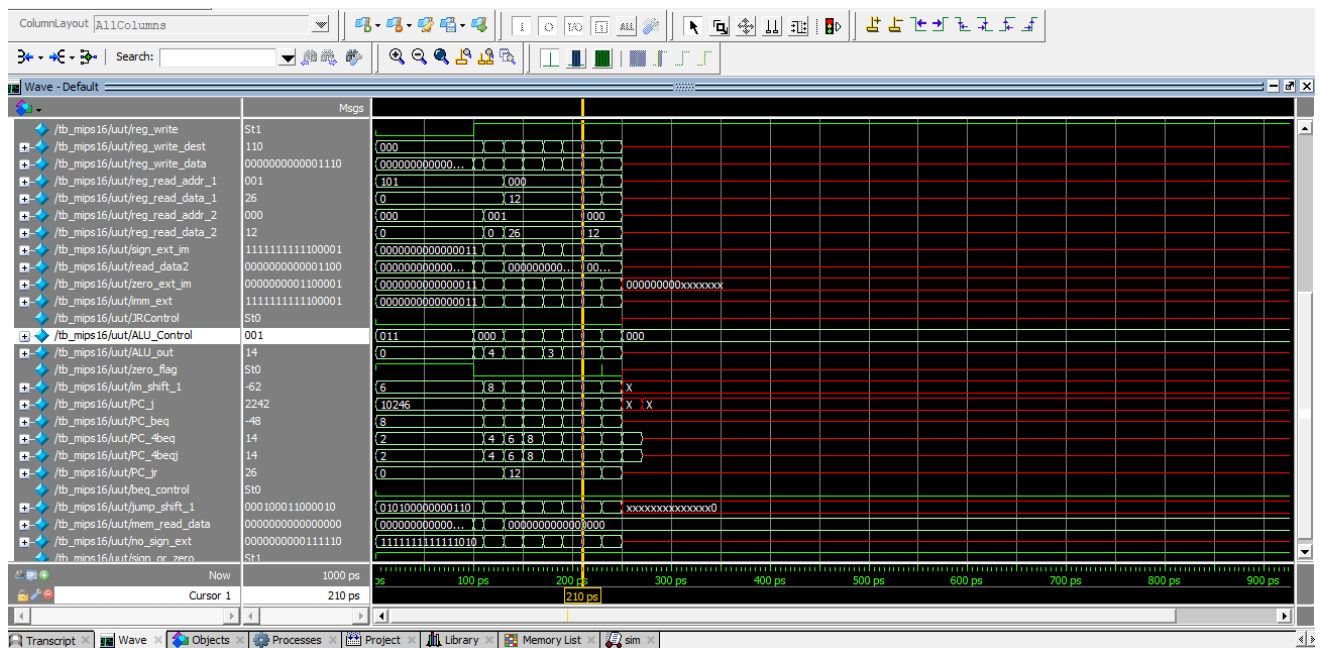
3] Shift Right Logical: -



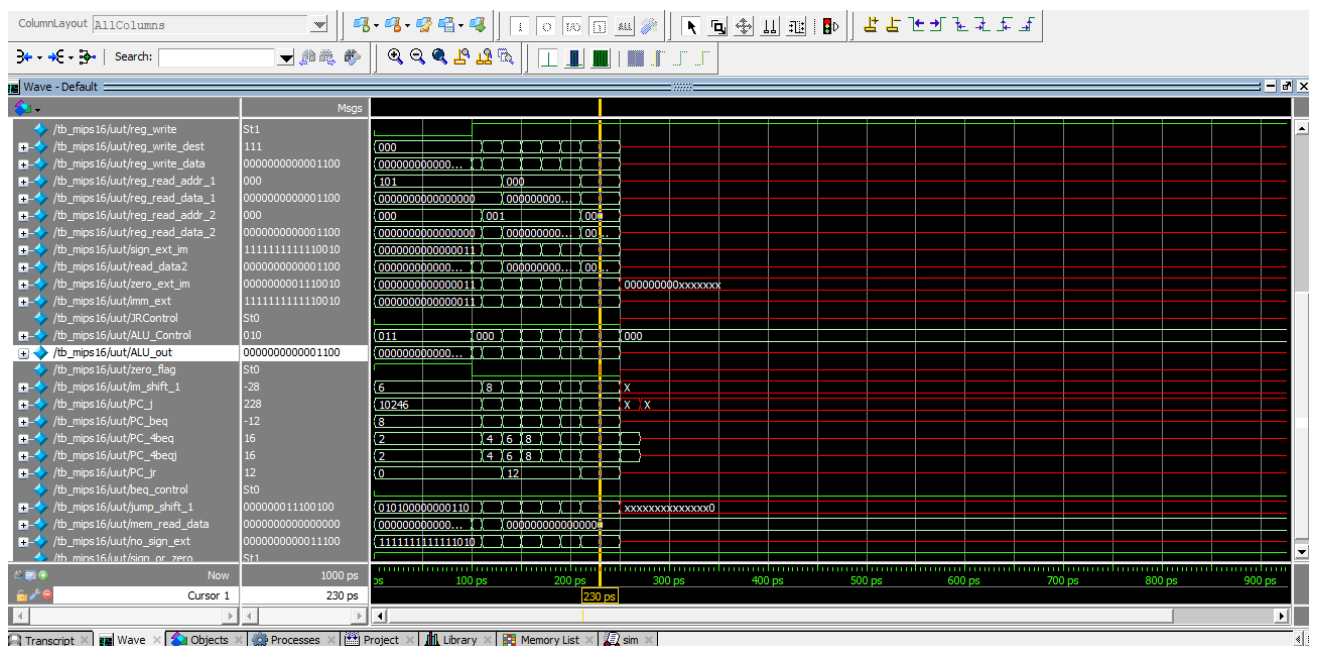
4] ADD Operation: -



5] SUB Operation: -



6] Logical AND Operation: -



URL for the Video Demonstration of the same: -

[EE705 Unit 23.mp4](#)