

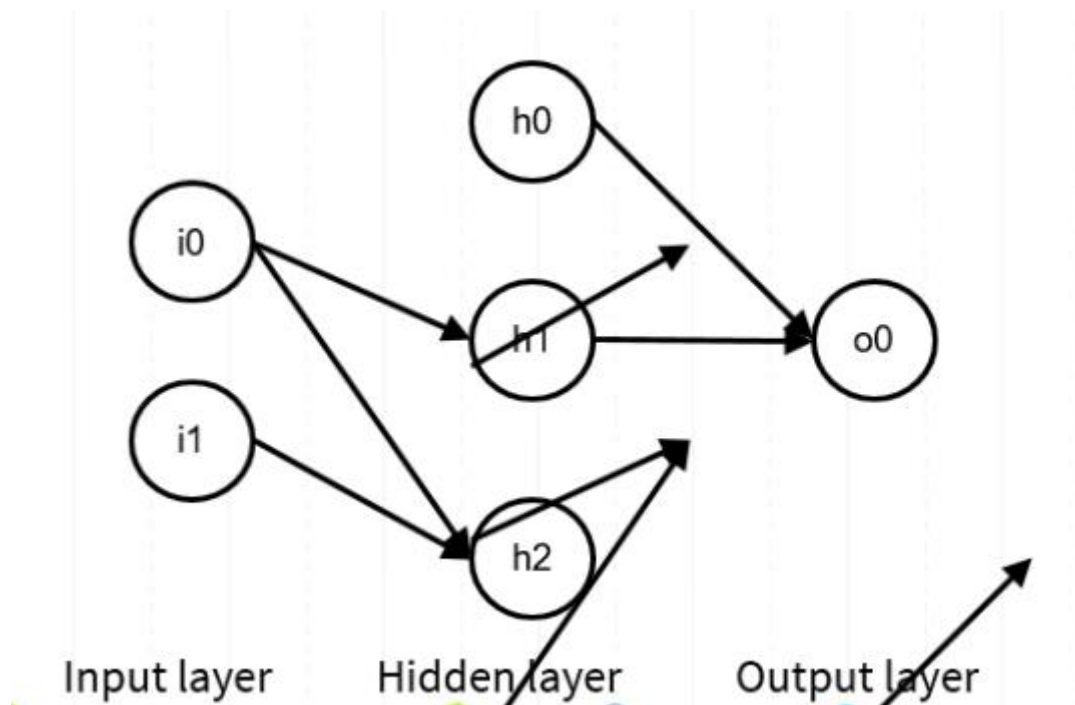
# EE 705: VLSI Design Lab

## Lab Task: Unit-22

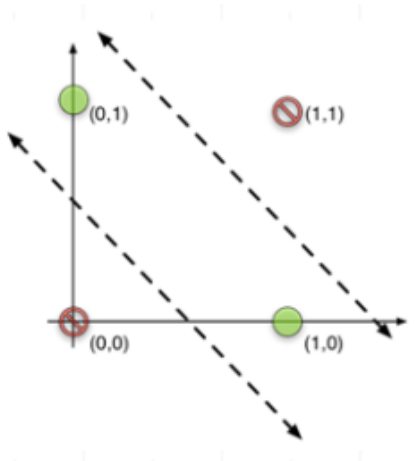
Mahesh Shahaji Patil

Roll No.20307R010

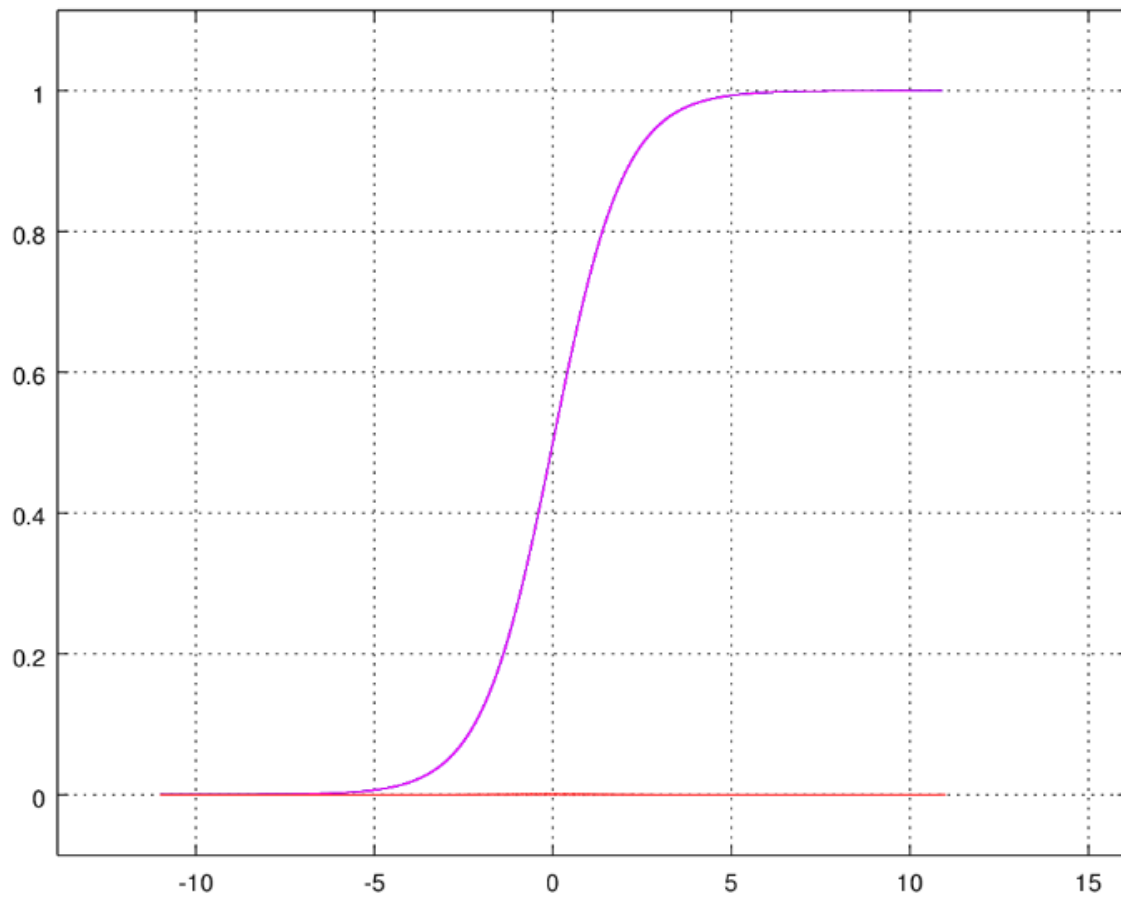
### 3-Layered ANN for XOR: -



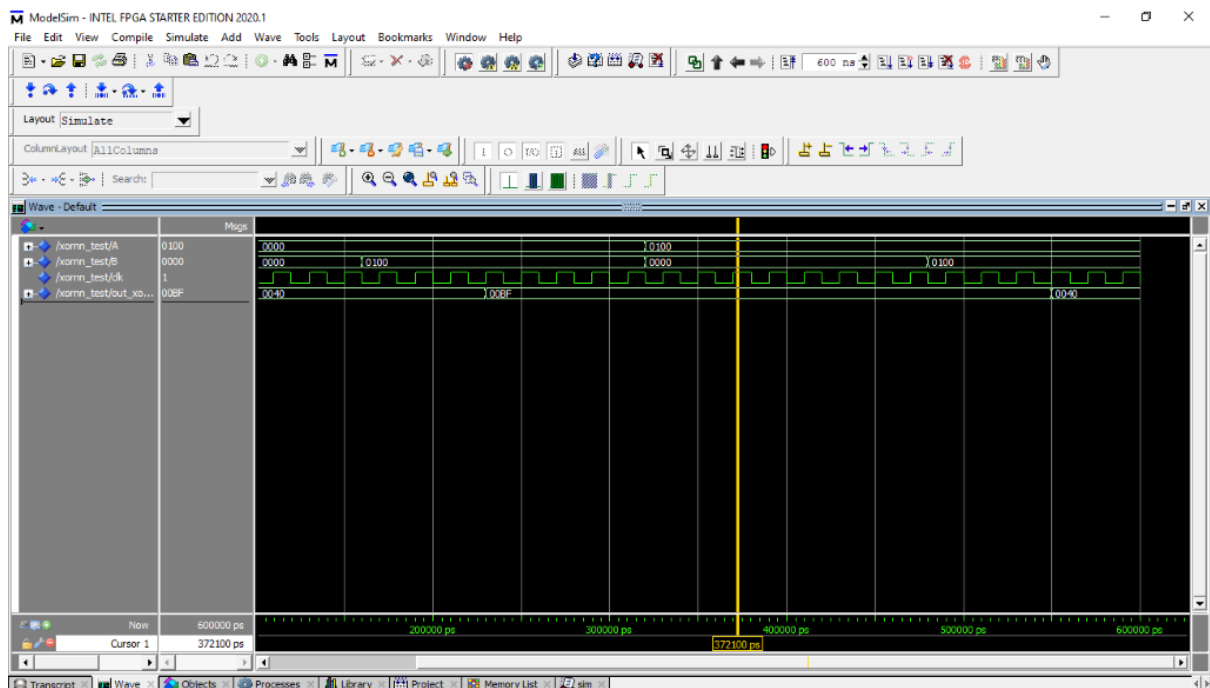
### Why Do we Need it?



**The use of following sigmoid function: -**



**Simulation Results: -**



**URL for the Video Demonstration of the same: -**

<https://iitbacin->

[my.sharepoint.com/:v:/g/personal/20307r010\\_iitb\\_ac\\_in/ESkLiGqQeR5Oumw\\_v5qmQQBEZ3aJrnKIn4e57Z6eKvVnA?e=Ee1TUC](https://iitbacin-my.sharepoint.com/:v:/g/personal/20307r010_iitb_ac_in/ESkLiGqQeR5Oumw_v5qmQQBEZ3aJrnKIn4e57Z6eKvVnA?e=Ee1TUC)