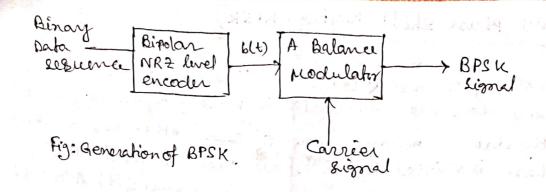
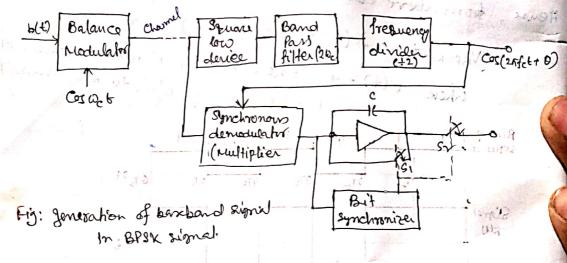
Binary Phase Shift heying (BPSK) In a coherent kinary PSK system, the Pair of signals \$1,(4) and \$2(4), has fixed amplitude, it has of survey phase when the data is kinary symbols I and when the data is birroug Symbol 'O' some other signal has the phase is shifted by 180° . Let assume that the, A is the reak amplitude of corrier signal S, (+) = ACO (27fct) the Power of the Rignal Ps ? In so that , A ? VaPs het we consider transmitted Ligned for Symbol 1 Q (t) = VaPs evs (2nfet) and for Symbol 10 \$2 (+) 2 \12 Ps cos (25fet+\$7) NOW WE KNOW that on (0+1) 2 - COSO (92(t) 2-V2Ps con(artet) Henre, from above educations we can define BPSK signal VBPSK (+), 2 b (+) \12Ps' cs (2nfc+) where b(t) = +1 when binary '1' is to be transmitted 2-1 when binay '01 is to be transmited. Binary 1 Syuma 3Th 1Tb Signel resident in someway in blt Carrier Generation of BPSK signal: In Bractice, The B. BPSK Signal may be generated by applying the carrier sissol to a balanced modulator and applying the bareband

Simil blt) as the modulating waveform.



Reception of BPSK

the form VBPSK = b(t) TPS CD (wet + 0), Dis' the Phane Shift depending upon the time delay from transmitter end to receiver end. The demodulation technique usually employed is called synchronous demodulation and hence a synchronous local cervier is necessary which is generated in synchronous eincuit.



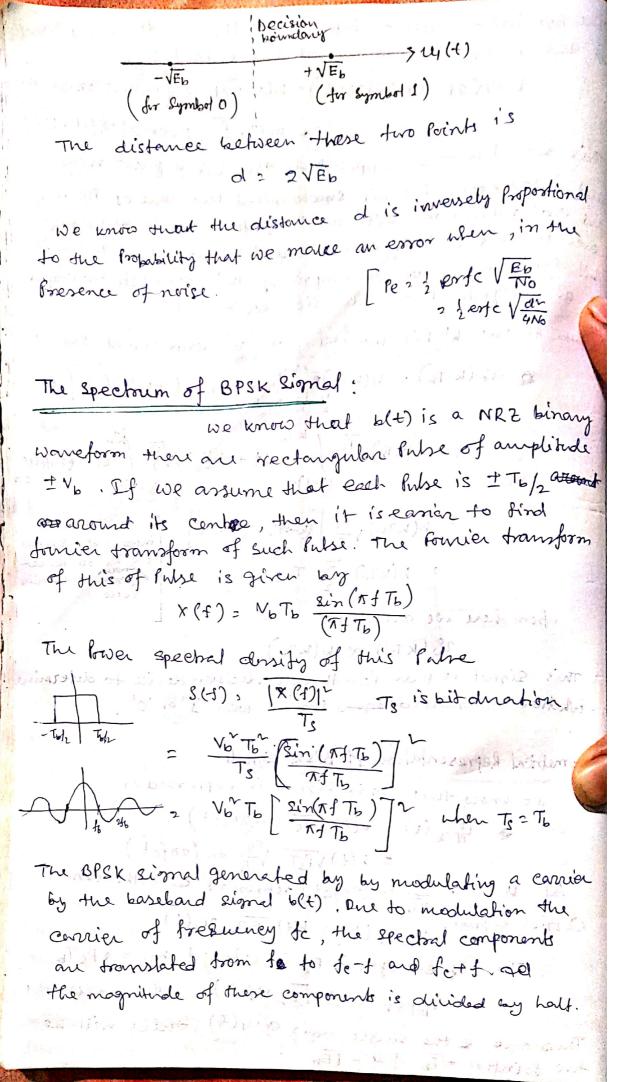
The received signal is squared to generate the signal

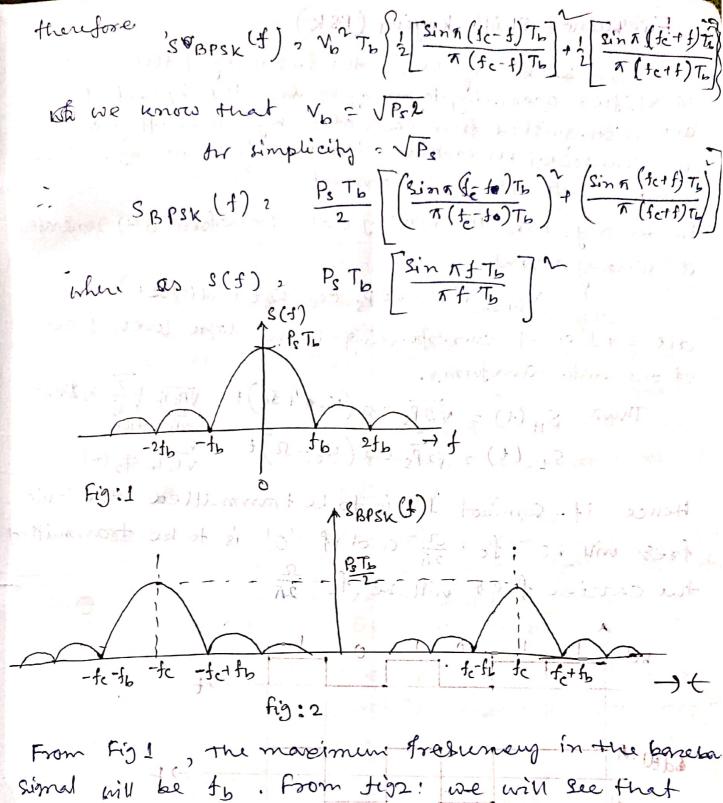
· cos (wet + 0) = { [1+ es 2 (wet + 0)]

Hore & represent De level. Then this simal allowed to law through a BPF whose forsto Paraband is contract of around 2tc. Bandpan filter remove the a De level and at the output, we get & cons(aw; t+0).

This signal having frequency equal to \$2 2tc. Hence it is Parsed through a frequency divider by two. Thus at the output of frequency divider, we get a carrier signal whose frequency be i; e, cos (25tct + P).

The synchronous demodulator is multiplied the input signal and the recovered corrier, at the output $b(t)\sqrt{2P_s}$ (with 0) = $b(t)\sqrt{2P_s}$ 1 [1+ co2(2\text{7}et+0)] b(t)√P3 [1+co2(27/ct+0)] This signal is then applied to the bit synchronizer and integrator. The bit symphonized takes care of starting and ending times of a but. The Integrator integrates the signal over one kit Period. At the end of bit duration To the hid synchronizer close so temprasily. This connects the output of an integrator to the decision device. Athe At the 4th bit interval, we can write output simal (27) To (KTb) = 6(KTb) (KTb) [1+ Cn 2 (27) ct+0)] at = $b(kT_b)\sqrt{\frac{P_s}{2}}\int kT_b$ $1 dt + b(kT_b)\sqrt{\frac{P_s}{2}}\int kT_b$ $\frac{1}{2}\int co2(2nfetig) dt$ (K-1)76 2000 20 (K-1)76 = b(KTb) $\sqrt{\frac{P_s}{2}}$ [(k-ce)Tb - (K-1)Tb] Since the integral of a sinucoid over a whole number of cycle has the value rest. from here we can write 28 (KTb) a b (KTb) This Signal is true Pars Abrough decision device to determine whether transmitted Eignal symbol was 'I' or 'o'. Geometrical Representation of BPSK signals We know that BPSK signal is expressed as 28psk (t) 2 b(t) \28s co(2016t) 2 b(t) VP,Tb V= en (21fet) ht u(4) 2 12 es (20/ct) represent an orthonormal Carrier signal. VBPSK(t) = b(t) \(\text{PsTb} \mu_1(t) \) The kint energy is Es is defined by = & Ps Tb. VBPSK = I VEb Uilt) when blt) = ±1 Thus one so the single axis of 4(t), there will be two Bointrat + TE 2 at - VEI Scanned with CamScanner





From Fig 1, the maximum fredumeny in the baselon signal will be to. From tigz: we will see that contre loke is centred at around contre carrier fret to and extends from to- to to fet to Thereforms bandwio of Bp BPSK Signal is 2 fb.

James 4278 Jordhannie

So for the governor BECK Sim

we have un who said has well -we