

analyzing main.o :

```
Sence79@DESKTOP-JJFQ8S2 MINGW32 ~/Desktop/lab2
$ make O_analyze
arm-none-eabi-objdump -h main.o

main.o:      file format elf32-littlearm

Sections:
Idx Name          Size      VMA           LMA           File off  Algn
  0 .text          000000b0  00000000  00000000  00000034  2**2
    CONTENTS, ALLOC, LOAD, RELOC, READONLY, CODE
  1 .data          00000004  00000000  00000000  000000e4  2**2
    CONTENTS, ALLOC, LOAD, DATA
  2 .bss           00000000  00000000  00000000  000000e8  2**0
    ALLOC
  3 .debug_info     00000128  00000000  00000000  000000e8  2**0
    CONTENTS, RELOC, READONLY, DEBUGGING
  4 .debug_abbrev   000000bf  00000000  00000000  00000210  2**0
    CONTENTS, READONLY, DEBUGGING
  5 .debug_loc      00000038  00000000  00000000  000002cf  2**0
    CONTENTS, READONLY, DEBUGGING
  6 .debug_aranges  00000020  00000000  00000000  00000307  2**0
    CONTENTS, RELOC, READONLY, DEBUGGING
  7 .debug_line     0000006a  00000000  00000000  00000327  2**0
    CONTENTS, RELOC, READONLY, DEBUGGING
  8 .debug_str      000000e9  00000000  00000000  00000391  2**0
    CONTENTS, READONLY, DEBUGGING
  9 .comment        00000012  00000000  00000000  0000047a  2**0
    CONTENTS, READONLY
10 .ARM.attributes 00000033  00000000  00000000  0000048c  2**0
    CONTENTS, READONLY
11 .debug_frame    0000002c  00000000  00000000  000004c0  2**2
    CONTENTS, RELOC, READONLY, DEBUGGING
```

```
Sence79@DESKTOP-JJFQ8S2 MINGW32 ~/Desktop/lab2
$ make nm
arm-none-eabi-nm main.o
00000000 T main
00000000 D R_ODR
```

analyzing StartupS.o :

```
Sections:
Idx Name          Size      VMA       LMA       File off  Algn
  0 .text          00000008  00000000  00000000  00000034  2**1
IN          CONTENTS, ALLOC, LOAD, RELOC, READONLY, CODE
  1 .data          00000000  00000000  00000000  0000003c  2**0
LF          CONTENTS, ALLOC, LOAD, DATA
  2 .bss           00000000  00000000  00000000  0000003c  2**0
1A          ALLOC
  3 .vectors       00000050  00000000  00000000  0000003c  2**0
D          CONTENTS, RELOC, READONLY
  4 .ARM.attributes 00000021  00000000  00000000  0000008c  2**0
F          CONTENTS, READONLY
ile  5 .debug_line    0000003c  00000000  00000000  000000ad  2**0
F          CONTENTS, RELOC, READONLY, DEBUGGING
  6 .debug_info    00000051  00000000  00000000  000000e9  2**0
F          CONTENTS, RELOC, READONLY, DEBUGGING
  7 .debug_abbrev  00000014  00000000  00000000  0000013a  2**0
F          CONTENTS, READONLY, DEBUGGING
  8 .debug_aranges 00000020  00000000  00000000  00000150  2**3
          CONTENTS, RELOC, READONLY, DEBUGGING
```

```
Sence79@DESKTOP-JJFQ8S2 MINGW32 ~/Desktop/lab2
$ make nm
arm-none-eabi-nm  StartupC.o
00000000 t _reset
          U main
00000006 t Vector_handler
```

analyzing Cortex-m3\_lab2.elf:

```
Sence79@DESKTOP-JJFQ8S2 MINGW32 ~/Desktop/lab2
$ make O_analyze
arm-none-eabi-objdump -h Cortex-m3_lab3.elf

Cortex-m3_lab3.elf:      file format elf32-littlearm

Sections:
Idx Name          Size      VMA           LMA           File off  Algn
  0 .text          00000108  08000000  08000000  00008000  2**2
    CONTENTS, ALLOC, LOAD, READONLY, CODE
  1 .data           00000004  08000108  08000108  00008108  2**2
    CONTENTS, ALLOC, LOAD, DATA
  2 .debug_info     00000179  00000000  00000000  0000810c  2**0
    CONTENTS, READONLY, DEBUGGING
  3 .debug_abbrev    000000d3  00000000  00000000  00008285  2**0
    CONTENTS, READONLY, DEBUGGING
  4 .debug_loc       00000038  00000000  00000000  00008358  2**0
    CONTENTS, READONLY, DEBUGGING
  5 .debug_aranges   00000040  00000000  00000000  00008390  2**3
    CONTENTS, READONLY, DEBUGGING
  6 .debug_line      000000a6  00000000  00000000  000083d0  2**0
    CONTENTS, READONLY, DEBUGGING
  7 .debug_str       000000c2  00000000  00000000  00008476  2**0
    CONTENTS, READONLY, DEBUGGING
  8 .comment         00000011  00000000  00000000  00008538  2**0
    CONTENTS, READONLY
  9 .ARM.attributes  00000031  00000000  00000000  00008549  2**0
    CONTENTS, READONLY
 10 .debug_frame     0000002c  00000000  00000000  0000857c  2**2
    CONTENTS, READONLY, DEBUGGING
```

```
Sence79@DESKTOP-JJFQ8S2 MINGW32 ~/Desktop/lab2
$ make nm
arm-none-eabi-nm Cortex-m3_lab3.elf
                 U _reset
08000100 t _reset
08000050 T main
08000108 D R_ODR
08000106 t Vector_handler
```